COMPLEMENTARY SILICON PLASTIC **POWER TRANSISTORS**

... designed for use in general-purpose amplifier and switching applications.

FEATURES:

* Collector-Emitter Sustaining Voltage-

V_{CEO(SUS)} = 40 V (Min) -2N6486, 2N6489 = 60 V (Min) -2N6487, 2N6490 = 80 V (Min) -2N6488, 2N6491

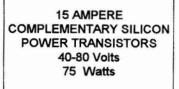
* DC Current Gain Specified to 15 Ampers

hFE = 20-150 @ I_c =5.0 A

MAXIMUM RATINGS

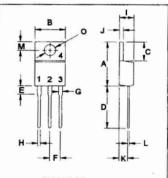
Characteristic	Symbol	2N6486 2N6489	2N6487 2N6490	2N6488 2N6491	Unit
Collector-Emitter Voltage	V _{CEO}	40	60	80	v
Collector-Base Voltage	V _{CBO}	50	70	90	v
Emitter-Base Voltage	V _{EBO}	5.0			v
Collector Current - Continuous	l _c	15			А
Base Current	l _B	5.0			А
Total Power Dissipation@T _c = 25°C Derate above 25°C	PD	75 0.6			w w/ºc
Operating and Storage Junction Temperature Range	T _j ,T _{STG}	-65 to +150			°C

NPN PNP 2N6486 2N6489 2N6487 2N6490 2N6488 2N6491



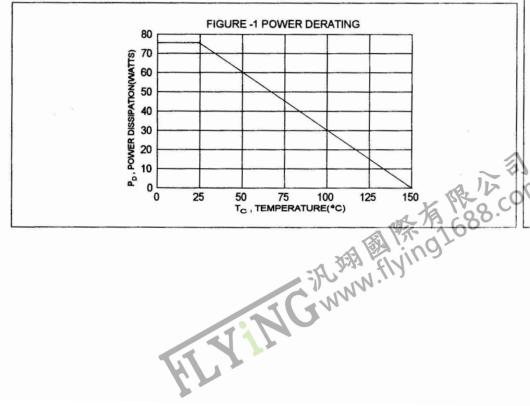


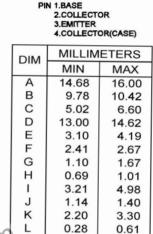




THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance Junction to Case	Rejc	1.67	°C/W





2.48

3.50

3.00

4.00

Μ

0

2N6486, 2N6487, 2N6488 NPN / 2N6489, 2N6490, 2N6491 PNP

ELECTRICAL CHARACTERISTICS ($T_c = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit	
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OFF CHARACTERISTICS

Collector - Emitter Sustaining Voltage (1) (I _C = 100 mA, I _B = 0)	2N6486, 2N6489 2N6487, 2N6490 2N6488, 2N6491	V _{CEO(SUS)}	40 60 80		v
Collector Cutoff Current $(V_{cE} = 20 V, I_{B} = 0)$ $(V_{cE} = 30 V, I_{B} = 0)$ $(V_{cE} = 40 V, I_{B} = 0)$	2N6486, 2N6489 2N6487, 2N6490 2N6488, 2N6491	I _{CEO}		1.0 1.0 1.0	mA
	2N6486, 2N6489 2N6487, 2N6490 2N6488, 2N6491 2N6486, 2N6489 2N6487, 2N6490 2N6488, 2N6491	ICEX		0.5 0.5 0.5 5.0 5.0 5.0	mA
Emitter Cutoff Current (V _{EB} = 5.0 V,I _C = 0)	-1	I _{EBO}		1.0	mA

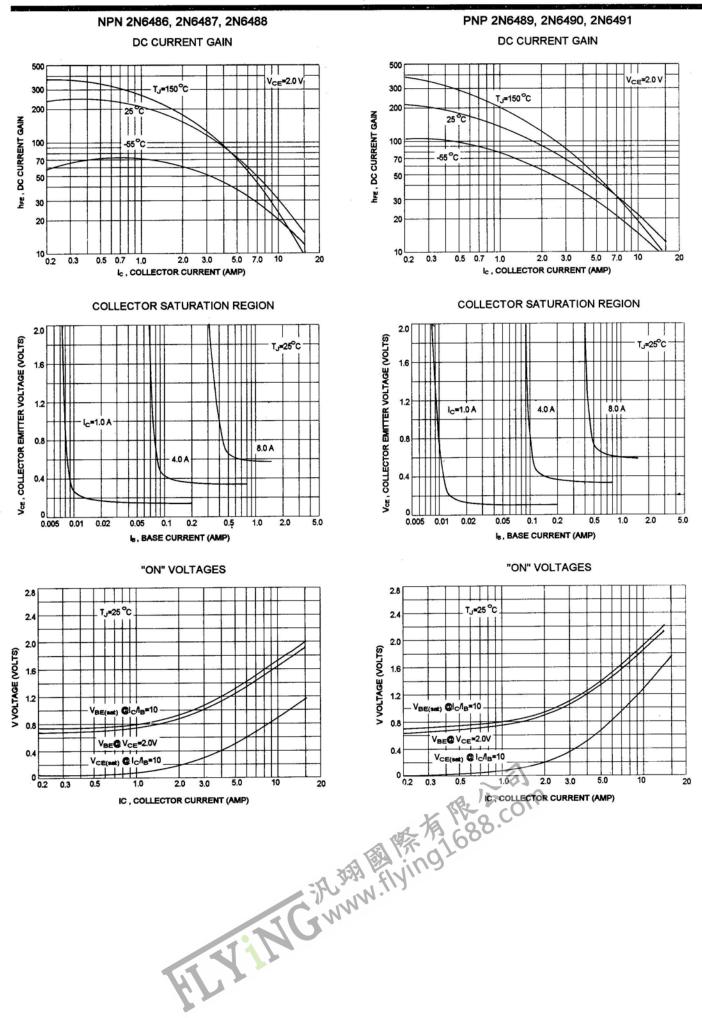
ON CHARACTERISTICS (1)

DC Current Gain (I _c = 5.0 A, V _{cE} = 4.0 V) (I _c = 15 A, V _{cE} = 4.0 V)	hFE	20 5.0	150	
Collector-Emitter Saturation Voltage ($I_c = 5.0 \text{ A}, I_B = 0.5 \text{ A}$) ($I_c = 15 \text{ A}, I_B = 5.0 \text{ A}$)	V _{CE(sat)}		1.3 3.5	v
Base-Emitter On Voltage (I _c = 5.0 A, V _{cE} = 4.0 V) (I _c = 15 A, V _{cE} = 4.0 V)	V _{BE(on)}		1.3 3.5	V

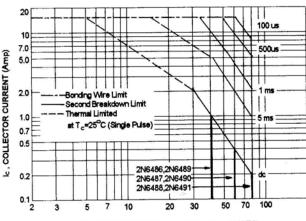
DYNAMIC CHARACTERISTICS

Current-Gain-Bandwidth Product (2) (I _c = 1.0 A, V _{CE} = 4.0 V, f = 1.0 MHz)	f _T	5.0	MHz
Small-Signal Current Gain (I _c = 1.0 A, V _{cE} =4.0 V, f = 1.0 KHz)	h _{fe}	15	

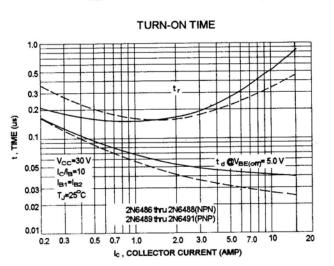
(1) Pulse Test: Pulse width = $300 \,\mu$ s, Duty Cycle $\leq 2.0\%$ (2) $f_T = |h_{f_0}| \cdot f_{test}$



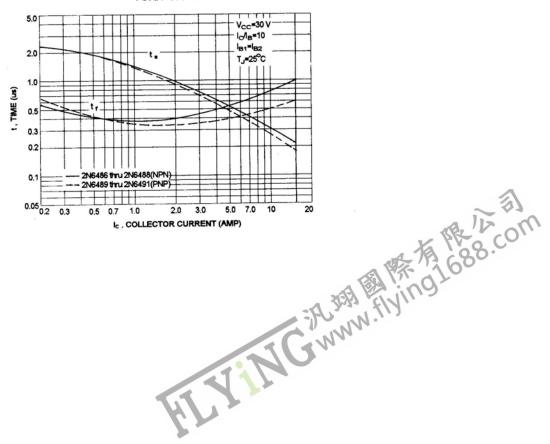
ACTIVE-REGION SAFE OPERATING AREA (SOA)



VCE, COLLECTOR EMITTER VOLTAGE (VOLTS)



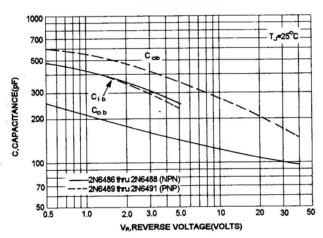




There are two limitation on the power handling ability of a transistor:average junction temperature and second breakdown safe operating area curves indicate $I_{C}-V_{CE}$ limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than curves indicate.

The data of SOA curve is base on $T_{J(PK)}$ =150 °C;T_c is variable depending on conditions. second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(PK)}$ <150°C,At high case temperatures, thermal limitation will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

CAPACITANCES





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