



NPN switching transistor 5 August 2020

1. General description

NPN switching transistor in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- High current (max. 600 mA)
- Low voltage (max. 40 V)
- AEC-Q101 qualified

3. Applications

Switching and linear amplification

4. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	40	V
I _C	collector current			-	-	600	mA
h _{FE}	DC current gain	V _{CE} = 10 V; I _C = 150 mA; T _j = 25 °C	[1]	100	-	300	
		V _{CE} = 10 V; I _C = 500 mA; T _j = 25 °C	[1]	40	-	-	

[1] Pulse test: $t_p \le 300 \ \mu s; \delta \le 0.02$

5. Pinning information

Table 2. Pinning information							
Pin	Symbol	Description	Simplified outline	Graphic symbol			
1	В	base	3	C			
2	E	emitter		в			
3	С	collector		E sym021			
			SOT23				



6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PMBT2222A	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

7. Marking

Table 4. Marking codes	
Type number	Marking code[1]
PMBT2222A	%1P

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

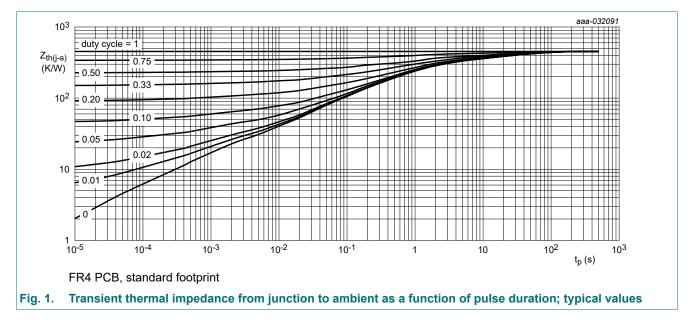
Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	75	V
V _{CEO}	collector-emitter voltage	open base		-	40	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
I _C	collector current			-	600	mA
I _{CM}	peak collector current			-	800	mA
I _{BM}	peak base current			-	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCM), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Therma	al characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	500	-	K/W

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



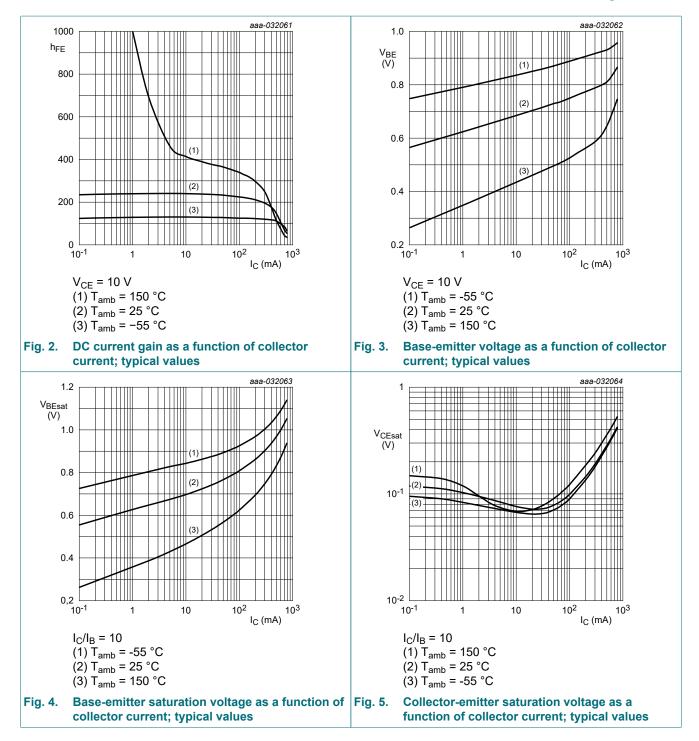
10. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = 60 V; I _E = 0 A; T _j = 25 °C		-	-	10	nA
	current	V _{CB} = 60 V; I _E = 0 A; T _j = 125 °C		-	-	10	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _j = 25 °C		-	-	10	nA
h _{FE}	DC current gain	V _{CE} = 10 V; I _C = 0.1 mA; T _j = 25 °C		35	-	-	
		V _{CE} = 10 V; I _C = 1 mA; T _j = 25 °C		50	-	-	
		V _{CE} = 10 V; I _C = 10 mA; T _j = 25 °C		75	-	-	
		V _{CE} = 10 V; I _C = 10 mA; T _{amb} = -55 °C		35	-	-	
		V _{CE} = 10 V; I _C = 150 mA; T _j = 25 °C	[1]	100	-	300	
		V _{CE} = 1 V; I _C = 150 mA; T _j = 25 °C	[1]	50	-	-	
		V _{CE} = 10 V; I _C = 500 mA; T _j = 25 °C	[1]	40	-	-	
V _{CEsat}	collector-emitter	I _C = 500 mA; I _B = 15 mA; T _j = 25 °C	[1]	-	-	300	mV
	saturation voltage	I _C = 500 mA; I _B = 50 mA; T _j = 25 °C	[1]	-	-	1	V
V _{BEsat}	base-emitter saturation	I _C = 150 mA; I _B = 15 mA; T _j = 25 °C	[1]	0.6	-	1.2	V
	voltage	I _C = 500 mA; I _B = 50 mA; T _j = 25 °C	[1]	-	-	2	V
t _d	delay time	I _C = 150 mA; I _{Bon} = 15 mA;		-	-	15	ns
t _r	rise time	I _{Boff} = -15 mA; V _{CC} = 10 V; T _j = 25 °C		-	-	20	ns
t _{on}	turn-on time			-	-	35	ns
t _s	storage time			-	-	200	ns
t _f	fall time	I _C = 150 mA; I _{Bon} = 15 mA; I _{Boff} = -15 mA; T _j = 25 °C		-	-	60	ns
t _{off}	turn-off time	I_{C} = 150 mA; I_{Bon} = 15 mA; I_{Boff} = 1 mA; T_{j} = 25 °C		-	-	250	ns
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _j = 25 °C		-	-	8	pF
C _e	emitter capacitance	V _{EB} = 500 mV; I _C = 0 A; i _c = 0 A; f = 1 MHz; T _j = 25 °C		-	-	25	pF
f _T	transition frequency	V _{CE} = 20 V; I _C = 20 mA; f = 100 MHz; T _j = 25 °C		300	-	-	MHz
NF	noise figure	V _{CE} = 5 V; I _C = 100 μA; R _S = 1 kΩ; f = 1 kHz; T _i = 25 °C		-	-	4	dB

[1] Pulse test: $t_p \le 300 \ \mu s; \delta \le 0.02$

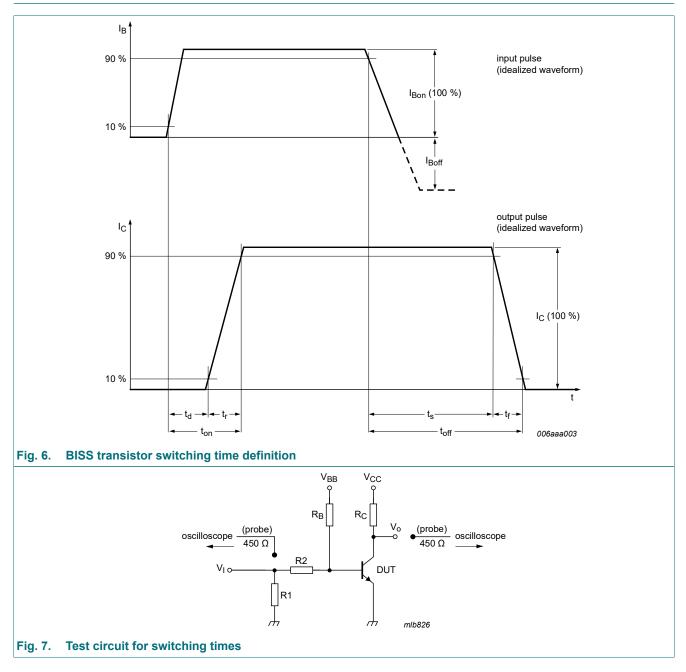
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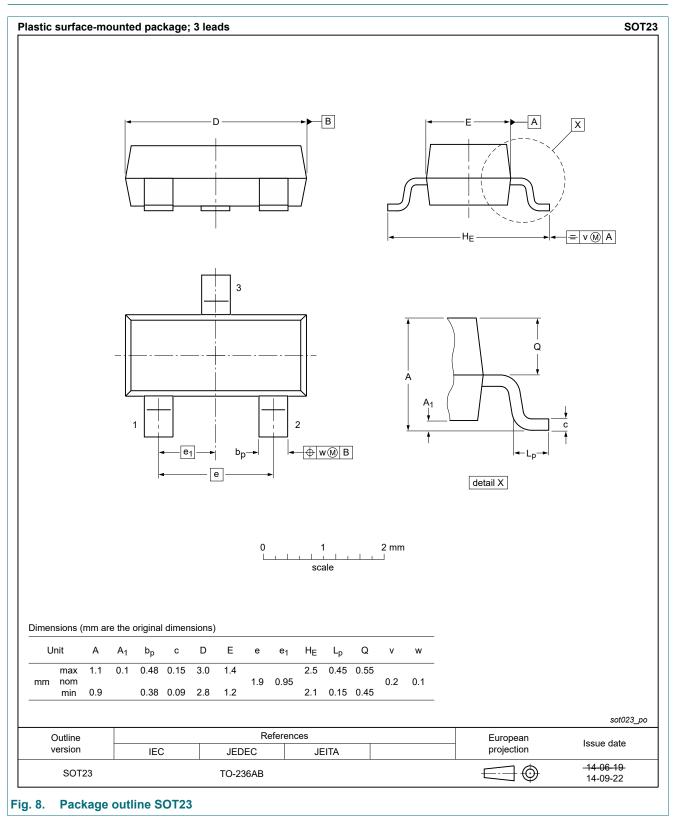
11. Test information



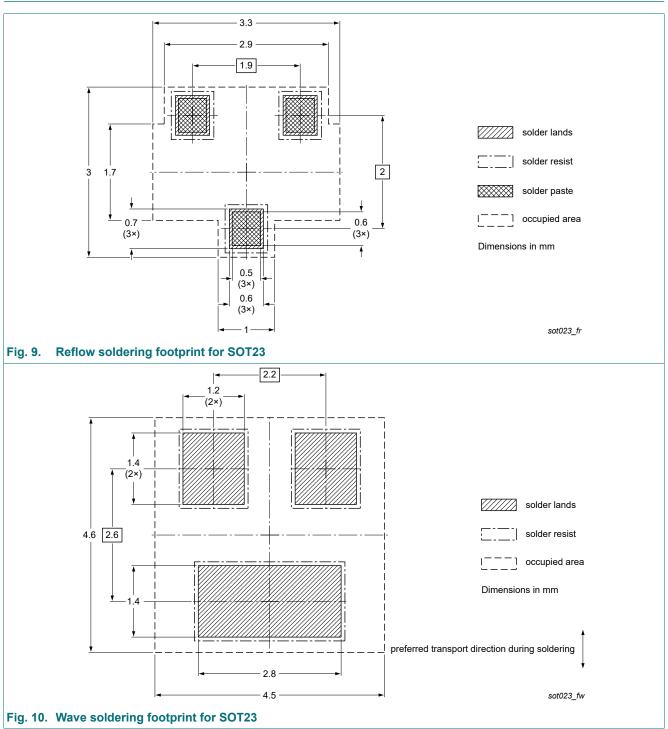
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMBT2222A v.7	20200805	Product data sheet	-	PMBT2222_2222A v.6
Modifications:	Thermal charac	ing" added		jed from T_{sp} to T_j in table 7
PMBT2222_2222A v.6	20101112	Product data sheet	-	PMBT2222_2222A v.5
PMBT2222_2222A v.5	20040122	Product specification	-	PMBT2222_2222A v.4
PMBT2222_2222A v.4	19990427	Product specification	-	PMBT2222 v.3
PMBT2222 v.3	19970909	Product specification	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

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