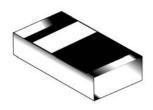


Switching Diode CD4148WN



FEATURES

- Silicon epitaxial planar diode
- SMD chip pattern, available in various dimension included 0805
- Leadfree and RoHS compliance components
- For AC switching input as rectified circuit and high reverse voltage location

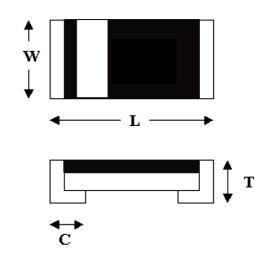
MECHANICAL CHARACTERISTICS

■ Size: 1206

Weight: approx. 10mgMarking: Cathode terminal

DIMENSIONS

| 1206 |
|----------|
| 3.2±0.2 |
| 1.5±0.2 |
| 0.85±0.1 |
| 0.55±0.2 |
| |



THERMAL CHARACTERISTICS¹⁾

| Parameter at T _{amb} =25°C ¹⁾ | Symbol | Value | Unit |
|---|------------------|------------|-------|
| Forward Power Dissipation | D | 400 | mW |
| Power derating above 25°C | P _{tot} | 3.2 | mW/°C |
| Junction Temperature | T _i | 150 | °C |
| Thermal Resistance Junction to Ambient air | R _{eJA} | 375 | °C/W |
| Operating& Storage Temperature range | T_{stg} | -55 to 150 | °C |

¹⁾ Valid provided that electrodes are kept at ambient temperature.



MAXIMUM RATING1)

| Parameter at T _{amb} =25°C ¹⁾ | Symbol | Value | Unit |
|---|--------------------|-------|------|
| Repetitive Peak Reverse Voltage | V_{RRM} | 100 | V |
| Average rectified current sin half wave rectification with resistive load | $I_{F(AV)}$ | 150 | mA |
| Repetitive Peak Forward Current at T _{amb} =25°C | ${ m I}_{\sf FRM}$ | 300 | mA |
| Non-Repetitive Surge Forward Current at t<1s and T _j =25°C | I _{FSM} | 500 | mA |
| at $t \le 8.3$ ms and $T_j = 25$ °C | | 1000 | mA |

¹⁾ Valid provided that electrodes are kept at ambient temperature.

ELECTRICAL CHARACTERISTICS¹⁾

| Parameter at T _{amb} =25°C ¹⁾ | Symbol | Value | Unit |
|--|-----------------|----------------------|------|
| Forward Voltage at I _F =10mA | V | 1.0 _{MAX} | V |
| at I _F =100mA | V_F | 1.25 _{MAX} | V |
| Leakage Current at V _R =20V | т | 0.025 _{MAX} | uA |
| Leakage Current at V _R =80V | I_{R} | 0.5 _{MAX} | uA |
| Capacitance at V _R =0V, f=1MHz | C_{tot} | 4 _{MAX} | pF |
| Reverse Recovery Time at $I_F = I_R = 10$ mA, $R_L = 100 \Omega$ | t _{rr} | 4 _{MAX} | ns |

¹⁾ Valid provided that electrodes are kept at ambient temperature.

TYPICAL CHARACTERISTICS

Figure 1. Forward Characteristic

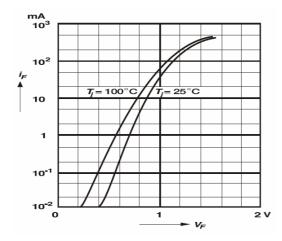


Figure 2. Power De-rating

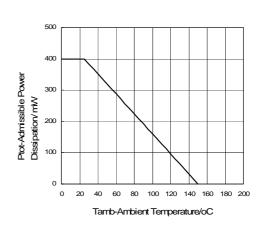




Figure 3. Forward Current De-rating

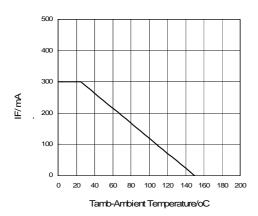
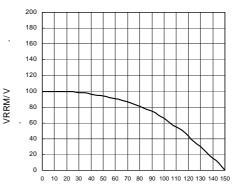


Figure 4. Reverse Voltage De-rating



Tamb-Ambient Temperature/oC

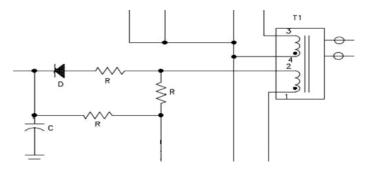
TEST CHARACTERISTICS

| Test Item | Test Condition | Requirement |
|---------------------------------|--|--|
| Solderability | Sn bath at 245±5°C for 2±0.5s | >95% area tin covered |
| Resistance to Soldering Heat | Sn bath at 260±5°C for 10±2s | V _F ,V _R & I _R within spec; no mechanical damage |
| Humidity Steady State | At 85°C 85%RH for 168hrs | V _F ,V _R & I _R within spec |
| Continue Forward Operating Life | At 25°C $I_F = 1.1I_F$ for 1000hrs | V _F ,V _R & I _R within spec |
| Thermal Shock | -55 ±5°C/5min to 150±5°C/5min for 10cycles | V _F ,V _R & I _R within spec |
| Bending Strength | Bending up to 2mm for 1cycle | V _F ,V _R & I _R within spec; no mechanical damage |



APPLICATIONS

- Function: Fast switching, suit for AC switching input as rectified circuit and high reverse voltage location application
- Typical Application circuit:



- Typical Product field: Power supply, adapter & inverter
- Soldering Condition:

Soldering Condition & Caution

■ Recommended Soldering Condition (Refer to IPC/JEDEC J-STD-020D 4-1&5.2)

| Recommended Profile Condition | Sn-Pb Soldering | Leadfree Soldering | Wave Soldering |
|-------------------------------------|-----------------|-----------------------|----------------|
| Ramp-up rate (from pre-heat stage) | <3°C/s | <3°C/s | △T<150°C |
| Dro hoot Tomporature & Time | 100-150 °C | 150-200 °C | 100-150 °C |
| Pre-heat Temperature & Time | 60-120s | 60-120s | 60-120s |
| Coldoring Tomporature 9. Time | 183 °C | 217 °C | 260±5°C |
| Soldering Temperature & Time | 60-150s | 60-150s | 5±2s |
| Poak Tomporaturo | 230±5°C | 245±5°C | 260±5°C |
| Peak Temperature | <260°C | <260°C | 200±3 C |
| Time within 5°C of peak temperature | 10-20s | 20-30s | - |
| Ramp-down rate | <6°C/s | <6°C/s | <6°C/s |
| Time 25°C to peak temperature | <6min | <8min | - |

Manual Soldering: Approx. 350°C for 3s, avoid solder iron tip direct touch the components body



Recommended Soldering Profile

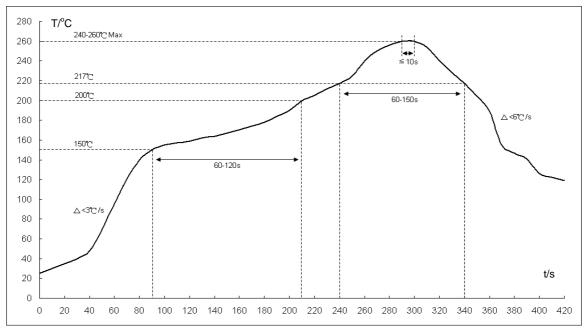


Fig1: Reflow soldering profile for lead-free solder (SnAgCu)

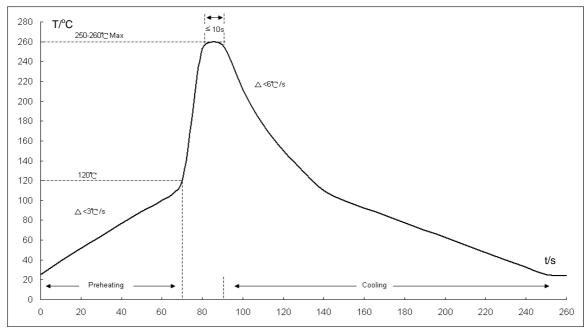
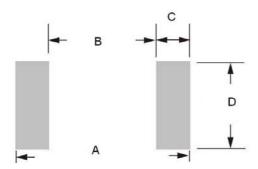


Fig2: Wave soldering profile

- *1. The recommended profiles are referring to IPC/JEDEC J-STD-020D & IEC-60068-2-58
- *2. Chip diodes are able to stand maximum soldering temperature up to 260° C max for 10s, and the soldering cycles with max 3 times, referring to IEC-60068-2-58



■ Recommended Soldering Footprint:



| ■ Reflow/Wave Soldering | | | | | |
|-------------------------|---------------|-----|---------|---------|--|
| Product Size | Dimension/ mm | | | | |
| Product Size | Α | В | С | D | |
| 1206 | 3.8-4.6 | 2.2 | 0.8-1.2 | 1.5-1.7 | |

■ Storage Condition: Product termination solderability can degrade due to high temperature and humidity or chemical environment. Storage condition must be in an ambient temperature of <40°C and ambient humidity of <75%RH, and free from chemical.

ENVIRONMENTAL CHARACTERISTICS

| | Haz | ardous | Substan | ce or E | lement/¡ | opm |
|---------|------------------------|--------|---------|------------------|----------|-------|
| Product | Pb | Cd | Hg | Cr ⁶⁺ | PBB | PBDE |
| | <1000 | <100 | <1000 | <1000 | <1000 | <1000 |
| | | | | | | |
| | Halogen Substance/ ppm | | | | | |
| Product | F | Cl | Е | Sr . | I | Total |
| | <900 | <900 |) <9 | 00 | <900 | <1500 |

PACKING METHOD

| Product | Quality/Reel | Reel Size | Tape |
|---------|--------------|-----------|-------|
| Troduct | 5,000pcs | 7" | Paper |

DISCLAIMERS

These products are not designed for use in applications where any failure or malfunction may resulted in personal injury, death or severe property or environmental damage such as medical, military, aircraft, space or life support equipments.