# **Preliminary** T8341AD

Low-Dropout, Constant-Current
White LED with Dimming Control and low Voltage Detec

#### **FEATURES**

- Low Voltage Detection and Indication:
   Detect Voltage Level Options by External Resistor.
- Up to 1.0A LED Bias Current.
- External Resistor to set Output Current.
- Simple LED Dimming Control.
- Build-in Morse Code S.O.S Signal and Single Flash Loop Flash Function.
- Low Dropout Voltage.
- 2.5V to 6V Supply Voltage Range.
- SOP-8 lead-free Package.
- ESD Human Body Mode Over 4KV.
- Thermal Protection 160 °C.
- Embedded OTP ROM for Function Options.

#### GENERAL DESCRIPTION

T8341AD provides the low-dropout bias supply and the high performance alternative solution for the white LEDs application. The build-in Low Voltage Detection and Indication function can provide the internal LED dimming. Morse Code S.O.S Signal and Single Flash Loop Flash Function are supported with the significantly lower dropout voltage. A embedded OTP ROM can provide a flexibility of function selection. The T8341AD is available in SOP-8 lead-free package.

# **Applications**

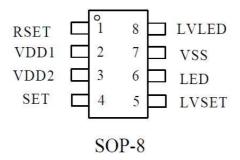
- Handheld Electronics
- Flash Light
- Lighting
- Bike lights

#### PART NUMBER EXAMPLES

PART NO.	PACKAGE
T8341AD	SOP-8



# PIN ARRANGEMENT (Top view)



#### PIN DESCRIPTION

SYMBOL	SOP-8	DESCRIPTION		
RSET	1	Connect to an external resistor to set output current.		
VSS	7	Ground		
VDD1	2	Analog Power Input (*Note)		
VDD2	3	Digital Power Input (*Note)		
LED	6	LED Cathode Connection.		
SET	4	Control ON/OFF or dimming and Flash mode		
LVLED	8	Low Voltage Detection Indicator LED Cathode Connection.		
LVSET	5	Connect to an external resistor to set "low voltage detection" level.		

<sup>\*</sup>Note: The connection to the power supply is necessary.



## **ABSOLUTE MAXIMUM RATINGS**

Parameter	Symbol	Value	Unit
Voltage on any pin relative to GND	$V_{_{\mathrm{IN}}}$	-3 to+7	V
Operating Temperature Rang	$T_{_{\mathrm{A}}}$	-40 to +85	$^{\circ}\!\mathbb{C}$
Maximum Soldering Temperature (at leads, 10 sec)	T LEAD	300	$^{\circ}\!\mathbb{C}$
Storage Temperature Rang	$T_s$	-65 to +150	$^{\circ}\!\mathbb{C}$
Continuous Power Dissipation ( $T_A = +70$ C)	SOP-8	800	mW

#### **Electrical Characteristics**

(Typical values are at TA=25°C, VDD=5V)

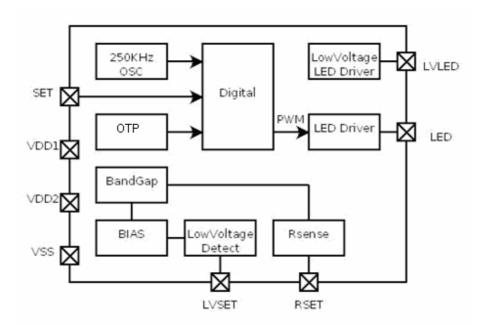
Symbol	Description	Conditions	Min.	Тур.	Max.	Unit
VDD	Operating voltage range	power supply input	2.5		6	V
VRSET	RSET bias voltage	no loading	0.475	0.5	0.525	V
ISET	SET input current range			100		$\mu$ A
ILED	Maximum LED sink current	$ \begin{array}{l} 0.5V\!\geqq\!V_{\text{in}}V_{\text{LED}}\leqq\!0.8V\\ V_{\text{in}}V_{\text{LED}}\geqq\!0.8V, \text{ILED}\!<\!1A\\ \text{ILED}\!=\!360\text{*-}(0.5/\text{RSET}) \end{array} $			1.0	A
IDD	Quiescent supply current	VDD = 5V VLED =3.5V SET LED OFF			2	μΑ
$V_{\scriptscriptstyle  m LED}$	LED operating range		0.7		VDD-1	V
$I_{\scriptscriptstyle LVLED}$	Low voltage detection output	4mA		Options ±10		%
$V_{\scriptscriptstyle LVLED}$	LVLED operating range		0.7		VDD-1	V
$ m V_{\scriptscriptstyle LVSET}$	Low voltage detection level	LVSET reference current	24.75	27.5	30.25	μΑ
V LVSET	Low voltage detection level	LVSET minimum voltage		0.1*VDD		V
ILSD	LED current in shutdown Standby Current	VLED= 3.3V, VDD=5V, TA=+25 C			1	$\mu$ A
t <sub>sh</sub>	SET pin Signal pulse width		30			mS
V <sub>IH</sub>	VSET Input high voltage	VSET >VIH for enable, VDD=5V	2			V
V <sub>IL</sub>	VSET Input low voltage	VSET <vil disable,<br="" for="">VDD=5V</vil>			0.8	V
$T_{\scriptscriptstyle TP}$	Thermal Protect	Thermal Protect±10%		160		$^{\circ}\!\mathbb{C}$

Notes:

<sup>\*</sup>Parameters are not tested at production. Parameters are guaranteed by design, characterization and process control.



## **Block Diagram**



## **Output Current Setting**

RSET pin controls the LED bias current. Imax Current flowing into LED is 360 times greater than the current flowing into RSET.

User can set the output current as the following formula: each channel:

ILED = 360 (0.5 V / RSET)

T8341 LED Pin ( $V_{\text{\tiny LED}}$ ) operating range  $0.7V > V_{\text{\tiny LED}} < VDD-1V$ 

Reference				
I LED(A)	1	0.8	0.5	0.3
$RSET(\Omega)$	180	225	360	600

## **Power on the Function Options**

The T8341AD can choose the initial states when powering on. The flexibility design allows starting the steps with all function in loop or choosing only the certain functions.

# **Dimming Function Option**

Dimming options are to set the output current 1/2, 1/3, 1/4, 1/5 and 1/6. This function can be the multiple choices as needed.

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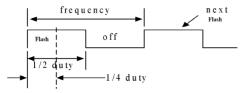


## **Flash Function Option**

Flash frequency options: 1Hz / 2 Hz /4 Hz / 6 Hz

Flash duty options: 1/2, 1/4, 1/8, 1/16

For example:



# **S.O.S** frequency options:

The time length of S.O.S and the interval time can be followed as the below table:

	Option1	Option2	Option3	Option4
time length of S.O.S	17.4sec	8.6sec	4.28sec	2.86sec

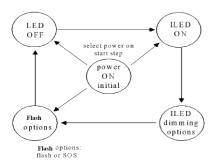
## **Low Voltage Detection**

The T8341AD builds in Low Voltage Detection and Indication function. If VIN voltage drops to the default value of Detection Voltage, LVLED pin will turn on to lighten the low voltage indicator. Detected options:

- a. Voltage level detection options are subject to the External Resistor, user can set the voltage level as following formula:
  - LV=55uA \* RLVS
- b. LVLED output current: 4 mA

#### **Function of SET**

When keeping at high level, SET pin will active as the requested function in order, like LED ON / OFF, dimming and Flash mode.



if none select step function, jump to next step



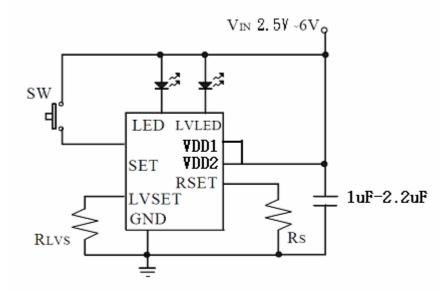
**OTP Table of Function Options** 

JII Table of Fullet	ton options				
Options item	Options				
Power-On State	LED OFF	LED ON	Flicker		
OFF State	LED OFF				
Dimming	1/2	1/3	1/4	1/5	1/6
Flicker Frequency	1Hz	2Hz	4Hz	6Hz	
Flicker Duty	1/2	1/4	1/8	1/16	
Flash Options	SUPER	s.o.s	Flash		
S.O.S Speed	17.4 Sec	8.6 Sec	4.28 Sec	2.86 Sec	
LVLED Current	4mA				
Package	SOP-8				



# **Application Circuit**

Refer to Block Diagram.

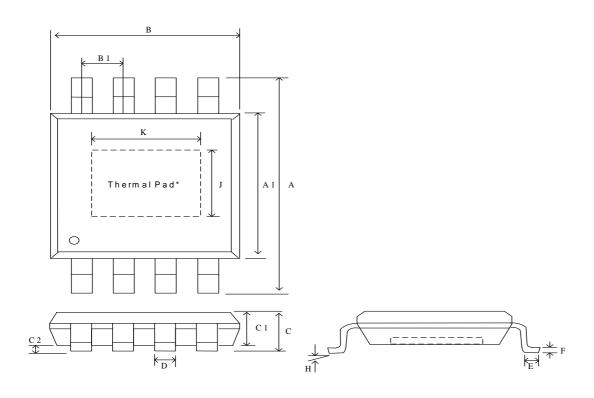


Very low-cost, high-efficiency solution for SOP-8 package

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# PACKAGE DIMENSIONS 8-LEAD SOP



Symbol	Dimension in mm			Dimension in inch		
	Min.	Тур.	Max.	Min.	Тур.	Max.
A	5.70	6.00	6.30	0.224	0.236	0.248
A1	3.75	3.95	4.10	0.148	0.156	0.164
В	ı	-	5.13	-	=	0.202
B1	ı	1.27	ı	-	0.050	ı
С	-	-	1.80	-	-	0.071
C1	1.35	1.55	1.75	0.052	0.061	0.069
C2	0	-	0.15	0.000	-	0.006
D	0.31	0.41	0.51	0.012	0.016	0.020
Е	0.30	0.50	0.70	0.012	0.020	0.028
F	0.10	0.15	0.25	0.004	0.006	0.010
J		2.23 REF			0.088 REF	
K		2.97 REF			0.117 REF	
Н		0~8°			0~8°	

#### \*Note:

The thermal pad on the IC's bottom has to be mounted on the copper foil.

To eliminate the noise influence, the thermal pad is suggested to be connected to GND on PCB.

In addition, desired thermal conductivity will be improved, if a heat-conducting copper foil on PCB is soldered with thermal pad. The thermal pad enhances the power dissipation. As a result, a large amount of current can be sunk safely in one package.