

# UNISONIC TECHNOLOGIES CO., LTD

**MMBF170 Preliminary Power MOSFET** 

# 0.5A, 60V N-CHANNEL **ENHANCEMENT MODE FIELD EFFECT TRANSISTOR**

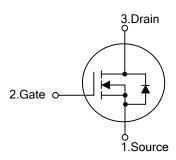
# **DESCRIPTION**

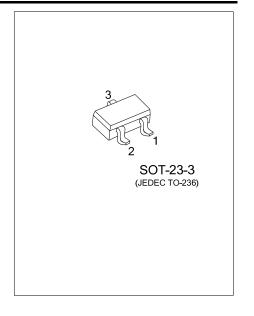
The UTC MMBF170 is an N-channel enhancement MOSFET using UTC's advanced technology to provide the customers with perfect R<sub>DS(ON)</sub>, low input capacitance, low gate threshold voltage and high switching speed.

#### **FEATURES**

- \*  $R_{DS(ON)}$ <5m $\Omega$  @  $V_{GS}$ =10V, $I_D$ =0.2A
- \* High Switching Speed
- \* Low Input Capacitance(typical 22pF)

### **SYMBOL**

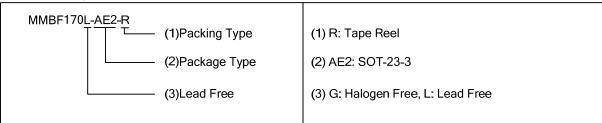




#### **ORDERING INFORMATION**

Ordering	Dookogo	Pin	Assignn	Dooking			
Lead Free	Halogen Free	Package	1	2	3	Packing	
MMBF170L-AE2-R	MMBF170G-AE2-R	SOT-23	S	G	D	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



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## ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		$V_{DSS}$	60	V	
Cata Cauraa Valtaga	Continuous	\/	±20	V	
Gate-Source Voltage	Pulsed	V <sub>GSS</sub>	±40	V	
Drain-Gate Voltage R <sub>GS</sub> ≤1.0MΩ		$V_{DGR}$	60	V	
Drain Current (Note 2)	Continuous	I <sub>D</sub>	500	mA	
Drain Current (Note 2)	Pulsed	I <sub>DM</sub>	800	mA	
Power Dissipation (Note 2)		Ъ	225	mW	
Derating above T <sub>A</sub> =25°C (Note 2)		P <sub>D</sub>	1.80	mW/°C	
Junction Temperature		TJ	150	°C	
Storage Temperature		T <sub>STG</sub>	-55~+150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Valid provided that terminals are kept at specified ambient temperature.

#### **■ THERMAL CHARACTERISTICS**

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	$\theta_{JA}$	556	°C/W	

### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C, unless otherwise specified)

	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT			
OFF CHARACTERISTICS (Note 1)									
Drain-Source Breakdown Voltage		I <sub>D</sub> =100μA, V <sub>GS</sub> =0V		70		V			
Drain-Source Leakage Current		V <sub>DS</sub> =60V, V <sub>GS</sub> =0V			1.0	μΑ			
Forward	- I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =+15V			+10	nA			
Reverse		V <sub>DS</sub> =0V, V <sub>GS</sub> =-15V			-10	nA			
ON CHARACTERISTICS (Note 1)									
Gate Threshold Voltage		$V_{DS}=V_{GS}$ , $I_{D}=-250\mu A$		2.1	3.0	V			
Static Drain-Source On-State Resistance		V <sub>GS</sub> =10V, I <sub>D</sub> =200mA			5.0				
		$V_{GS}$ =4.5V, $I_D$ =50mA			5.3	Ω			
Forward Transconductance		$V_{DS}$ =10V, $I_{D}$ =0.2A 80				mS			
DYNAMIC PARAMETERS									
Input Capacitance				22	40	pF			
Output Capacitance		V <sub>GS</sub> =0V, V <sub>DS</sub> =10V, f=1.0MHz		11	30	pF			
Reverse Transfer Capacitance				2.0	5.0	pF			
Turn-ON Delay Time		$V_{DD}$ =25V, $I_{D}$ =0.5A, $V_{GS}$ =10V, $R_{GEN}$ =50 $\Omega$			10	ns			
Turn-OFF Delay Time					10	ns			
	Forward Reverse	BV <sub>DSS</sub>	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$			

Notes: 1. Pulse width ≤300µs, duty cycle ≤2%.

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