

FEATURES

- Extremely Low Supply Current(50 μ A,Typ.)
- Very Low Dropout Voltage
- 300mA Output Current
- Compatible with MLCC
- High Output Voltage Accuracy $\pm 1.4\%$
- Standard Custom Output Voltages
- Over Current and Over Temperature Protection
- Small input/output differential : 0.3V for full load
- Moisture Sensitivity Level 3

APPLICATIONS

- Battery Operated Systems
- Portable Computers
- Portable Cameras and Video Recorders
- Medical Instruments
- Instrumentation
- Cellular/GSM/PHS Phones
- Linear Post-Regulators for SMPS
- Pagers

DESCRIPTION

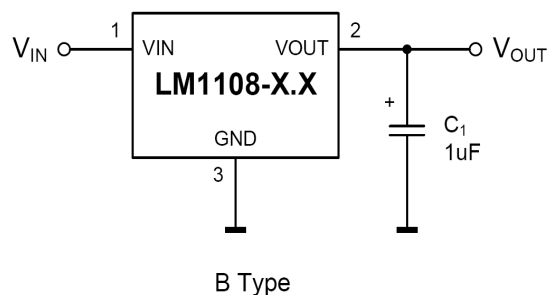
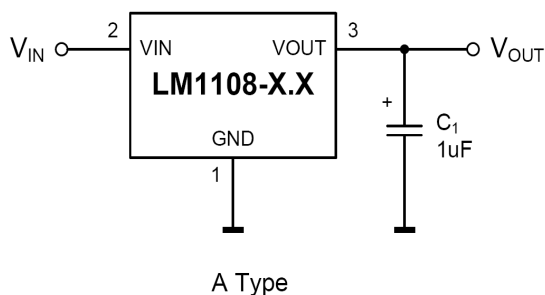
The LM1108 is a fixed output, high accuracy (typically $\pm 0.5\%$) CMOS low drop-out regulator. Total supply current is typically 50 μ A at full load (20 to 60 times lower than in bipolar regulators). LM1108 key features include ultra low noise operation, very low dropout voltage (typically 240mV at full load), and fast response to step changes in load.

The LM1108 incorporates both over temperature and over current protection.

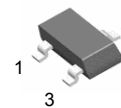
The LM1108 is stable with an output capacitor of only 1 μ F and has a maximum output current of 300mA.

It is available in a SOT-23 & SOT-89 package

TYPICAL APPLICATION CIRCUIT

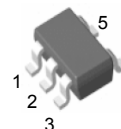


SOT-23 (2.92*1.60*1.10)



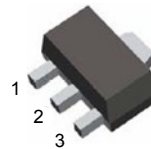
1. GND 2. Vin 3. Vout [A type]
1. Vin 2. Vout 3. GND [B type]

SOT-23-5L (SOT-25)



1. GND 2. Vin 3. Vout 4. NC 5. NC

SOT-89



1. GND 2. Vin 3. Vout

ORDERING INFORMATION

Device	Marking	Package
LM1108SF-X.X (A type)	HXX	SOT-23 3L
LM1108BSF-X.X (B type)	BXX	
LM1108GSF-X.X (H/F)	HGXX	
LM1108SF5-X.X	HXX	SOT-23 5L
LM1108F-X.X	1108 X.X	SOT-89

("X.X" = Output Voltage
= 1.5, 1.8, 2.5, 3.0, 3.3, 3.6, 5.0)

ABSOLUTE MAXIMUM RATING

Characteristic		Symbol	Value	Unit
Supply Voltage		Vin	+6.5	V
Output Current		Iout	300	mA
Output Voltage		Vout	Vss-0.3 to Vin+0.3	V
Total Power Dissipation	SOT23 PKG	Pd	230	mW
	SOT89 PKG		500	
Operating Ambient Temperature		Topr	-40 ~ +85	°C
Lead Temperature (soldering, 5 sec)			260	°C
Storage Temperature		Tstg	-40 ~ +125	°C

ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Condition	Limit			Units
			Min	Typ	Max	
Output Voltage Accuracy	Vout	Io=1mA	-1.5%	1	1.5%	V
		Io=0~300 mA		2		
Line Regulation	$\Delta V_{out}/\Delta V_{in}$	Io=1mA, (Vout+0.1V)<Vin<6.5V		0.1	0.3	%/V
Load Regulation(Note.1)	$\Delta V_{out}/\Delta I_o$	Vin=6V, 0.1mA<Io<300 mA, Cout=1uF		0.005	0.04	%/mA
Maximum Output Current	Io	Vin=5V, Vout > 0.96VRATING	300	500		mA
Current Limit			400			mA
Ground Current	Ignd	Io=0~300 mA		15	30	μA
Dropout Voltage for Vout>2.5V	Vdrop	Io=100 mA		100	180	mV
		Io=300 mA		300	550	
2.0V<Vout<2.5V		Io=100 mA		150	300	
		Io=300 mA		450	800	
Vout<2.0V		Io=100 mA		200	400	
		Io=300 mA		600	1100	

Note.1 : Load Regulation is measured using pulse techniques with duty cycle<5%

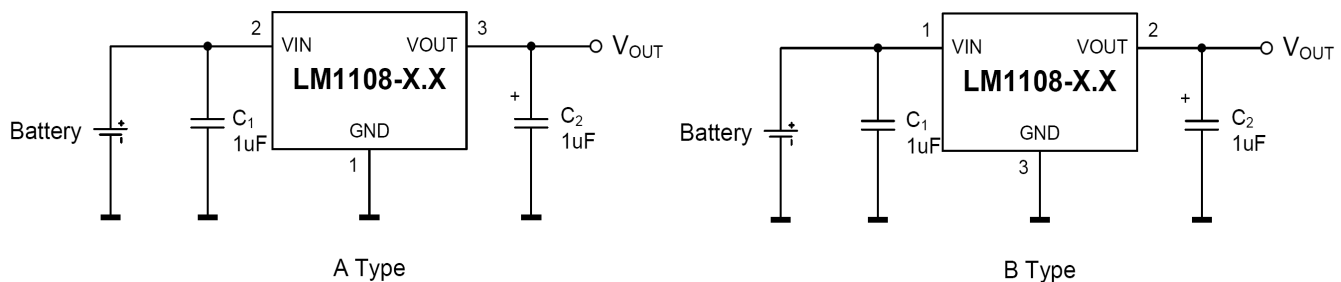
Detail Description

- The LM1108 is a precision, fixed output LDO.

Unlike bipolar regulators, the LM1108's supply current does not increase with load current.

In addition, V_{OUT} remains stable and within regulation over the entire 0mA to $I_{OUT\ MAX}$ operating load current range, (an important consideration in RTC and CMOS RAM battery back-up applications).

Figure3-1 shows a typical application circuit.



1. Output Capacitor

1uF(min) capacitor from V_{OUT} to ground is required.

The output capacitor should have an effective series resistance greater than 0.1Ω and less than $.0\Omega$.

1uF capacitor should be connected from V_{IN} to GND if there is more than 10 inches of wire between the regulator and the AC filter capacitor or if a battery is used as the power source. Aluminum electrolytic or tantalum capacitor types can be used. (Since many aluminum electrolytic capacitors freeze at approximately -30°C , solid tantalums are recommended for applications operating below -25°C)

When operating from sources other than batteries, supply-noise rejection and transient response can be improved by increasing the value of the input and output capacitors and employing passive filtering techniques.

2. THERMAL CONSIDERATIONS

2.1 Thermal Shutdown

Integrated thermal protection circuitry shuts the regulator off when die temperature exceeds 150°C.

The regulator remains off until the die temperature drops to approximately 140°C.

2.2 Power Dissipation

The amount of power the regulator dissipate is primarily a function of input and output voltage, and output current. The following equation is used to calculate worst case actual power dissipation:

EQUATION 2-1:

The maximum allowable power dissipation (Equation 2-2) is a function of the maximum ambient temperature (T_{AMAX}), the maximum allowable die temperature (T_{JMAX}) and the thermal resistance from junction-to-air (θ_{JA}).

TABLE 2-1 :

$$P_D \approx (V_{INMAX} - V_{OUTMIN})I_{LOADMAX}$$

Where:

P_D = Worst case actual power dissipation

V_{INMAX} = Maximum voltage on V_{IN}

 V_{OUTMIN} = Minimum regulator output voltage

$I_{LOADMAX}$ = Maximum output (load) current

EQUATION 2-2:

$$P_{D\text{MAX}} = \frac{(T_{J\text{MAX}} - T_{A\text{MAX}})}{\theta_{JA}}$$

Where all terms are previously defined.

Table 2-1 shows various values of θ_{JA} for the LM1108 versus board copper area.

TABLE 2-1 : THERMAL RESISTANCE GUIDELINES FOR LM1108

Copper Area (Topside)*	Copper Area (Backside)	Board Area	Thermal Resistance (θ_{JA})
2500 sq mm	2500 sq mm	2500 sq mm	45°C/W
1000 sq mm	2500 sq mm	2500 sq mm	45°C/W
225 sq mm	2500 sq mm	2500 sq mm	53°C/W
100 sq mm	2500 sq mm	2500 sq mm	59°C/W
1000 sq mm	1000 sq mm	1000 sq mm	52°C/W
1000 sq mm	0 sq mm	1000 sq mm	55°C/W

NOTE: *Tab of device attached to topside copper

Equation 2-1 can be used in conjunction with Equation 2-2 to ensure regulator thermal operation is within limits.

For example:

Given:

$$\begin{aligned} V_{IN\text{MAX}} &= 3.3\text{V} + 10\% \\ V_{OUT\text{MIN}} &= 2.7\text{V} - 0.5\% \\ I_{LOAD\text{MAX}} &= 275\text{mA} \\ T_{J\text{MAX}} &= 125^\circ\text{C} \\ T_{A\text{MAX}} &= 95^\circ\text{C} \\ \theta_{JA} &= 59^\circ\text{C/W} \end{aligned}$$

Find: 1. Actual power dissipation
2. Maximum allowable dissipation

Actual power dissipation:

$$\begin{aligned} P_D &\approx (V_{IN\text{MAX}} - V_{OUT\text{MIN}})I_{LOAD\text{MAX}} \\ &= [(3.3 \times 1.1) - (2.7 \times .995)]275 \times 10^{-3} \\ &= 260\text{mW} \end{aligned}$$

Maximum allowable power dissipation:

$$\begin{aligned} P_{D\text{MAX}} &= \frac{(T_{J\text{MAX}} - T_{A\text{MAX}})}{\theta_{JA}} \\ &= \frac{(125 - 95)}{59} \\ &= 508\text{mW} \end{aligned}$$

In this example, the LM1108 dissipates a maximum of 260mW; below the allowable limit of 508 mW. In a similar manner, Equation 2-1 and Equation 2-2 can be used to calculate maximum current and/or input voltage limits. For example, the maximum allowable V_{IN} , is found by substituting the maximum allowable power dissipation of 508 mW into Equation 2-1, from which $V_{IN\text{MAX}} = 4.6\text{V}$.