



## 01N30

Preliminary

Power MOSFET

### 0.1A, 300V N-CHANNEL POWER MOSFET

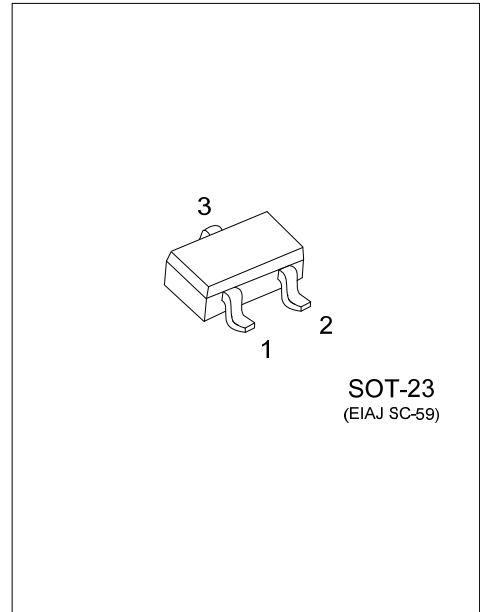
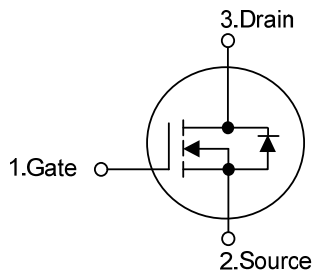
#### DESCRIPTION

The UTC **01N30** is a N-channel mode power MOSFET using UTC's advanced technology to provide customers with a minimum on-state resistance, low gate charge and superior switching performance.

#### FEATURES

- \*  $R_{DS(ON)} \leq 9.0\Omega$  @  $V_{GS}=10V, I_D=0.05A$
- \* High switching speed
- \* 100% avalanche tested

#### SYMBOL



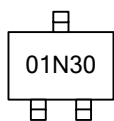
#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
01N30L-AE3-R	01N30G-AE3-R	SOT-23	G	S	D	Tape Reel

Note: Pin Assignment: G: Gate S: Source D: Drain

<p>01N30G-AE3-R</p>	<p>(1) R: Tape Reel</p> <p>(2) AE3: SOT-23</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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#### MARKING



### ■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	$V_{DSS}$	300	V
Gate-Source Voltage	$V_{GSS}$	$\pm 30$	V
Continuous Drain Current	$I_D$	0.1	A
Avalanche Current	$I_{AR}$	0.1	A
Power Dissipation	$P_D$	0.3	W
Junction Temperature	$T_J$	+150	$^{\circ}\text{C}$
Storage Temperature	$T_{STG}$	-55 ~ +150	$^{\circ}\text{C}$

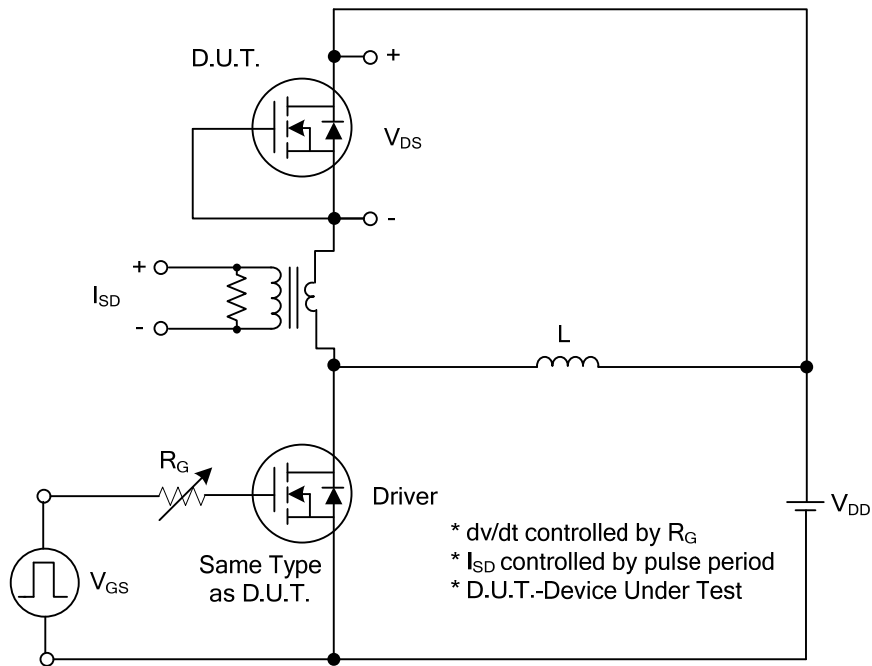
- Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.  
2. Repetitive Rating: Pulse width limited by maximum junction temperature.

### ■ ELECTRICAL CHARACTERISTICS

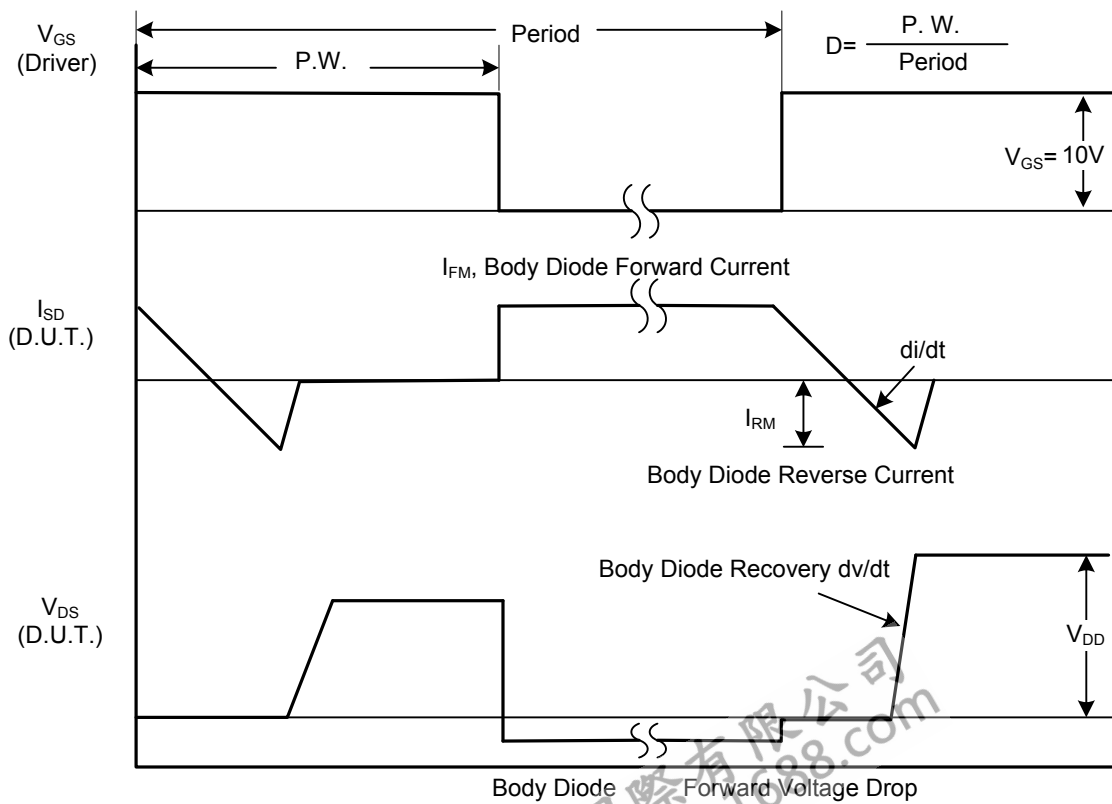
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$I_D=250\mu\text{A}, V_{DS}=0\text{V}$	300			V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=300\text{V}$			10	$\mu\text{A}$
Gate-Source Leakage Current	$I_{GSS}$	Forward $V_{GS}=+30\text{V}, V_{DS}=0\text{V}$			$\pm 100$	nA
		Reverse $V_{GS}=-30\text{V}, V_{DS}=0\text{V}$			$\pm 100$	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$I_D=250\mu\text{A}$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10\text{V}, I_D=0.05\text{A}$			9.0	$\Omega$
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	$C_{ISS}$	$V_{GS}=0\text{V}, V_{DS}=25\text{V}, f=1\text{MHz}$		94.2		pF
Output Capacitance	$C_{OSS}$			14.1		pF
Reverse Transfer Capacitance	$C_{RSS}$			12.1		pF
<b>SWITCHING PARAMETERS</b>						
Total Gate Charge	$Q_G$	$V_{DS}=30\text{V}, V_{GS}=10\text{V}, I_D=1.3\text{A}$ $I_G=100\mu\text{A}$ (Note1, 2)		4.8		nC
Gate to Source Charge	$Q_{GS}$			0.3		nC
Gate to Drain Charge	$Q_{GD}$			0.5		nC
Turn-ON Delay Time	$t_{D(ON)}$	$V_{DS}=50\text{V}, V_{GS}=10\text{V}, I_D=0.5\text{A},$ $R_G=25\Omega$ (Note1, 2)		16		ns
Rise Time	$t_R$			16.8		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			63.2		ns
Fall-Time	$t_F$			38.8		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Body-Diode Continuous Current	$I_S$				0.1	A
Maximum Body-Diode Pulsed Current	$I_{SM}$				0.4	A
Drain-Source Diode Forward Voltage	$V_{SD}$	$I_S=0.1\text{A}$			1.4	V
Reverse Recovery Time	$t_{rr}$	$V_{GS}=0\text{V}, I_S=0.1\text{A},$		57.2		ns
Reverse Recovery Charge	$Q_{rr}$	$dI_F/dt=100\text{A}/\mu\text{s}$		54.8		nC

- Notes: 1. Pulse Test : Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .  
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

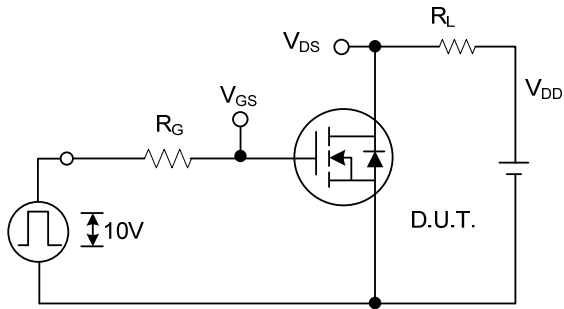


Peak Diode Recovery dv/dt Test Circuit

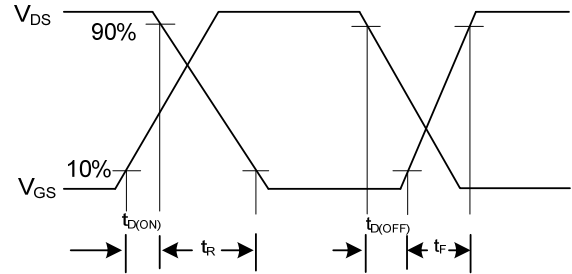


Peak Diode Recovery dv/dt Waveforms

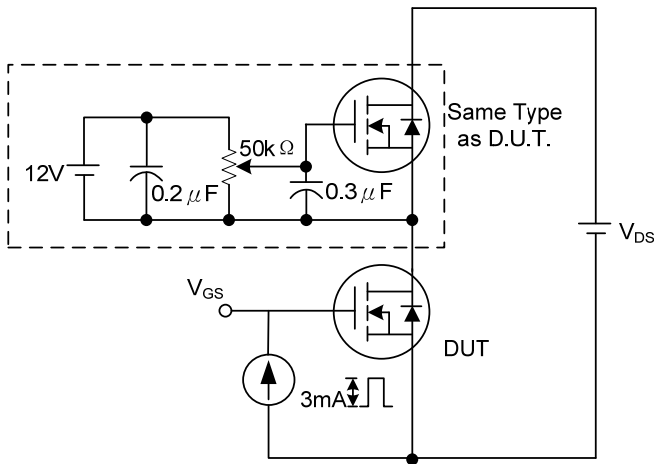
■ TEST CIRCUITS AND WAVEFORMS



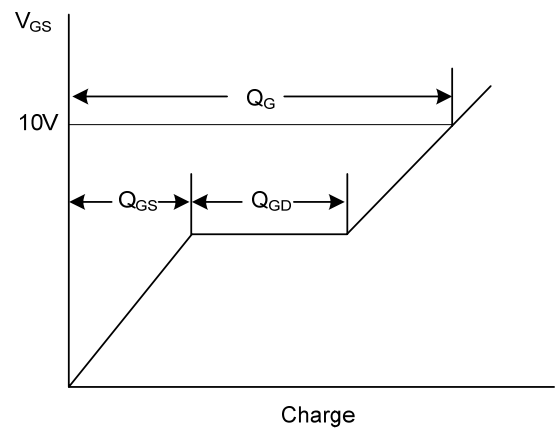
Switching Test Circuit



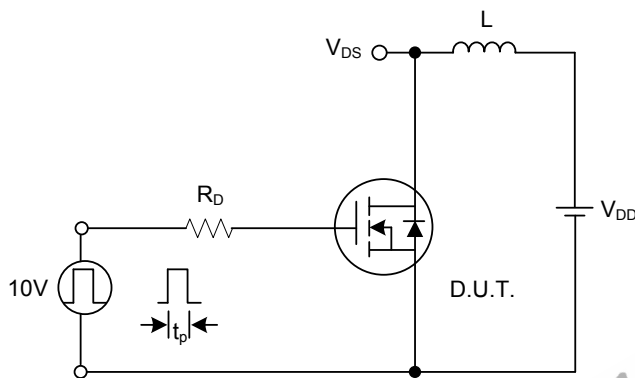
Switching Waveforms



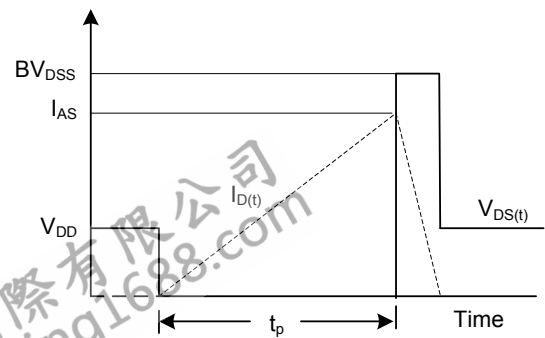
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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