

# UNISONIC TECHNOLOGIES CO., LTD

01N45 Preliminary Power MOSFET

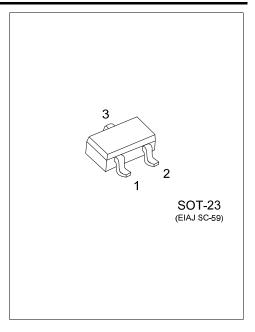
# 0.1A, 450V N-CHANNEL POWER MOSFET

#### DESCRIPTION

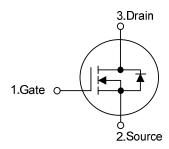
The UTC **01N45** is a N-channel mode power MOSFET using UTC's advanced technology to provide customers with a minimum on-state resistance, low gate charge and superior switching performance.

#### ■ FEATURES

- \*  $R_{DS(ON)} \le 16\Omega$  @  $V_{GS}=10V$ ,  $I_D=0.05A$
- \* High switching speed
- \* Typically 5.76nC low gate charge
- \* 100% avalanche tested



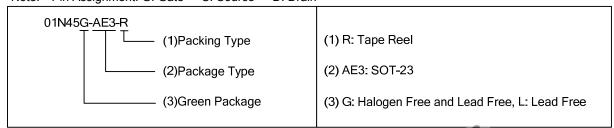
#### ■ SYMBOL



#### ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Daakina
Lead Free	Halogen Free	Package	1	2	3	Packing
01N45L-AE3-R	01N45G-AE3-R	SOT-23	G	S	D	Tape Reel

Note: Pin Assignment: G: Gate S: Source D: Drain



#### ■ MARKING



www.unisonic.com.tw 1 of 4

#### **ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	RATINGS	UNIT	
Drain-Source Voltage	$V_{DSS}$	450	V	
Gate-Source Voltage	$V_{GSS}$	±30	V	
Continuous Drain Current	I <sub>D</sub>	0.1	Α	
Avalanche Current	I <sub>AR</sub>	0.1	Α	
Power Dissipation	P <sub>D</sub>	0.3	W	
Junction Temperature	TJ	+150	°C	
Storage Temperature	T <sub>STG</sub>	-55 ~ <b>+</b> 150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

#### **ELECTRICAL CHARACTERISTICS**

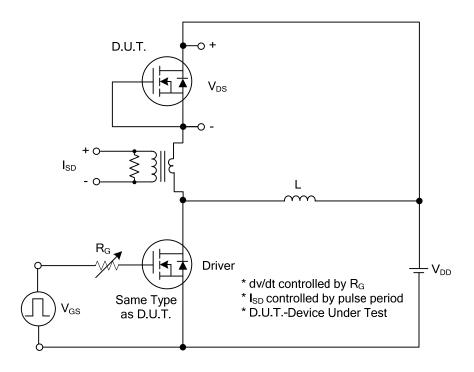
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>DS</sub> =0V	450			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =450V			10	μΑ
Coto Source Legicage Current Forward	I <sub>GSS</sub>	V <sub>GS</sub> =+30V, V <sub>DS</sub> =0V			+100	nA
Gate-Source Leakage Current Reverse		V <sub>GS</sub> =-30V, V <sub>DS</sub> =0V			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	I <sub>D</sub> =250μA	1.0		3.0	V
Static Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =0.05A			16	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C <sub>ISS</sub>			133		рF
Output Capacitance	Coss	$V_{GS}$ =0V, $V_{DS}$ =25V, f=1MHz		13		рF
Reverse Transfer Capacitance	C <sub>RSS</sub>			5		рF
SWITCHING PARAMETERS						
Total Gate Charge	$Q_G$	V 50V V 40V I 0.5A		5.76		nC
Gate to Source Charge	$Q_{GS}$	$V_{DS}$ =50V, $V_{GS}$ =10V, $I_{D}$ =0.5A $I_{G}$ = 100 $\mu$ A (Note1, 2)		0.26		nC
Gate to Drain Charge	$Q_GD$	Π <sub>G</sub> - ΤουμΑ (Note 1, 2)		0.54		nC
Turn-ON Delay Time	t <sub>D(ON)</sub>			14.4		ns
Rise Time	$t_R$	$V_{DS}$ =30V, $V_{GS}$ =10V, $I_{D}$ =0.5A,		16.8		ns
Turn-OFF Delay Time	t <sub>D(OFF)</sub>	R <sub>G</sub> =25Ω (Note1, 2)		62.8		ns
Fall-Time	t <sub>F</sub>			46		ns
SOURCE- DRAIN DIODE RATINGS AND	CHARACTERI	ISTICS				
Drain-Source Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =0.1A,V <sub>GS</sub> =0V			1.4	V
Maximum Body-Diode Continuous Current	I <sub>S</sub>				0.1	Α
Maximum Body-Diode Pulsed Current	I <sub>SM</sub>				0.4	Α
Reverse Recovery Time	t <sub>rr</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =0.1A,		43.6		ns
Reverse Recovery Charge	Q <sub>rr</sub>	dI <sub>F</sub> /dt=100A/μs		49		nC

Notes: 1. Pulse Test : Pulse width ≤300µs, Duty cycle ≤2%.

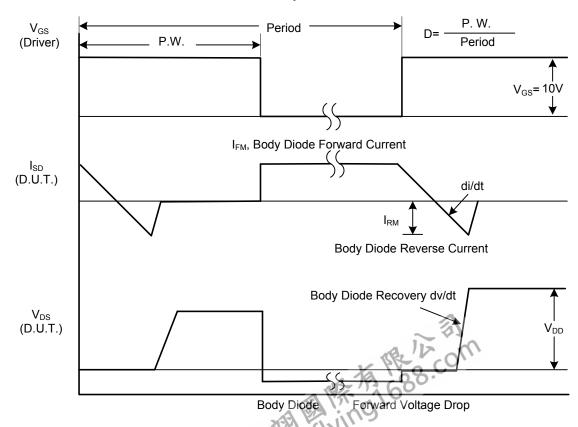
2. Essentially independent of operating temperature.



# **■ TEST CIRCUITS AND WAVEFORMS**

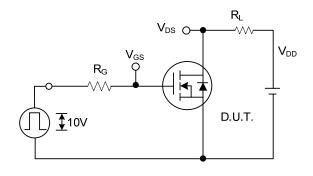


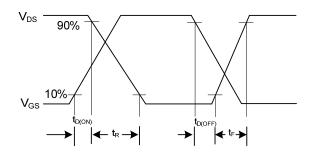
# Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

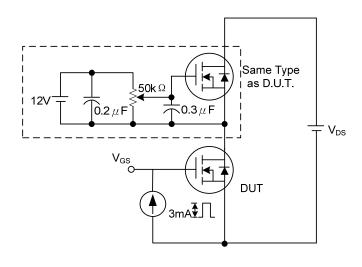
# **TEST CIRCUITS AND WAVEFORMS**

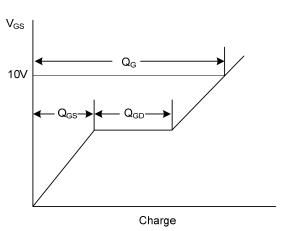




**Switching Test Circuit** 

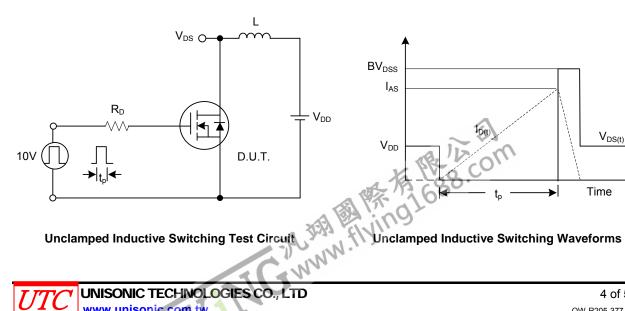
**Switching Waveforms** 

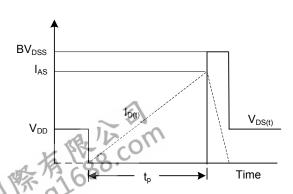




**Gate Charge Test Circuit** 

**Gate Charge Waveform** 





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