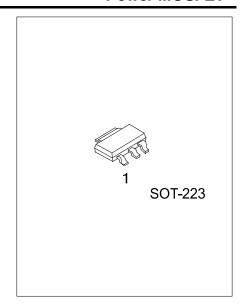
03N65-CB Preliminary Power MOSFET

0.3A, 650V N-CHANNEL POWER MOSFET

■ DESCRIPTION

The UTC **03N65-CB** is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.



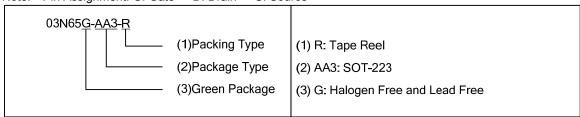
■ FEATURES

- * $R_{DS(on)}$ < 26 Ω @ V_{GS} =10V, I_{D} =0.15A
- * High Switching Speed
- * High Cell Density Trench Technology

■ ORDERING INFORMATION

Ordering Number	Package	Pin Assignment			Dooking	
		1	2	3	Packing	
03N65G-AA3-R	SOT-223	G	D	S	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



■ MARKING



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ABSOLUTE MAXIMUM RATINGS (T_C =25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	650	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous	I_{D}	0.3	Α
	Pulsed	I_{DM}	1.2	Α
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	7.3	mJ
Power Dissipation		P_{D}	5.0	W
Junction Temperature		T_J	150	°C
Storage Temperature Range		T_{STG}	-55 ~ + 150	°C

- Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
 - 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
 - 3. L=30mH, I_{AS} =0.7A, V_{DD} =50V, R_{G} =25 Ω , Starting T_{J} = 25°C

THERMAL DATA

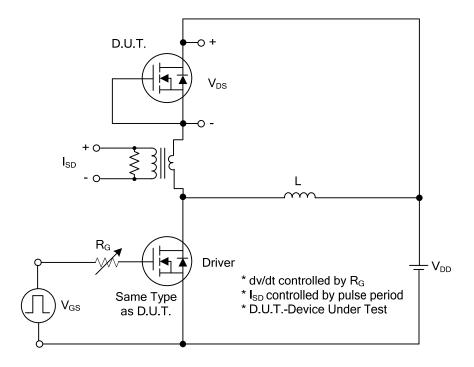
PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	150	°C/W	
Junction to Case	$\theta_{ m JC}$	25	°C/W	

ELECTRICAL CHARACTERISTICS (T_J =25°C, unless otherwise specified)

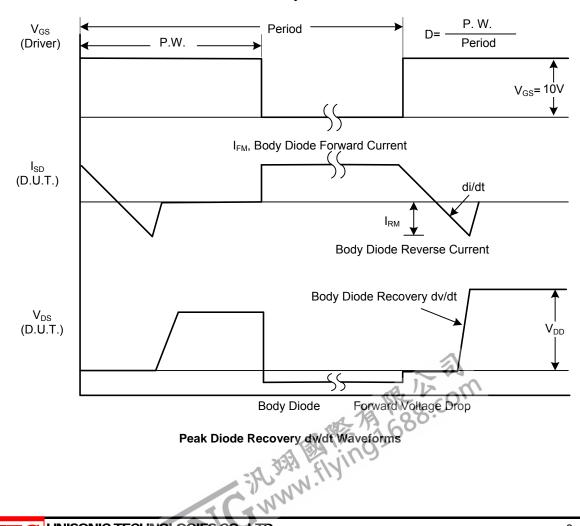
PARAMETER		SYMBOL	TEST CONDITIONS		TYP	MAX	UNIT
OFF CHARACTERISTICS		, 3 	,				
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μA, V _{GS} =0V	650			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =650V, V _{GS} =0V			10	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{GS} =+30V, V _{DS} =0V			+100	nA
	Reverse		V _{GS} =-30V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2.0		4.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =0.15A			26	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}			60		pF
Output Capacitance		Coss	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		10		pF
Reverse Transfer Capacitance		C_{RSS}			4.5		pF
SWITCHING PARAMETERS							
Total Gate Charge (Note 1)		Q_G	\ - 50\\ \\ - 10\\ \ \ - 0.2A		4.0		nC
Gate to Source Charge Gate to Drain Charge		Q_GS	V_{DS} = 50V, V_{GS} = 10V, I_{D} = 0.3A, I_{D} =100 μ A (Note 1, 2)		8.0		nC
		Q_{GD}			0.5		nC
Turn-on Delay Time (Note 1)		t _{D(ON)}			27		ns
Rise Time Turn-off Delay Time		t_{R}	V_{DS} = 30V, V_{GS} = 10V, I_{D} = 0.3A,		14		ns
		t _{D(OFF)}	$R_G = 25\Omega \text{ (Note 1, 2)}$		48		ns
Fall-Time		t _F			20		ns
SOURCE- DRAIN DIODE RATING	GS AND CH	ARACTERIS [®]	TICS				
Maximum Body-Diode Continuous	Current	Is				0.3	Α
Maximum Body-Diode Pulsed Current		I _{SM}				1.2	Α
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I _S =0.3A, V _{GS} =0V			1.4	V
Notes: 1. Pulse Test: Pulse width	≤ 300µs, Dut	y cycle ≤ 2%.	SE ON	*			
Essentially independent	of operating	temperature.	I _S =0.3A, V _{GS} =0V				
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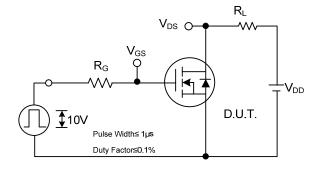
TEST CIRCUITS AND WAVEFORMS

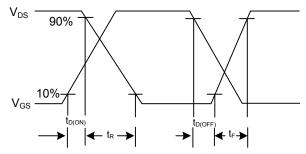


Peak Diode Recovery dv/dt Test Circuit



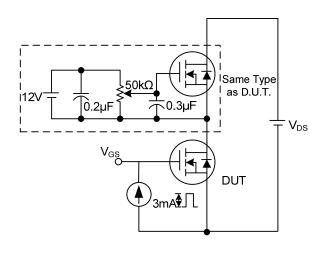
TEST CIRCUITS AND WAVEFORMS (Cont.)

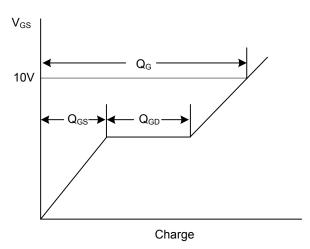




Switching Test Circuit

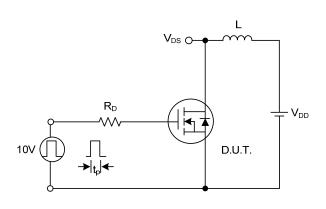
Switching Waveforms

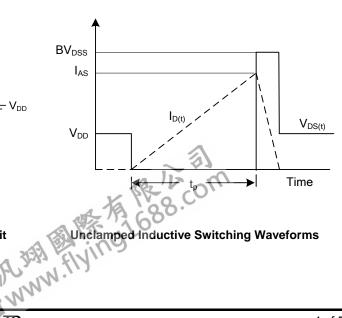




Gate Charge Test Circuit

Gate Charge Waveform





Unclamped Inductive Switching Test Circuit

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