

# **UTC** UNISONIC TECHNOLOGIES CO., LTD

# 04N30

Preliminary

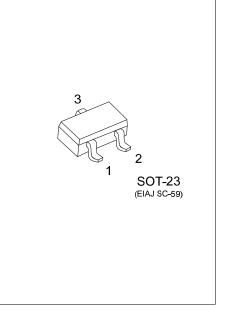
# 0.4A, 300V N-CHANNEL **POWER MOSFET**

#### DESCRIPTION

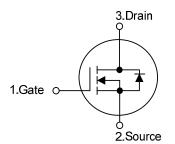
The UTC 04N30 is a N-channel mode power MOSFET using UTC's advanced technology to provide customers with a minimum on-state resistance, low gate charge and superior switching performance.

#### **FEATURES**

- \*  $R_{DS(ON)} \le 6.8\Omega$  @  $V_{GS}=10V$ ,  $I_D=0.2A$
- \* High switching speed
- \* 100% avalanche tested



#### SYMBOL



#### ORDERING INFORMATION

Ordering	Daakaga	Pin Assignment			Booking			
Lead Free	Halogen Free	Package	1	2	3	Packing		
04N30L-AE3-R	04N30G-AE3-R	SOT-23	G	S	D	Tape Reel		
Note: Pin Assignment: G: Gate S: Source D: Drain								
04N30G-AE3-R (1)Packing Type   (2)Package Type (2)Package Type   (3)Green Package (3) G: Halogen Free and Lead Free, L: Lead Free								



#### **ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V <sub>DSS</sub>	300	V
Gate-Source Voltage	V <sub>GSS</sub>	±30	V
Continuous Drain Current	ID	0.4	А
Power Dissipation	PD	1.14	W
Junction Temperature	TJ	+150	°C
Storage Temperature	T <sub>STG</sub>	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

#### THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	150	°C/W
Junction to Case	θ <sub>JC</sub>	109	°C/W

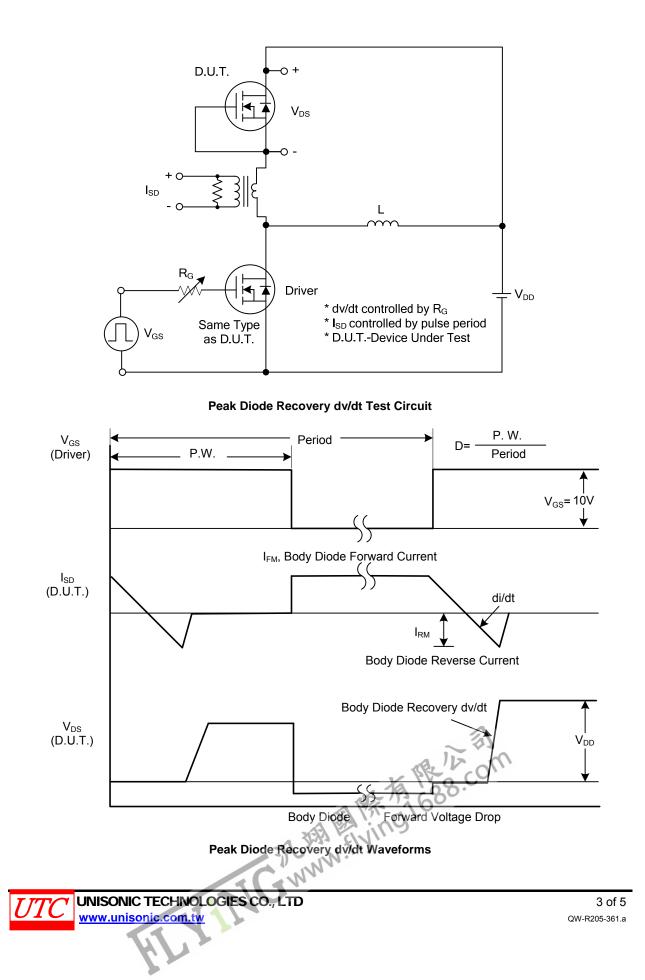
Note: The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.

#### **ELECTRICAL CHARACTERISTICS**

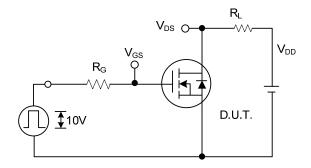
PARAMETER		SYMBOL	TEST CONDITIONS		TYP	MAX	UNIT
OFF CHARACTERISTICS			1	1	1		
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>DS</sub> =0V	300		1	V
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> =300V			10	μA
	Forward	_	V <sub>GS</sub> =+30V, V <sub>DS</sub> =0V			±100	nA
Gate-Source Leakage Current Reverse		I <sub>GSS</sub>	V <sub>GS</sub> =-30V, V <sub>DS</sub> =0V			±100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V <sub>GS(TH)</sub>	I <sub>D</sub> =250µA, V <sub>DS</sub> =V <sub>GS</sub>			3.0	V
Static Drain-Source On-State Re	esistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =0.2A			6.8	Ω
DYNAMIC PARAMETERS				_		_	
Input Capacitance	nput Capacitance				230		pF
Output Capacitance		C <sub>ISS</sub> C <sub>OSS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1MHz		18		рF
Reverse Transfer Capacitance		C <sub>RSS</sub>			6		pF
SWITCHING PARAMETERS							
Total Gate Charge		Q <sub>G</sub>	-V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =1.3A		10		nC
Gate to Source Charge		Q <sub>GS</sub>			0.8		nC
Gate to Drain Charge		Q <sub>GD</sub>	-I <sub>G</sub> = 100μΑ (Note1, 2)		0.5		nC
Turn-ON Delay Time		t <sub>D(ON)</sub>			13		ns
Rise Time		t <sub>R</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V, I <sub>D</sub> =0.5A,		16		ns
Turn-OFF Delay Time		t <sub>D(OFF)</sub>	R <sub>G</sub> =25Ω (Note1, 2)		77		ns
Fall-Time					38		ns
SOURCE- DRAIN DIODE RATI	NGS AND (	CHARACTERI	ISTICS				
Maximum Body-Diode Continuo	us Current	ls				0.4	Α
Maximum Body-Diode Pulsed Current		I <sub>SM</sub>				1.6	А
Drain-Source Diode Forward Voltage		V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =0.4A	0.1		1.48	V
Notes: 1. Pulse Test : Pulse wid	th ≤300µs, I	Duty cycle ≤2%	6.	)			
2. Essentially independer	nt of operati	ing temperatur	e. alla	2			
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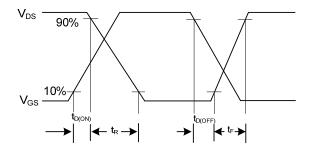
### TEST CIRCUITS AND WAVEFORMS



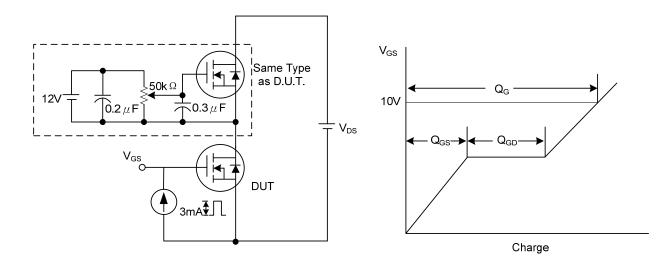
## ■ TEST CIRCUITS AND WAVEFORMS (Cont.)





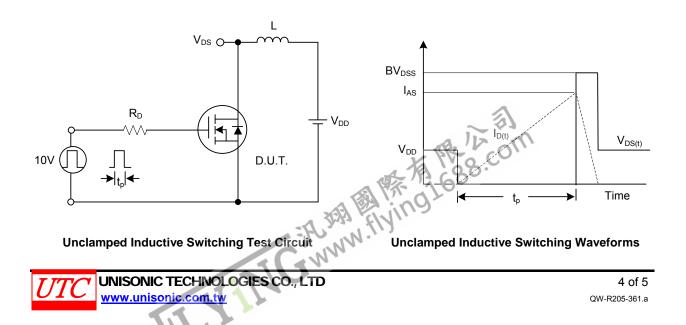






**Gate Charge Test Circuit** 

Gate Charge Waveform



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