UNISONIC TECHNOLOGIES CO., LTD

05N30 **Power MOSFET**

0.5A, 300V N-CHANNEL **POWER MOSFET**

DESCRIPTION

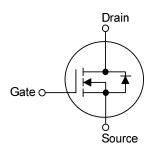
The UTC 05N30 is a N-channel mode power MOSFET using UTC's advanced technology to provide customers with a minimum on-state resistance, low gate charge and superior switching performance.

FEATURES

- * $R_{DS(ON)} \le 5.0\Omega$ @ $V_{GS} = 10V$, $I_D = 0.25A$
- * High switching speed
- * 100% avalanche tested

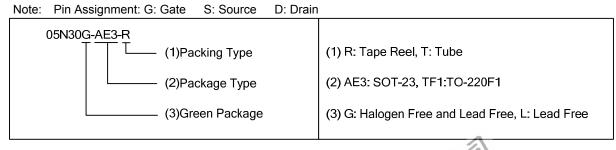
TO-220F1 SOT-23 (EIAJ SC-59)

SYMBOL



ORDERING INFORMATION

Ordering Number		Daakaga	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
05N30L-AE3-R	05N30G-AE3-R	SOT-23	G	S	D	Tape Reel	
05N30L-TF1-T	05N30G-TF1-T	TO-220F1	G	D	S	Tube	



MARKING



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■ **ABSOLUTE MAXIMUM RATINGS** (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	300	V
Gate-Source Voltage		V_{GSS}	±30	V
Continuous Drain Current		I _D	0.5	Α
Pulsed Drain Current (Note 2)		I _{DM}	2.0	Α
Power Dissipation	SOT-23	_	0.3	W
	TO-220F1	P _D	7	W
Junction Temperature		TJ	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (T_J =25°C, unless otherwise specified)

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PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS				1	1	1	
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μA, V _{DS} =0V	300			V
Drain-Source Leakage Current		I_{DSS}	V _{DS} =300V			10	μΑ
Gate-Source Leakage Current	Forward	I _{GSS}	V_{GS} =+30V, V_{DS} =0V			100	nA
	Reverse		V_{GS} =-30V, V_{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	I _D =250μA	1.0		3.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =0.25A			5.0	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C_{ISS}			145		pF
Output Capacitance		Coss	V _{GS} =0V, V _{DS} =25V, f=1MHz		23.2		pF
Reverse Transfer Capacitance		C_{RSS}			4.7		pF
SWITCHING PARAMETERS							
Total Gate Charge (Note 1)		Q_{G}	\\ -240\\ \\ -10\\ -0.54		9.2		nC
Gate to Source Charge		Q_GS	V _{DS} =240V, V _{GS} =10V, I _D =0.5A		1.5		nC
Gate to Drain Charge		Q_GD	IG- 100μΑ (Note1, 2)		0.62		nC
Turn-ON Delay Time (Note 1)		$t_{D(ON)}$			2		ns
Rise Time		t_R	V_{DS} =150V, V_{GS} =10V, I_{D} =0.5A,		4		ns
Turn-OFF Delay Time		t _{D(OFF)}	R _G =25Ω (Note1, 2)		7		ns
Fall-Time		t_{F}			45		ns
SOURCE- DRAIN DIODE RATII	NGS AND CH	ARACTERIS [®]	TICS				
Maximum Body-Diode Continuous Current		Is				0.5	Α
Maximum Body-Diode Pulsed Current		I _{SM}				2.0	Α
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I _S =0.5A, V _{GS} =0V			1.4	V

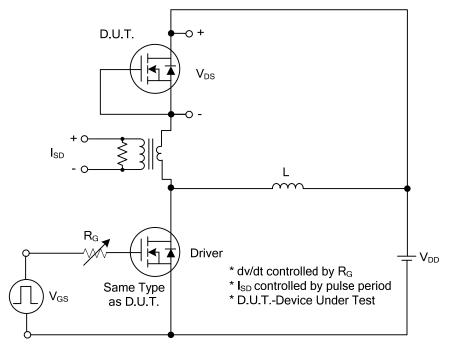
Notes: 1. Pulse Test : Pulse width ≤300µs, Duty cycle ≤2%.

^{2.} Essentially independent of operating temperature.

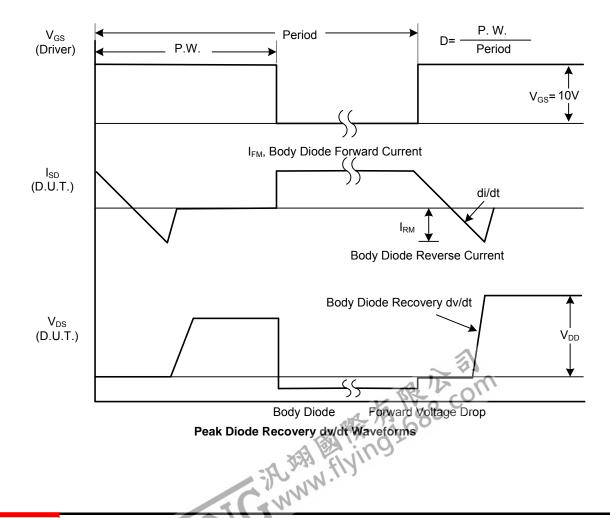


^{2.} Repetitive Rating: Pulse width limited by maximum junction temperature.

■ TEST CIRCUITS AND WAVEFORMS

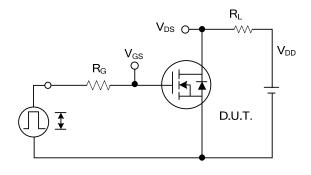


Peak Diode Recovery dv/dt Test Circuit



05N30 Power MOSFET

■ TEST CIRCUITS AND WAVEFORMS (Cont.)



V_{DS} 90%

V_{GS} 10%

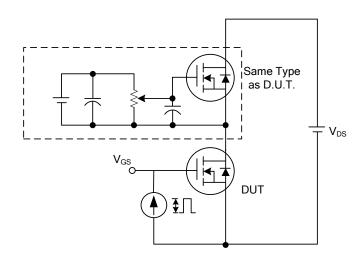
t_{D(ON)}

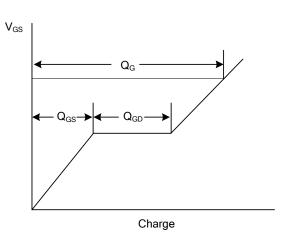
t_R → |

t_R → |

Switching Test Circuit

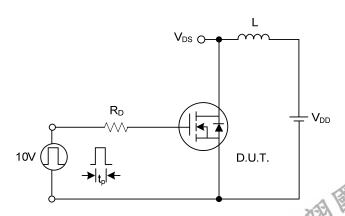
Switching Waveforms

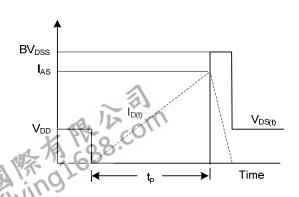




Gate Charge Test Circuit

Gate Charge Waveform





Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

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