# UNISONIC TECHNOLOGIES CO., LTD

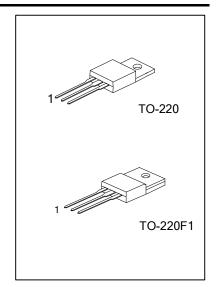
10N40 **Power MOSFET Preliminary** 

# 10.5A, 400V N-CHANNEL **POWER MOSFET**

#### **DESCRIPTION**

The UTC 10N40 is an N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology specializes in allowing a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

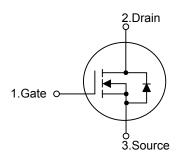
The UTC 10N40 is universally applied in electronic lamp ballast based on half bridge topology and high efficient switched mode power supply.



#### **FEATURES**

- \* High switching speed
- \*  $R_{DS(ON)}$ =0.65 $\Omega$  @  $V_{GS}$ =10V
- \* 100% avalanche tested

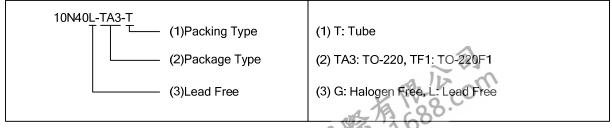
#### **SYMBOL**



#### **ORDERING INFORMATION**

Ordering Number		Dookogo	Pin	Dooking			
Lead Free	Halogen Free	Package	1	2	3	Packing	
10N40L-TA3-T	10N40G-TA3-T	TO-220	G	D	S	Tube	
10N40L-TF1-T	10N40G-TF1-T	TO-220F1	G	D	S	Tube	

S: Source Note: Pin Assignment: G: Gate D: Drain



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# ■ ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub>=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	400	V
Gate-Source Voltage		$V_{GSS}$	±30	V
Duraita Ocumanat	Continuous (T <sub>C</sub> =25°C)	I <sub>D</sub>	10.5	Α
Drain Current	Pulsed (Note 2)	I <sub>DM</sub>	42	Α
Avalanche Current (	Note 2)	I <sub>AR</sub>	11	Α
Avalancha Energy	Single Pulsed (Note 3)	E <sub>AS</sub>	360	mJ
Avalanche Energy	Repetitive (Note 4)	E <sub>AR</sub>	13.5	mJ
Peak Diode Recove	ry dv/dt (Note 4)	dv/dt	4.5	V/ns
TO-220			135	W
Power Dissipation	TO-220F1	5	44	W
TO-220		P <sub>D</sub>	1.07	W/°C
Derate above 25°C	TO-220F1		0.35	W/°C
Junction Temperatu	re	TJ	+150	°C
Storage Temperatur	е	T <sub>STG</sub>	-55~+150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature
- 3. L = 5.7mH,  $I_{AS}$  = 10.5A,  $V_{DD}$  = 50V,  $R_{G}$  = 25 $\Omega$ , Starting  $T_{J}$  = 25 $^{\circ}$ C
- 4.  $I_{SD} \le 10.5$ A, di/dt  $\le 200$ A/ $\mu$ s,  $V_{DD} \le BV_{DSS}$ , Starting  $T_J = 25$ °C

# ■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient		$\theta_{JA}$	62.5	°C/W	
lunation to Cook	TO-220	θ <sub>JC</sub>	0.93	°C/M	
Junction to Case	TO-220F1		2.86	°C/W	



# ■ **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub>=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TVD	MAX	LINIT			
OFF CHARACTERISTICS	STIVIBUL	TEST CONDITIONS	IVIIIN	ווף	IVIAX	UNIT			
	D\/	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V	400			V			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	•	400	0.54		V/°C			
Breakdown Voltage Temperature Coefficient		Reference to 25°C, I <sub>D</sub> =250µA		0.54	4				
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =400V, V <sub>GS</sub> =0V			1	μA			
Gate- Source Leakage Current Forward	$I_{GSS}$	$V_{GS}$ =+30V, $V_{DS}$ =0V			+100	nA			
Reverse   ON CHARACTERISTICS		V <sub>GS</sub> =-30V, V <sub>DS</sub> =0V			-100	nA			
Gate Threshold Voltage	V	\/ =\/	2.0		4.0	V			
Static Drain-Source On-State Resistance	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA		0.5	0.65	Ω			
DYNAMIC PARAMETERS	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =5.25A		0.5	0.03	12			
		<u> </u>		840	1090	pF			
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1.0MHz							
Output Capacitance	C <sub>OSS</sub>			250	325	pF			
Reverse Transfer Capacitance	C <sub>RSS</sub>			80	110	pF			
SWITCHING PARAMETERS									
Total Gate Charge	$Q_G$	V <sub>GS</sub> =10V, V <sub>DS</sub> =320V, I <sub>D</sub> =10.5A		28	35	nC			
Gate to Source Charge	$Q_GS$	(Note 1, 2)		4		nC			
Gate to Drain Charge	$Q_{GD}$	(11010-1, 2)		15		nC			
Turn-ON Delay Time	t <sub>D(ON)</sub>	$V_{DD}$ =200V, $I_{D}$ =10.5A, $R_{G}$ =25 $\Omega$ (Note 1, 2)		14	40	ns			
Rise Time	t <sub>R</sub>			89	190	ns			
Turn-OFF Delay Time	t <sub>D(OFF)</sub>			81	170	ns			
Fall-Time	$t_{F}$			81	170	ns			
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS									
Maximum Body-Diode Continuous Current	Is				10.5	Α			
Maximum Body-Diode Pulsed Current	I <sub>SM</sub>				42	Α			
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =10.5A, V <sub>GS</sub> =0V			1.4	V			
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>S</sub> =10.5A, V <sub>GS</sub> =0V,		290		ns			
Body Diode Reverse Recovery Charge	$Q_{RR}$	dl <sub>F</sub> /dt=100A/μs (Note 1)		2.4		μC			

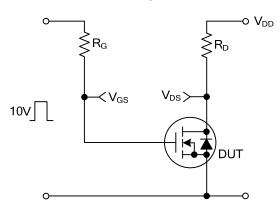
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%

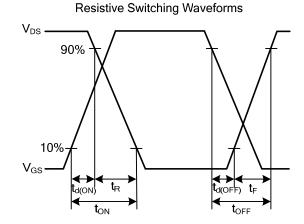


<sup>2.</sup> Essentially independent of operating temperature

# **■ TEST CIRCUITS AND WAVEFORMS**

Resistive Switching Test Circuit





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