

10N65K

Power MOSFET

10A, 650V N-CHANNEL POWER MOSFET

■ DESCRIPTION

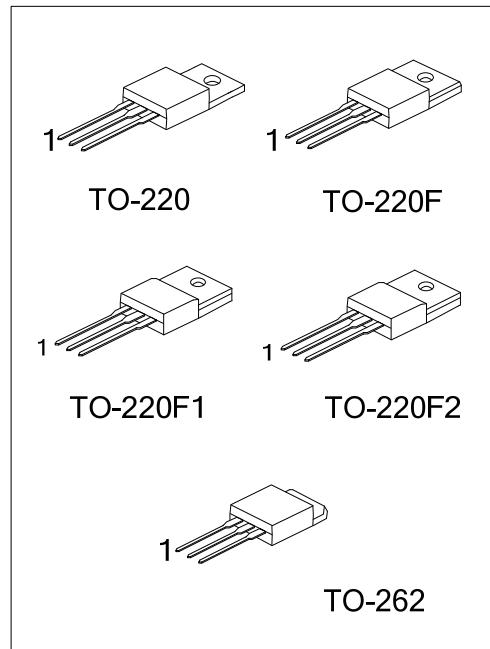
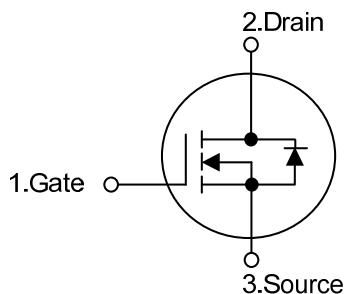
The UTC 10N65K is an N-channel Power MOSFET using UTC's advanced technology to provide customers a minimum on-state resistance and superior switching performance, etc.

The UTC 10N65K is generally applied in high efficient DC to DC converters, PWM motor controls and bridge circuits, etc.

■ FEATURES

- * $R_{DS(ON)} < 1.2\Omega$ @ $V_{GS} = 10V$
- * High Switching Speed
- * Improved dv/dt capability

■ SYMBOL



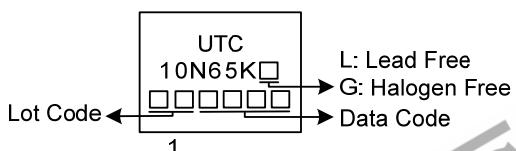
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
10N65KL-TA3-T	10N65KG-TA3-T	TO-220	G	D	S	Tube
10N65KL-TF3-T	10N65KG-TF3-T	TO-220F	G	D	S	Tube
10N65KL-TF1-T	10N65KG-TF1-T	TO-220F1	G	D	S	Tube
10N65KL-TF2-T	10N65KG-TF2-T	TO-220F2	G	D	S	Tube
10N65KL-T2Q-T	10N65KG-T2Q-T	TO-262	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

	(1) T: Tube (2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1, TF2: TO-220F2, T2Q: TO-262 (3) L: Lead Free, G: Halogen Free and Lead Free
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_c = 25^\circ\text{C}$ unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	650	V
Gate-Source Voltage		V_{GSS}	± 30	V
Avalanche Current (Note 2)		I_{AR}	10	A
Drain Current	Continuous	I_D	10	A
	Pulsed (Note 2)	I_{DM}	38	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	300	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220/TO-262	P_D	156	W
	TO-220F/TO-220F1		50	W
	TO-220F2		52	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Operating Temperature		T_{OPR}	-55 ~ +150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

3. $L=6\text{mH}$, $I_{AS}=10\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\ \Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 9.5\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient		θ_{JA}	65.2	$^\circ\text{C/W}$
Junction to Case	TO-220/TO-262	θ_{JC}	0.8	$^\circ\text{C/W}$
	TO-220F/TO-220F1		2.5	$^\circ\text{C/W}$
	TO-220F2		2.4	$^\circ\text{C/W}$

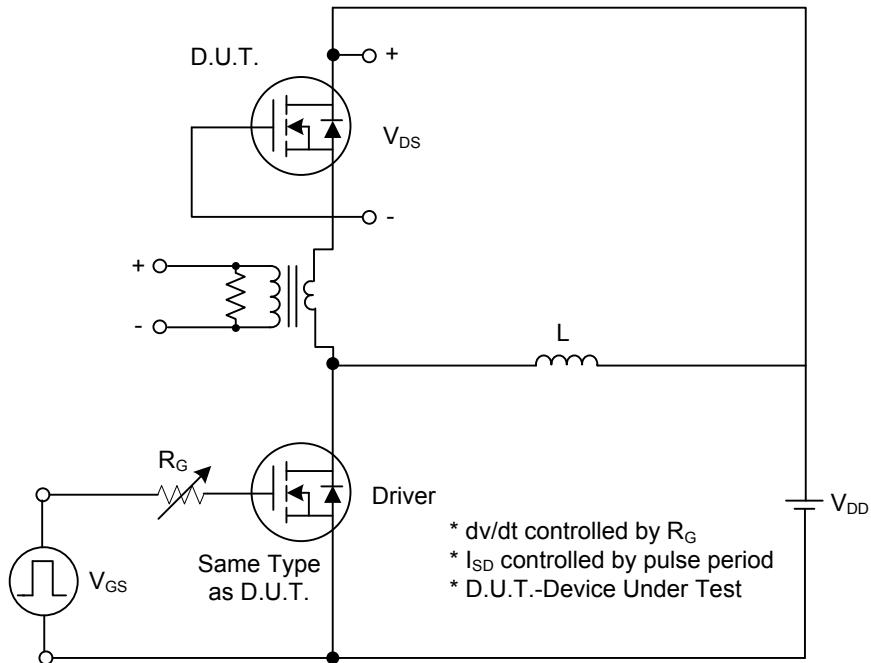
■ ELECTRICAL CHARACTERISTICS($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 250\mu\text{A}$	650			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}} = 650\text{V}, V_{\text{GS}} = 0\text{V}$		1		μA
Gate-Source Leakage Current	Forward	$V_{\text{GS}} = 30\text{ V}, V_{\text{DS}} = 0\text{ V}$		100		nA
	Reverse	$V_{\text{GS}} = -30\text{ V}, V_{\text{DS}} = 0\text{ V}$		-100		nA
Breakdown Voltage Temperature Coefficient	$\Delta\text{BV}_{\text{DSS}}/\Delta T_J$	$I_{\text{D}}=250\mu\text{A}$, Referenced to 25°C		0.7		$\text{V}/^\circ\text{C}$
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS}(\text{TH})}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250\mu\text{A}$	2.5		4.5	V
Static Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 10\text{V}, I_{\text{D}} = 5\text{A}$	0.5	0.89	1.2	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{ MHz}$		1200	1700	pF
Output Capacitance	C_{OSS}			110	160	pF
Reverse Transfer Capacitance	C_{RSS}			10.5	16	pF
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	$t_{\text{D}(\text{ON})}$	$V_{\text{DD}}=30\text{V}, I_{\text{D}}=0.5\text{A}, R_{\text{G}}=25\Omega$ (Note 1, 2)		75		ns
Turn-On Rise Time	t_{R}			60		ns
Turn-Off Delay Time	$t_{\text{D}(\text{OFF})}$			180		ns
Turn-Off Fall Time	t_{F}			65		ns
Total Gate Charge	Q_{G}	$V_{\text{DS}}=50\text{V}, I_{\text{D}}=1.3\text{A}, V_{\text{GS}}=10\text{ V}$ (Note 1, 2)		31.3	42	nC
Gate-Source Charge	Q_{GS}			9.3		nC
Gate-Drain Charge	Q_{GD}			6.8		nC
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Drain-Source Diode Forward Voltage	V_{SD}	$V_{\text{GS}} = 0\text{ V}, I_{\text{S}} = 10\text{A}$			1.4	V
Maximum Continuous Drain-Source Diode Forward Current	I_{S}				10	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}				38	A

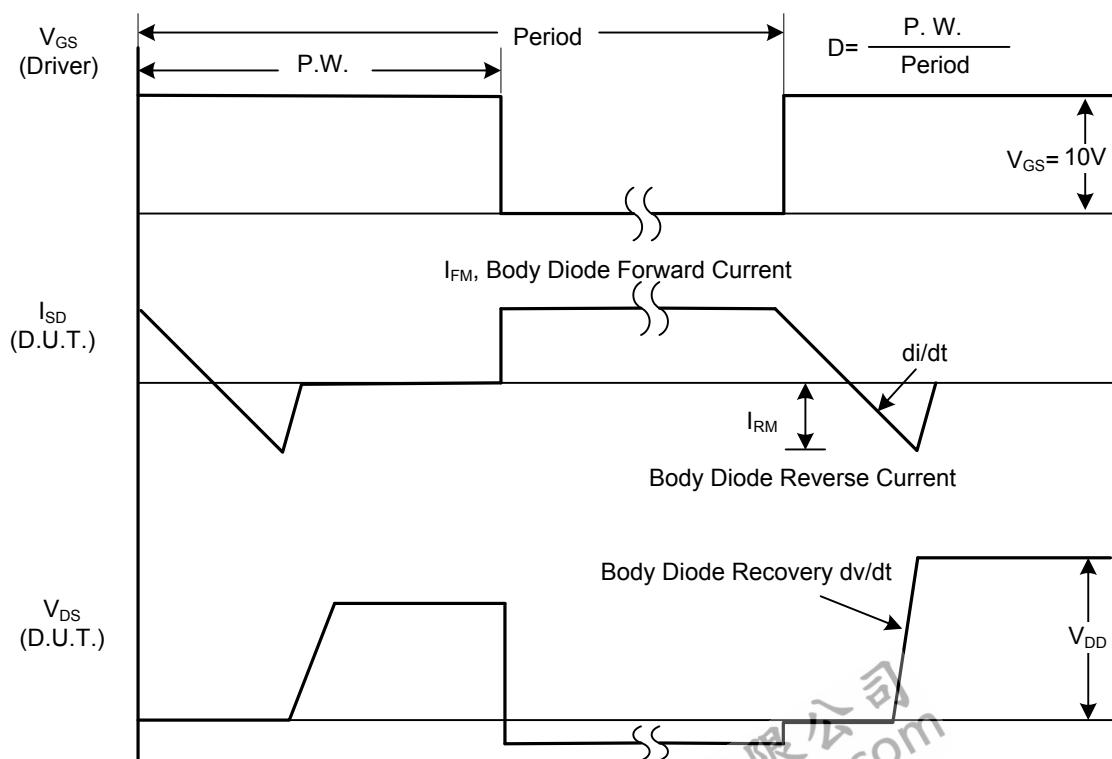
Note: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

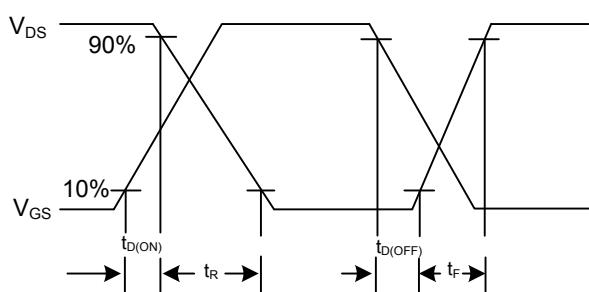
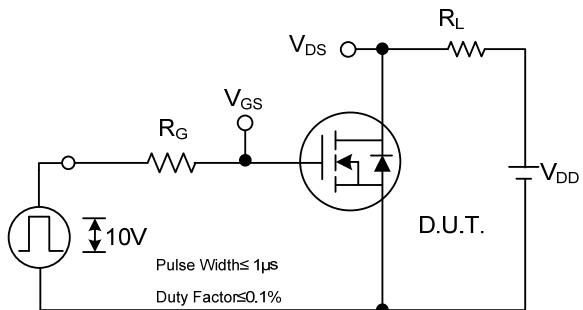


Peak Diode Recovery dv/dt Test Circuit



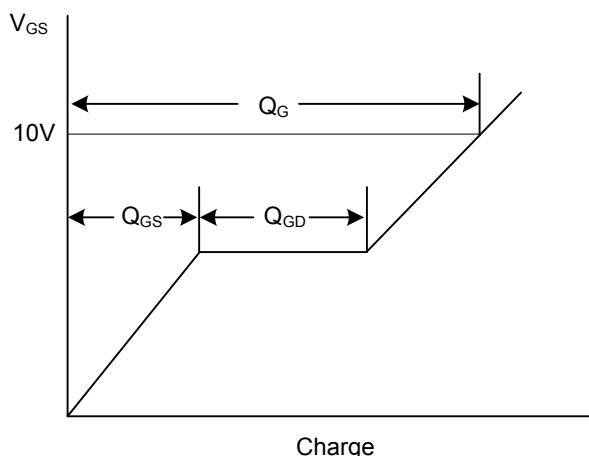
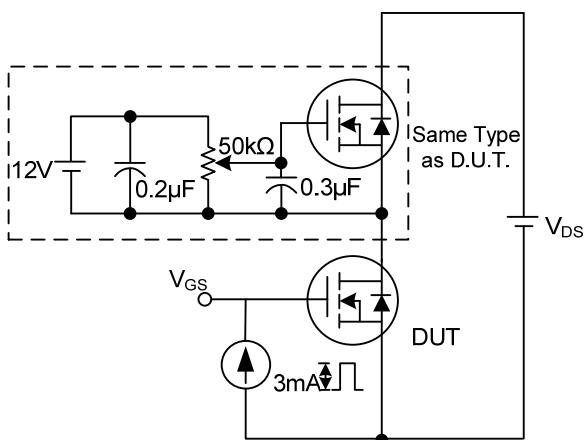
Peak Diode Recovery dv/dt Waveforms

■ TEST CIRCUITS AND WAVEFORMS (Cont.)



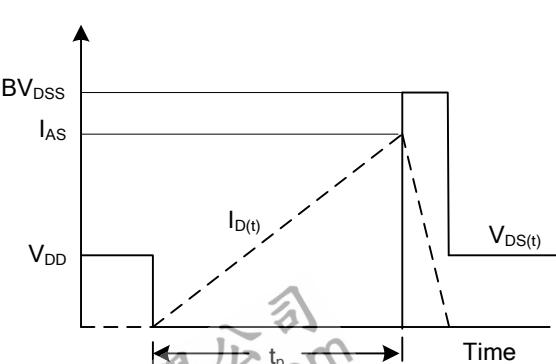
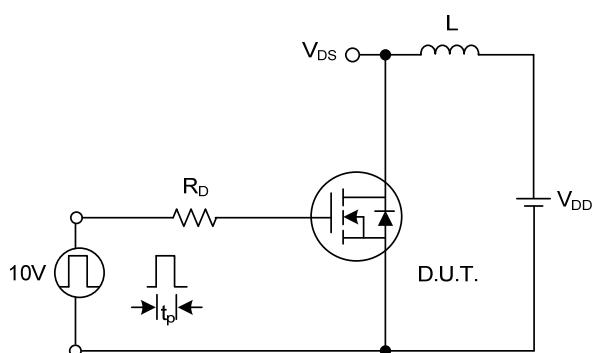
Switching Test Circuit

Switching Waveforms



Gate Charge Test Circuit

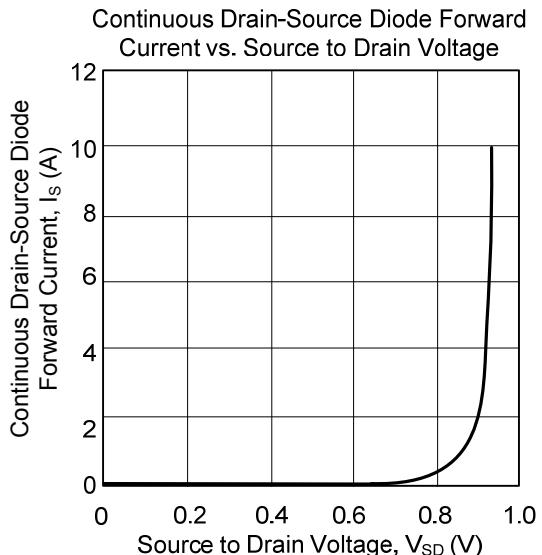
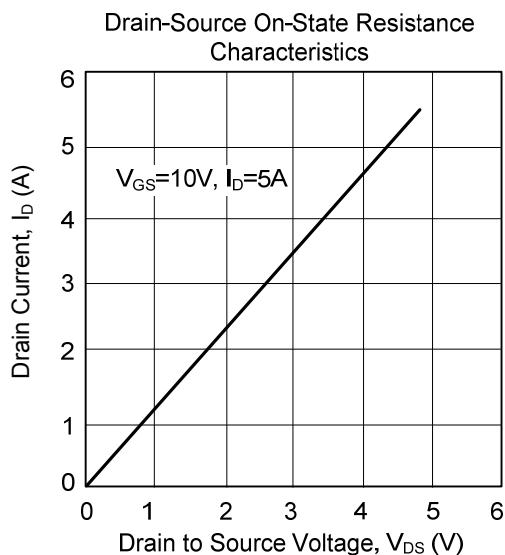
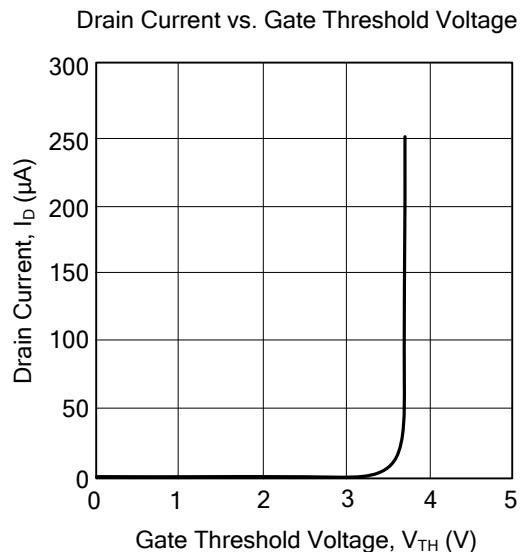
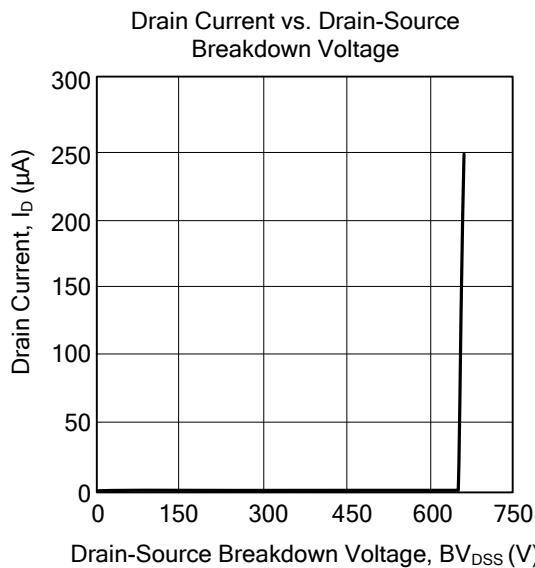
Gate Charge Waveform



Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS



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