

UNISONIC TECHNOLOGIES CO., LTD

11NM50

Preliminary

11A, 500V N-CHANNEL SUPER-JUNCTION MOSFET

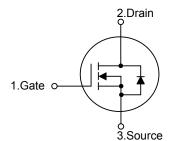
DESCRIPTION

The UTC 11NM50 is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at DC-DC, AC-DC converters for power applications.

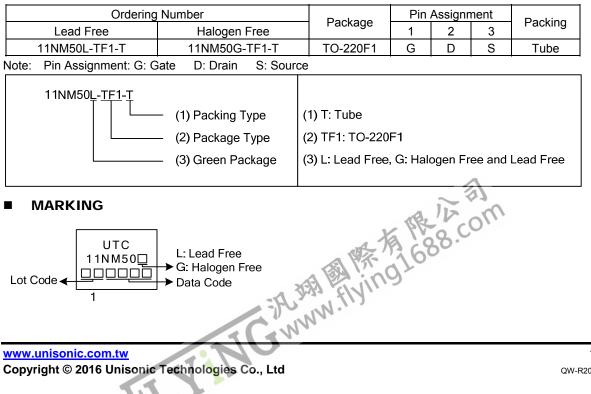
FEATURES

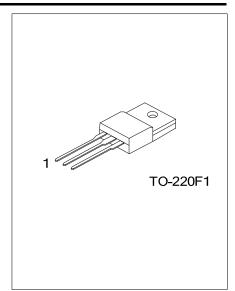
- * R_{DS(ON)} < 0.32Ω @ V_{GS}=10V, I_D=5.5A
- * High switching Speed
- * 100% avalanche tested
- * Improved dv/dt capability

SYMBOL









■ ABSOLUTE MAXIMUM RATINGS (T_c=25°C, unless otherwise specified)

PARA	METER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	500	V
Gate-Source Voltage		V _{GSS}	±30	V
Drain Current	Continuous	I _D	11	А
	Pulsed (Note 2)	I _{DM}	44	А
Avalanche Current (Note 2)		I _{AR}	6.5	А
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	297	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	5.2	V/ns
Power Dissipation		PD	40	W
Junction Temperature		ΤJ	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 22mH, I_{AS} = 5.2A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C

4. $I_{SD} \le 11A$, di/dt $\le 200A/\mu$ s, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL RESISTANCES CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ _{JA}	62.5	°C/W	
Junction to Case	θις	3.1	°C/W	

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

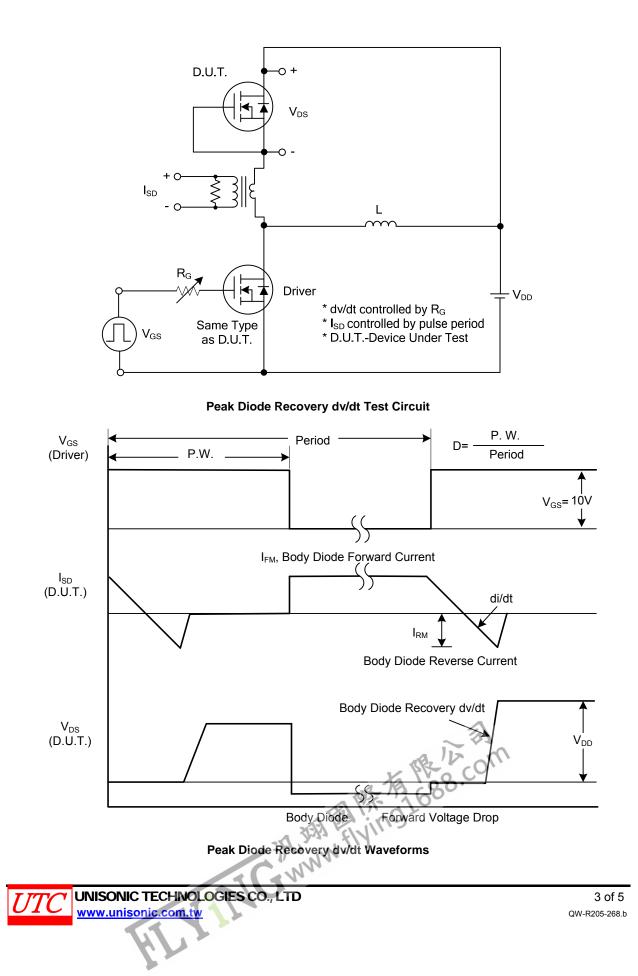
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PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS				=		1	<u>۲</u>
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250µA	500			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =500V, V _{GS} =0V			10	μA
Gate-Source Leakage Current	Forward	- I _{GSS}	V _{DS} =0V ,V _{GS} =30V			+100	nA
	Reverse		V _{DS} =0V ,V _{GS} =-30V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} = V _{GS} , I _D =250µA	2.5		4.5	V
Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =5.5A			0.32	Ω
DYNAMIC PARAMETERS							
Input Capacitance	put Capacitance				755		рF
Output Capacitance		C _{ISS} C _{OSS}	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		630		рF
Reverse Transfer Capacitance		C _{RSS}			62		рF
SWITCHING PARAMETERS							
Total Gate Charge (Note 1)		Q_{G}	V _{DS} =50V, V _{GS} =10V, I _D =1.3A,		75		nC
Gate to Source Charge		Q_{GS}	l _G =100μA (Note 1, 2)		5.5		nC
Gate to Drain Charge		Q_{GD}	$IG = 100 \mu A (100 E T, Z)$		23.5		nC
Turn-ON Delay Time (Note 1)		t _{D(ON)}			58		ns
Rise Time		t _R	V_{DD} =30V, V_{GS} =10V, I_{D} =0.5A,		139		ns
Turn-OFF Delay Time		t _{D(OFF)}	R _G =25Ω (Note 1, 2)		218		ns
Fall-Time		t⊧			174		ns
SOURCE- DRAIN DIODE RATIN	IGS AND CH	ARACTERIS		\mathcal{O}			
Maximum Body-Diode Pulsed Current		ls	K Pro CO			11	Α
Drain-Source Diode Forward Voltage (Note 1)		I _{SM}	1 A 680.			44	Α
Maximum Body-Diode Continuous Current		V _{SD}	ls =11A, V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time (Note 1)		tr	I _S =11A, V _{GS} =0V,		330		ns
Body Diode Reverse Recovery Charge		Qrt	dl _F /dt=100A/µs		4.4		μC
Notes: 1. Pulse Test : Pulse widtl	n ≤ 300µs, Dut	ty cycle $\leq 2\%$	N .				

2. Essentially independent of operating temperature.

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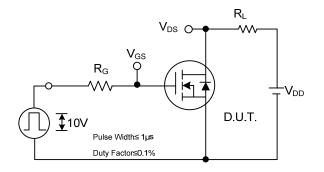


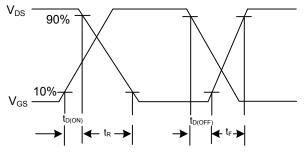
■ TEST CIRCUITS AND WAVEFORMS



11NM50

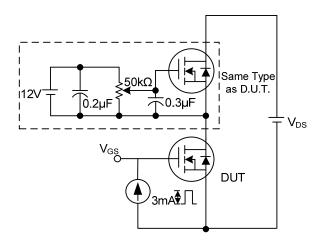
TEST CIRCUITS AND WAVEFORMS (Cont.)



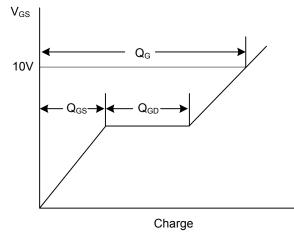


Switching Test Circuit

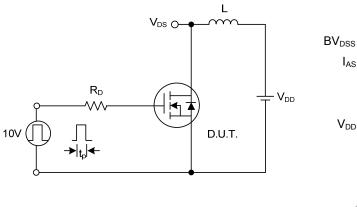


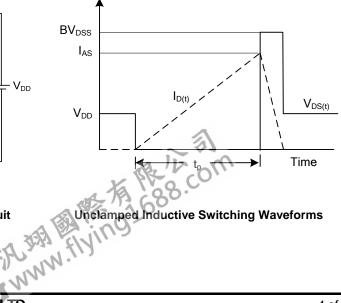


Gate Charge Test Circuit



Gate Charge Waveform







Unclamped Inductive Switching Test Circuit

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