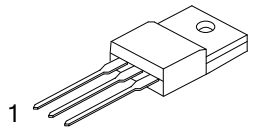
12A, 800V N-CHANNEL POWER MOSFET



TO-220F2

■ DESCRIPTION

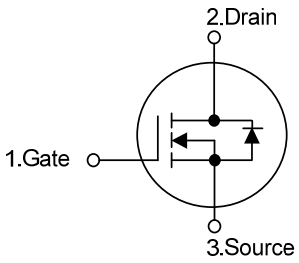
The UTC **12N80-C** is a N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology allows a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

■ FEATURES

* $R_{DS(ON)}$ < 1.0Ω @ V_{GS}=10V, I_D=6.0A
* Fast switching
* 100% avalanche tested
* Improved dv/dt capability

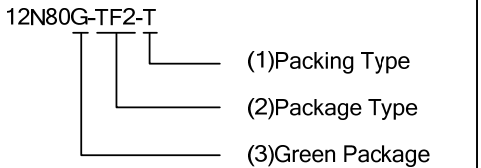
■ SYMBOL



■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
12N80L-TF2-T	12N80G-TF2-T	TO-220F2	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

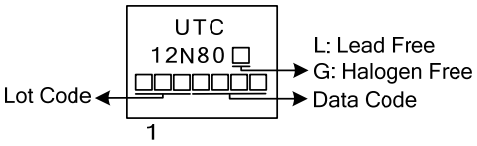


(1) T: Tube

(2) TF2: TO-220F2

(3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	800	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous	I_D	12	A
	Pulsed (Note 2)	I_{DM}	36	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	864	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	1.5	V/ns
Power Dissipation		P_D	66	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. L = 12mH, I_{AS} = 12A, V_{DD} = 50V, R_G = 25 Ω Starting T_J = 25°C
4. I_{SD} ≤ 12A, di/dt ≤200A/μs, V_{DD} ≤BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

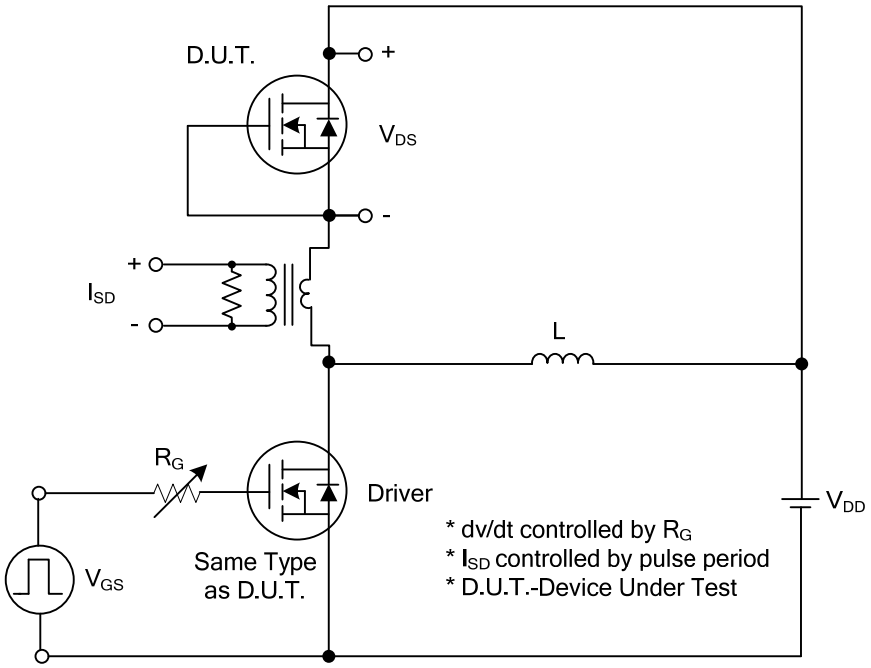
PARAMETER	SYMBOL	RATING	UNIT
Junction to Ambient	θ_{JA}	62.5	°C/W
Junction to Case	θ_{JC}	1.89	°C/W

■ ELECTRICAL CHARACTERISTICS(T_J =25°C, unless otherwise specified)

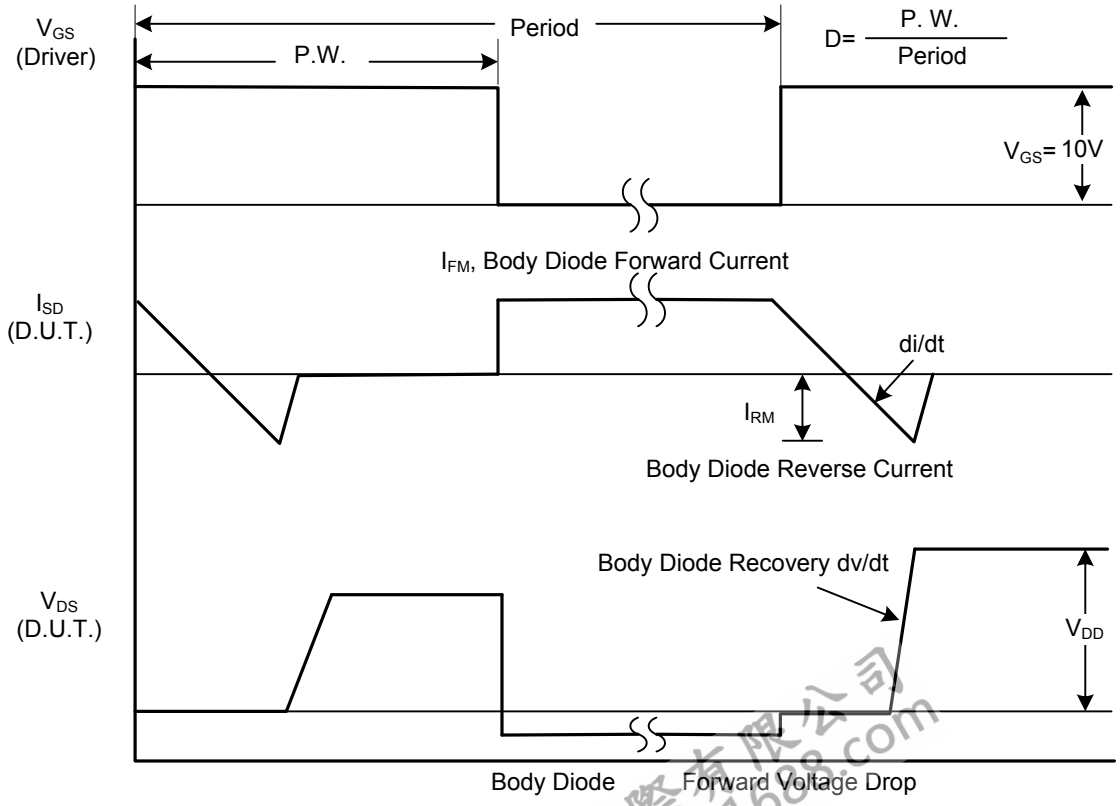
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	V_{GS}=0V, I_D= 250μA	800			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=800V, V_{GS}=0V			1	μA
Gate-Source Leakage Current	Forward	I_{GSS}	V_{GS}=30V, V_{DS}=0V			100	nA
	Reverse		V_{GS}=-30V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=6.0A			1.0	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C_{ISS}	V_{GS}=0V, V_{DS}=25V, f=1.0 MHz		1900		pF
Output Capacitance		C_{OSS}			300		pF
Reverse Transfer Capacitance		C_{RSS}			55		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge (Note 1)		Q_G	V_{DS}=400V, V_{GS}=10V, I_D=12A , I_G=1mA (Note 1, 2)		78		nC
Gate to Source Charge		Q_{GS}			24		nC
Gate to Drain Charge		Q_{GD}			32		nC
Turn-ON Delay Time (Note 1)		$t_{D(ON)}$	V_{DD}=300V, V_{GS}=10V, I_D=12A, R_G=25Ω (Note 1, 2)		28		ns
Rise Time		t_R			36		ns
Turn-OFF Delay Time		$t_{D(OFF)}$			188		ns
Fall-Time		t_F			48		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I_S				12	A
Maximum Body-Diode Pulsed Current		I_{SM}				36	A
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I_S=12A, V_{GS}=0V			1.4	V
Body Diode Reverse Recovery Time (Note 1)		t_{rr}	I_S=12A, V_{GS}=0V, dI_F/dt =100A/μs		700		ns
Body Diode Reverse Recovery Charge		Q_{rr}			10.4		μC

Notes: 1. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

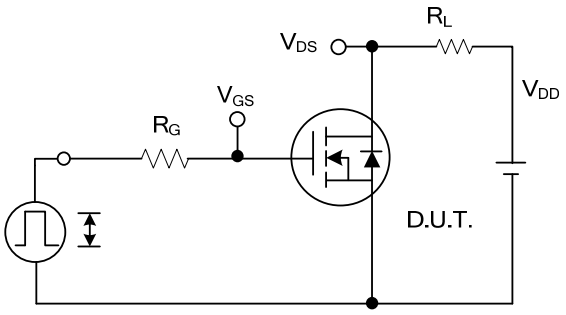


Peak Diode Recovery dv/dt Test Circuit

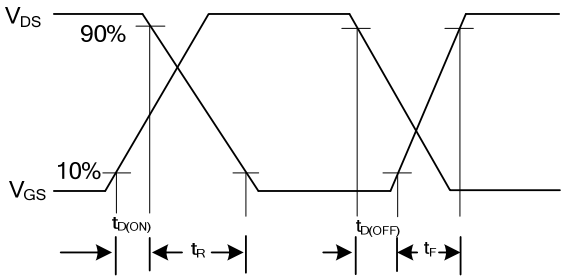


Peak Diode Recovery dv/dt Waveforms

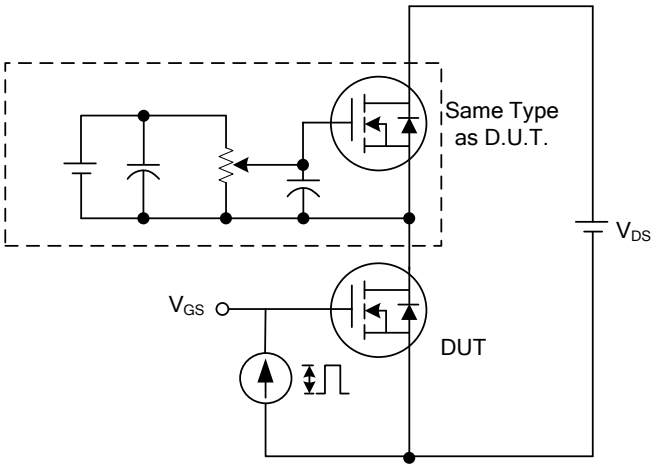
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



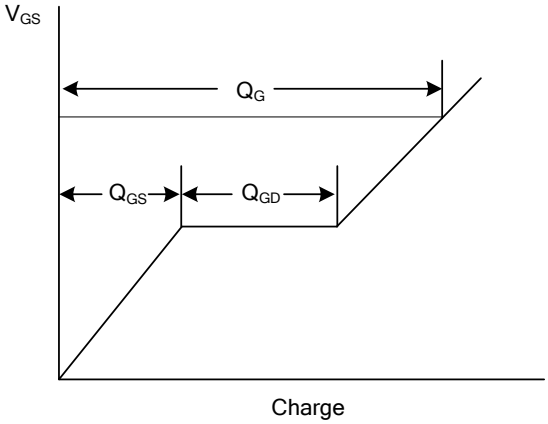
Switching Test Circuit



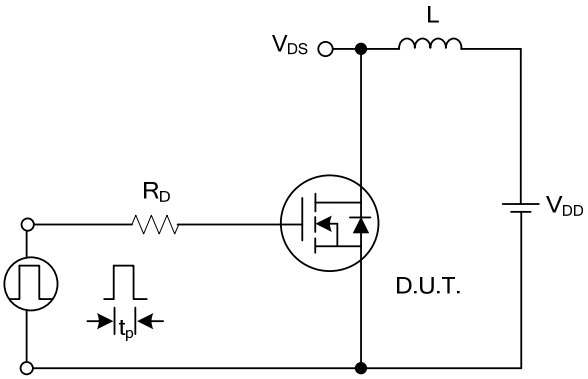
Switching Waveforms



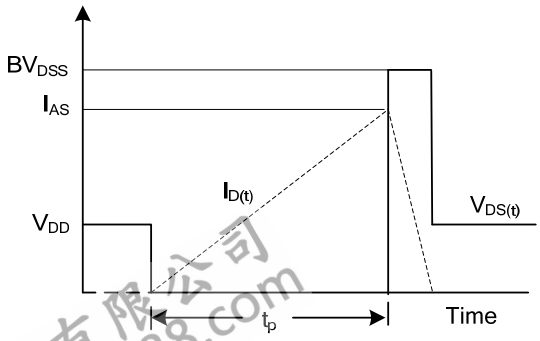
Gate Charge Test Circuit



Gate Charge Waveform



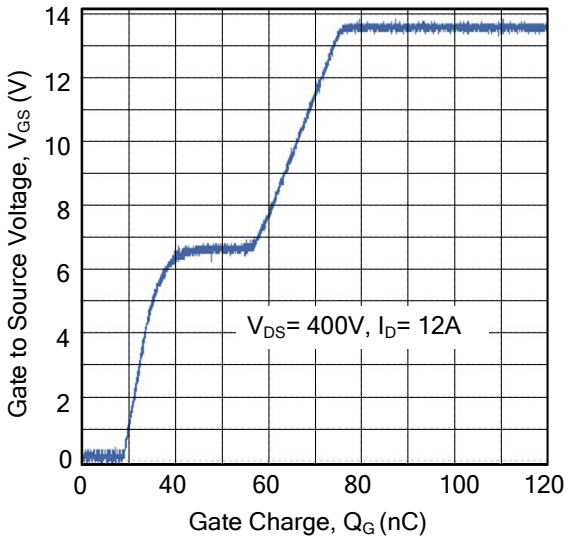
Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS

Gate Charge vs. Gate to Source Voltage



Capacitance Characteristics