



## 15N10

Power MOSFET

### 14.7A, 100V (D-S) N-CHANNEL POWER MOSFET

#### DESCRIPTION

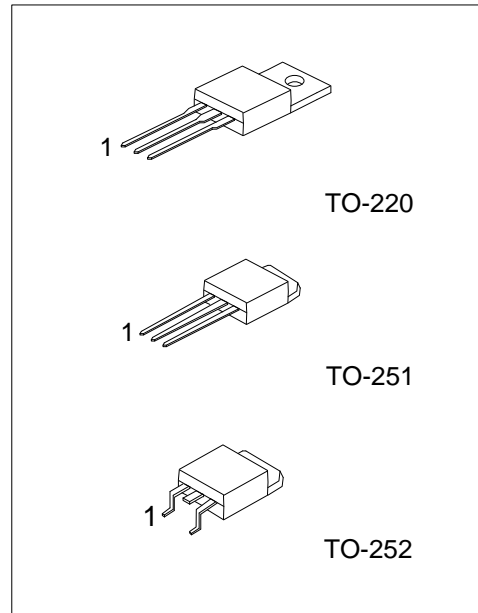
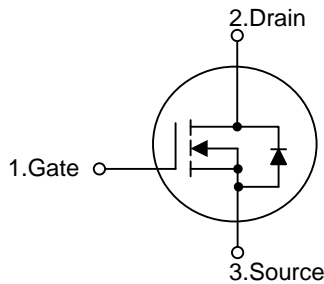
The UTC 15N10 is an N-Channel enhancement MOSFET, it uses UTC's advanced technology to provide customers with a minimum on-state resistance, high switching speed and low gate charge.

The UTC 15N10 is suitable for high efficiency switching DC/DC converter, LCD display inverter and load switch.

#### FEATURES

- \*  $R_{DS(ON)} \leq 100 \text{ m}\Omega @ V_{GS}=10\text{V}, I_D=8.0\text{A}$
- \*  $R_{DS(ON)} \leq 110 \text{ m}\Omega @ V_{GS}=4.5\text{V}, I_D=8.0\text{A}$
- \* High switching speed

#### SYMBOL



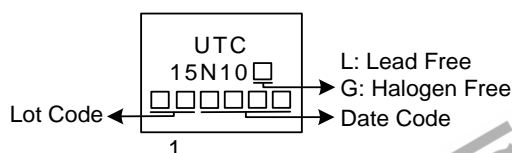
#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
15N10L-TA3-T	15N10G-TA3-T	TO-220	G	D	S	Tube
15N10L-TM3-T	15N10G-TM3-T	TO-251	G	D	S	Tube
15N10L-TN3-R	15N10G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>15N10G-TA3-T</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Green Package</p>	<p>(1) T: Tube, R: Tape Reel</p> <p>(2) TA3: TO-220, TM3: TO-251, TN3: TO-252</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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#### MARKING



### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER			SYMBOL	RATINGS	UNIT
Drain-Source Voltage			V <sub>DSS</sub>	100	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Drain Current	Continuous	T <sub>C</sub> =25°C	I <sub>D</sub>	14.7	A
		T <sub>C</sub> =70°C		13.6	A
	Pulsed		I <sub>DM</sub>	30	A
Power Dissipation	T <sub>C</sub> =25°C	TO-220	P <sub>D</sub>	60	W
		TO-251		34.7	W
		TO-252			
Operating Junction Temperature			T <sub>J</sub>	-55 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### ■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220	θ <sub>JA</sub>	62.5	°C/W
	TO-251/TO-252		110	°C/W
Junction to Case (Note)	TO-220	θ <sub>JC</sub>	2.5	°C/W
	TO-251/TO-252		3.6 (Note)	°C/W

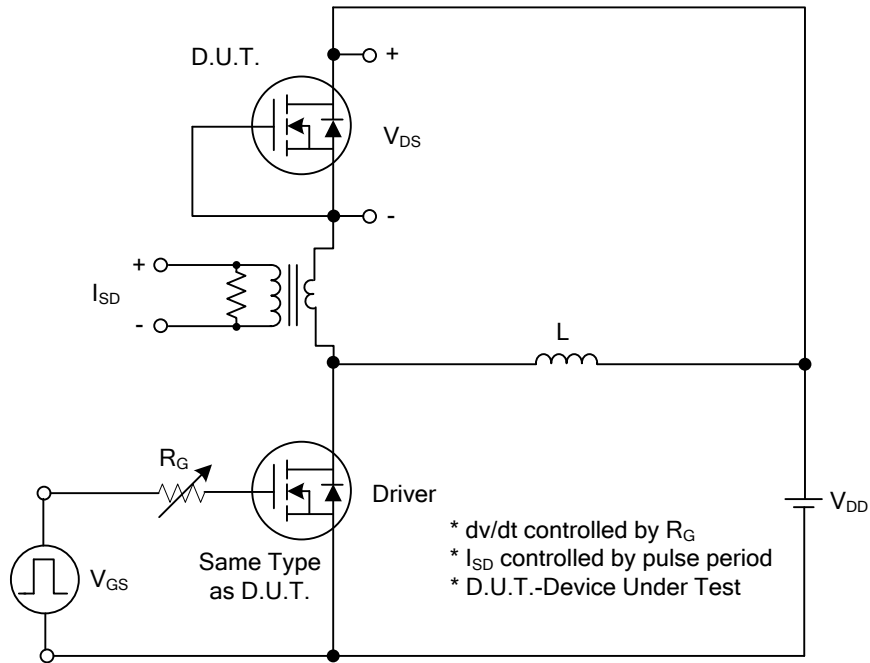
Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C, unless otherwise specified)

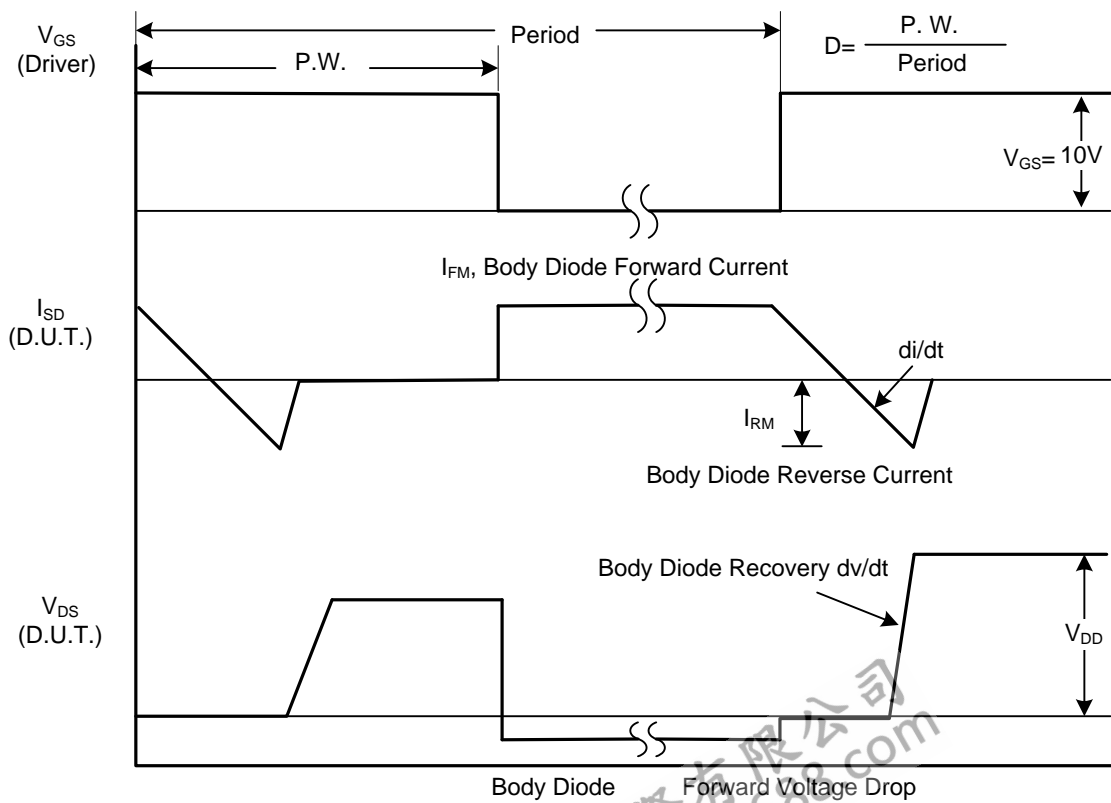
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V	100			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V			1	μA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =+20V, V <sub>DS</sub> =0V			+100	nA
		V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V			-100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0		3.0	V
Drain-Source On-State Resistance (Note)	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =8.0A			100	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =8.0A			110	mΩ
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =15V, f=1MHz		1411		pF
Output Capacitance	C <sub>OSS</sub>			78		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			65		pF
Gate-Resistance	R <sub>G</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz		0.81		Ω
<b>SWITCHING PARAMETERS</b>						
Total Gate Charge	Q <sub>G</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =80V, I <sub>D</sub> =15A		34		nC
Total Gate Charge	Q <sub>G</sub>	V <sub>GS</sub> =4.5V, V <sub>DS</sub> =80V, I <sub>D</sub> =15A		18.4		nC
Gate to Source Charge	Q <sub>GS</sub>			5.5		nC
Gate to Drain Charge	Q <sub>GD</sub>			8		nC
Turn-ON Delay Time	t <sub>D(ON)</sub>			13		ns
Rise Time	t <sub>R</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =15A, R <sub>G</sub> =25Ω		22		ns
Turn-OFF Delay Time	t <sub>D(OFF)</sub>			100		ns
Fall-Time	t <sub>F</sub>			43		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =8A, V <sub>GS</sub> =0V		0.9	1.2	V

Note: Pulse test: pulse width ≤ 300us, duty cycle ≤ 2%, Guaranteed by design, not subject to production testing.

## TEST CIRCUITS AND WAVEFORMS

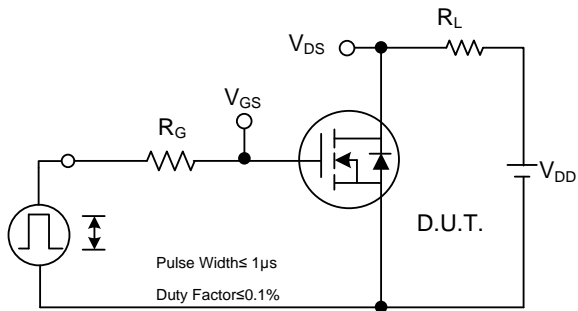


Peak Diode Recovery  $dv/dt$  Test Circuit

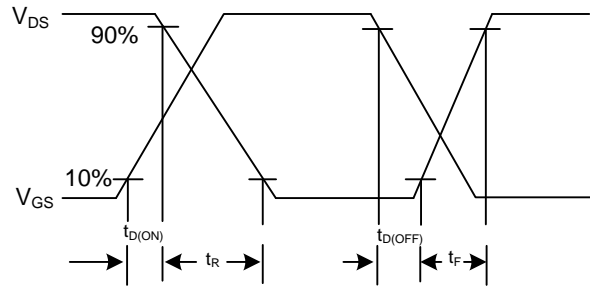


Peak Diode Recovery  $dv/dt$  Waveforms

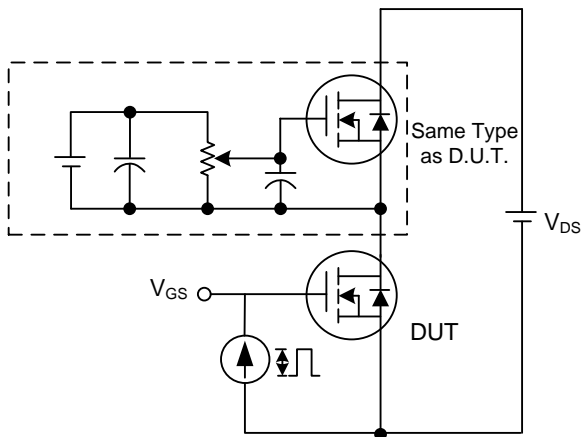
## TEST CIRCUITS AND WAVEFORMS



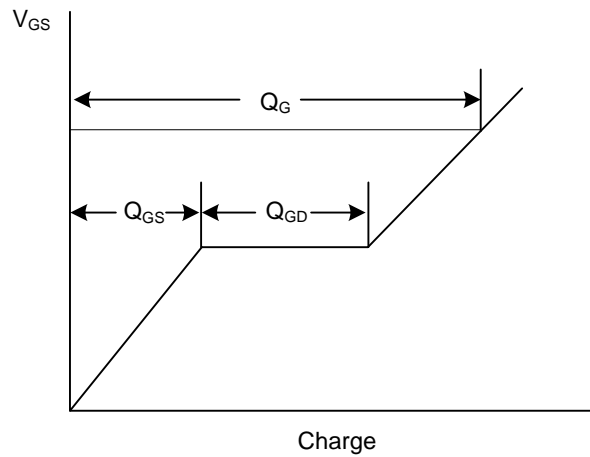
Switching Test Circuit



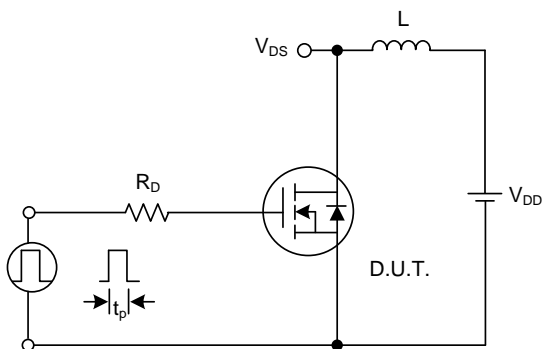
Switching Waveforms



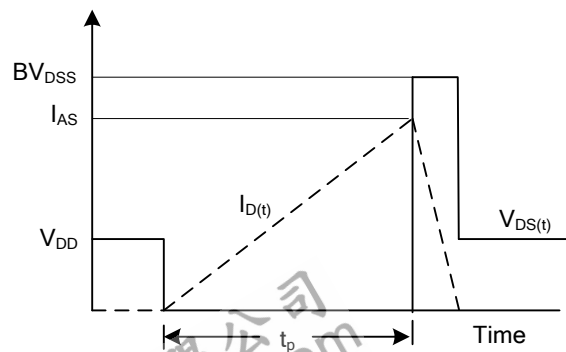
Gate Charge Test Circuit



Gate Charge Waveform

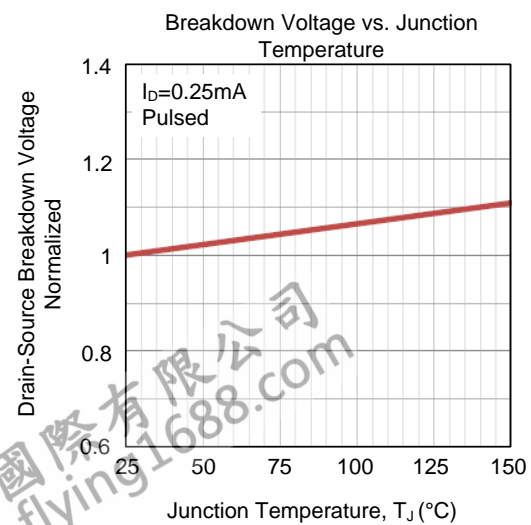
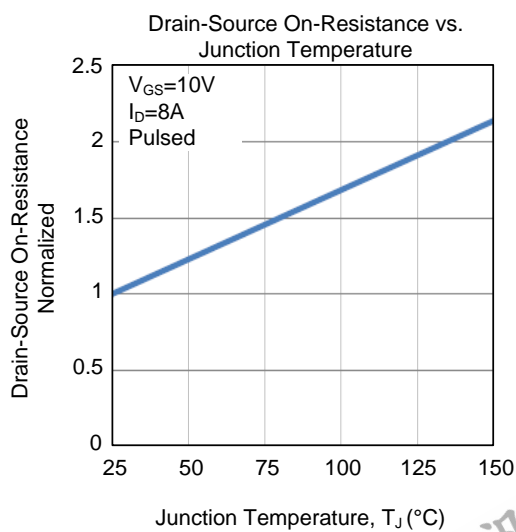
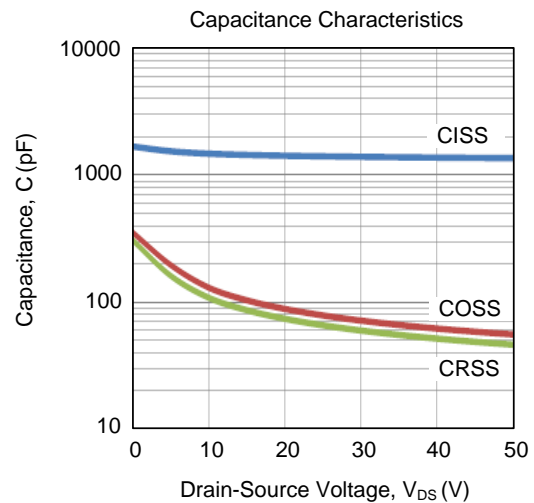
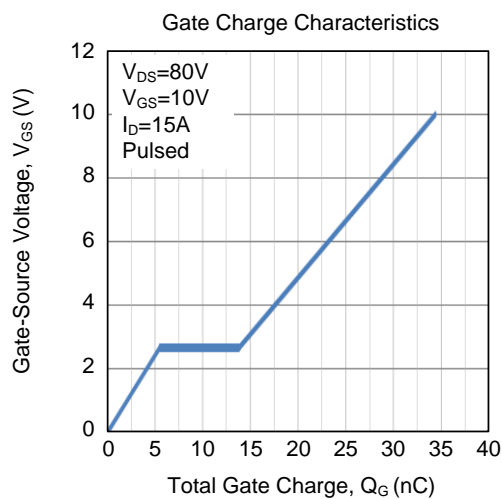
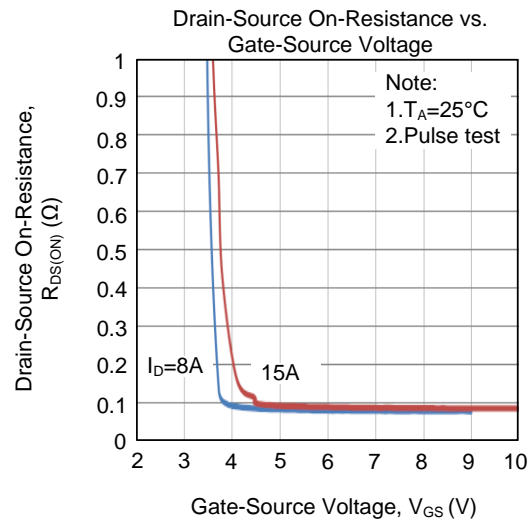
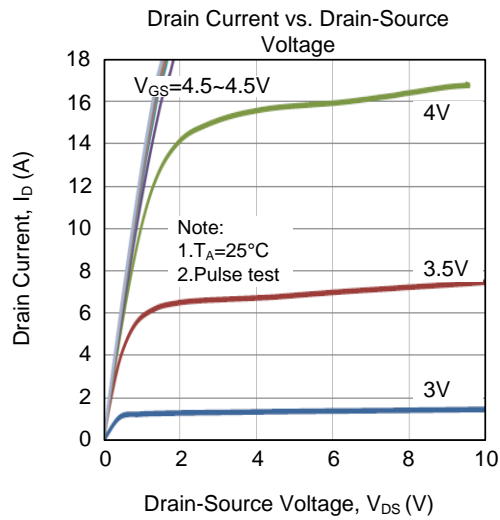


Unclamped Inductive Switching Test Circuit

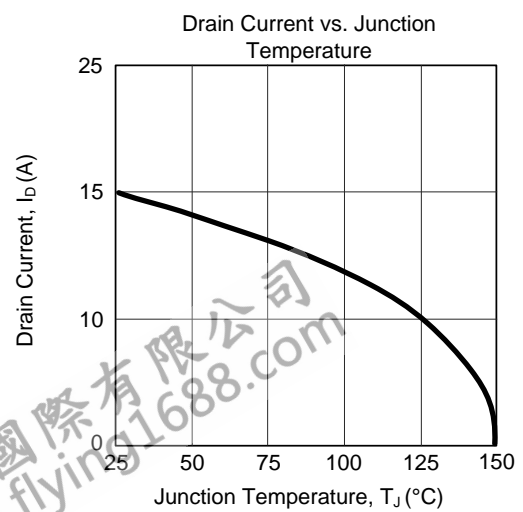
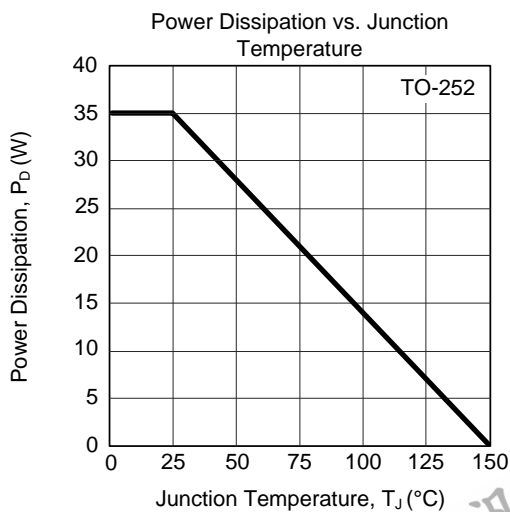
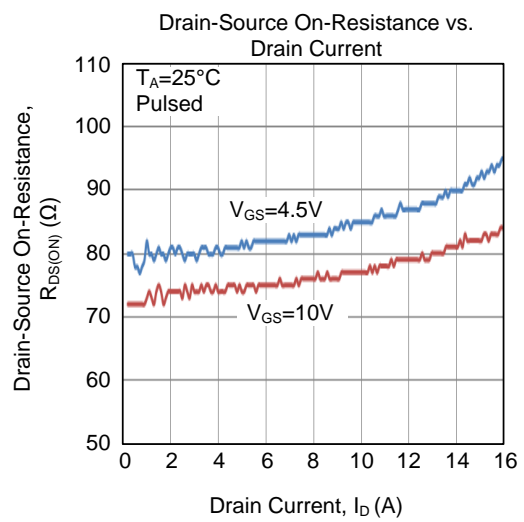
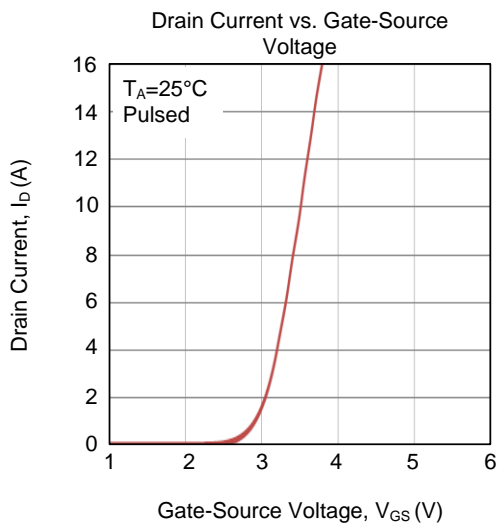
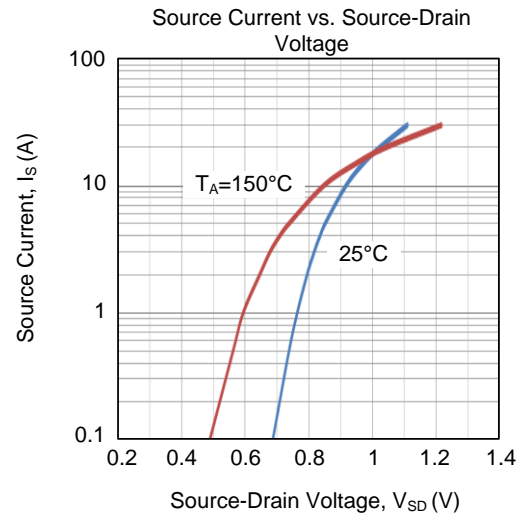
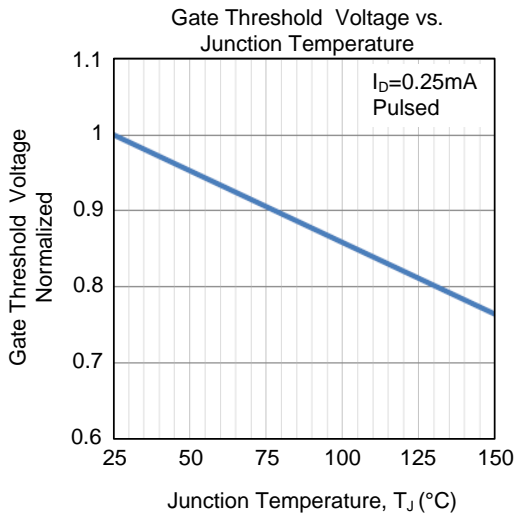


Unclamped Inductive Switching Waveforms

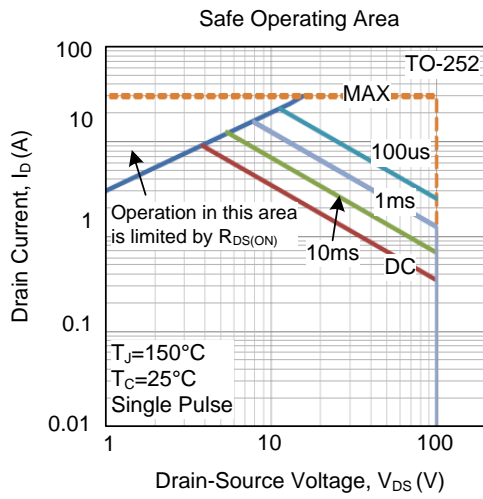
## ■ TYPICAL CHARACTERISTICS



## ■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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