



18N50-MH

Preliminary

Power MOSFET

18A, 500V N-CHANNEL POWER MOSFET

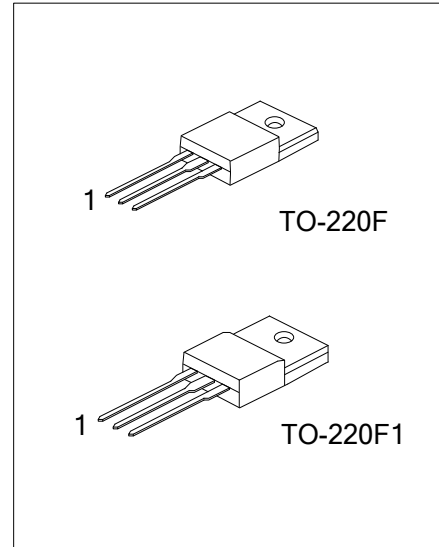
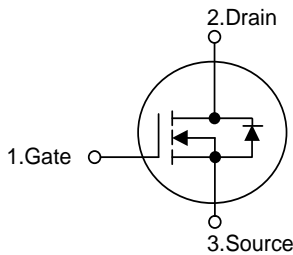
DESCRIPTION

The UTC 18N50-MH is a high voltage power MOSFET combines advanced trench MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

FEATURES

- * $R_{DS(ON)} \leq 0.3 \Omega @ V_{GS}=10V, I_D=9.0A$
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness

SYMBOL



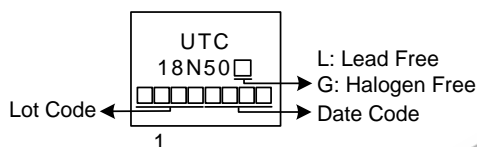
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
18N50L-TF1-T	18N50G-TF1-T	TO-220F1	G	D	S	Tube
18N50L-TF3-T	18N50G-TF3-T	TO-220F	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>18N50G-TF1-T</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Green Package</p>	<p>(1) T: Tube</p> <p>(2) TF1: TO-220F1, TF3: TO-220F</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
---	--

MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V _{DSS}	500	V
Gate-Source Voltage	V _{GSS}	±30	V
Continuous Drain Current	I _D	18	A
Pulsed Drain Current (Note 2)	I _{DM}	36	A
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	1380
Peak Diode Recovery dv/dt (Note 4)			
Power Dissipation	P _D	44	W
Junction Temperature	T _J	+150	°C
Storage Temperature	T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 30mH, I_{AS} = 9.6A, V_{DD} = 100V, R_G = 25 Ω, Starting T_J = 25°C

4. I_{SD} ≤ 9.0A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Ambient	θ _{JA}	62.5	°C/W
Junction to Case	θ _{JC}	2.84	°C/W

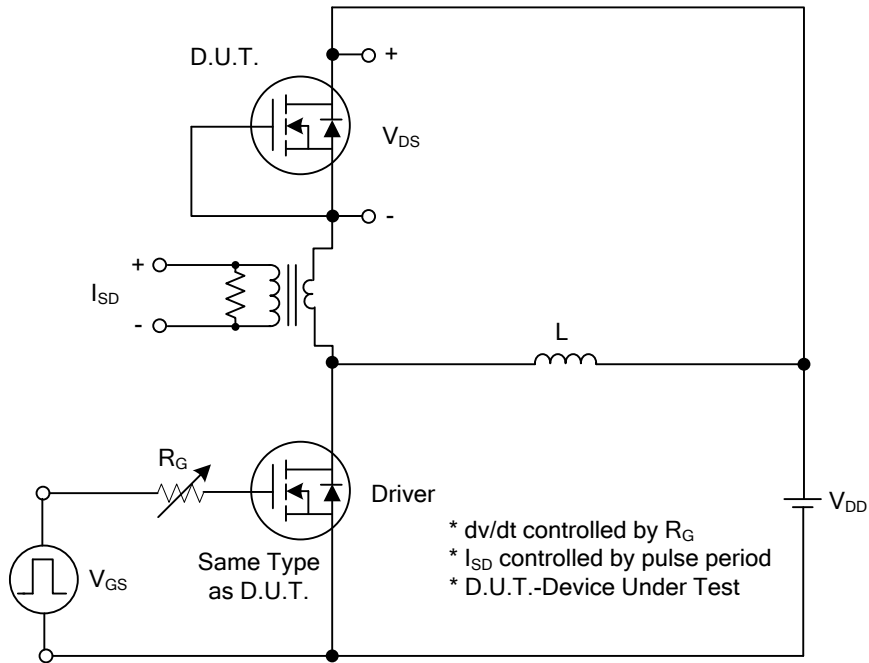
■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	500			V	
Drain-Source Leakage Current	I _{DSS}	V _{DS} =500V, V _{GS} =0V			10	μA	
Gate- Source Leakage Current	Forward	I _{GSS}			100	nA	
	Reverse						V _{GS} =30V, V _{DS} =0V
		V _{GS} =-30V, V _{DS} =0V			-100	nA	
ON CHARACTERISTICS							
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.0		4.0	V	
Static Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =9.0A			0.3	Ω	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C _{ISS}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz		2860		pF	
Output Capacitance	C _{OSS}				305		pF
Reverse Transfer Capacitance	C _{RSS}				40		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge (Note 1)	Q _G	V _{DS} =480V, V _{GS} =10V, I _D =18A I _G =1mA (Note 1, 2)		76		nC	
Gate-Source Charge	Q _{GS}				13		nC
Gate-Drain Charge	Q _{GD}				28		nC
Turn-On Delay Time (Note 1)	t _{D(ON)}	V _{DS} =100V, V _{GS} =10V, I _D =18A, R _G =25Ω (Note 1, 2)		38		ns	
Turn-On Rise Time	t _R				43		ns
Turn-Off Delay Time	t _{D(OFF)}				236		ns
Turn-Off Fall Time	t _F				54		ns
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS							
Maximum Body-Diode Continuous Current	I _S				18	A	
Maximum Body-Diode Pulsed Current	I _{SM}				36	A	
Drain-Source Diode Forward Voltage (Note 1)	V _{SD}	I _S =18A, V _{GS} =0V			1.4	V	
Reverse Recovery Time (Note 1)	t _{rr}	I _S =18A, V _{GS} =0V		400		ns	
Reverse Recovery Charge	Q _{rr}	di/dt=100A/μs		12.5		μC	

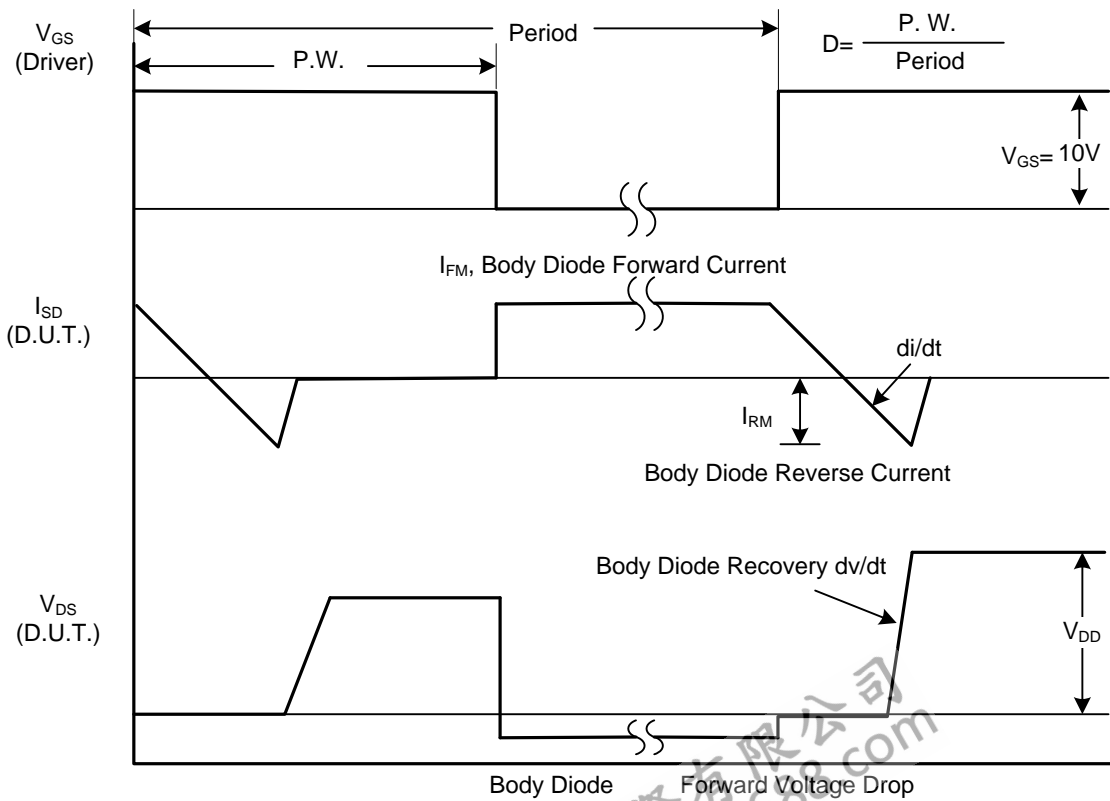
Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

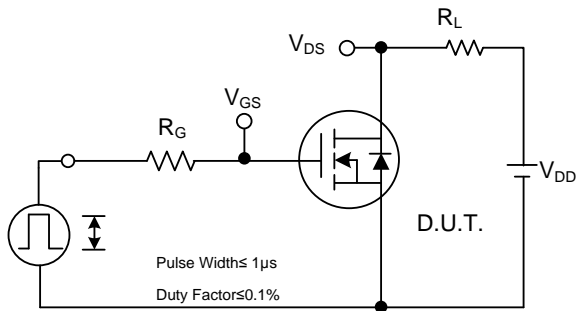


Peak Diode Recovery dv/dt Test Circuit

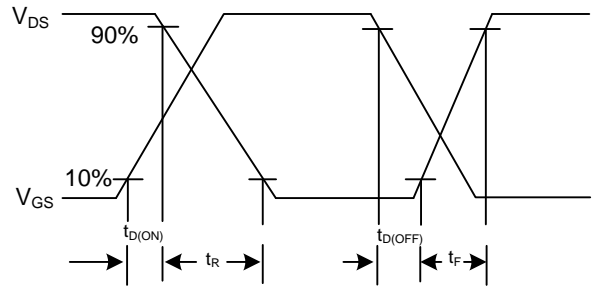


Peak Diode Recovery dv/dt Waveforms

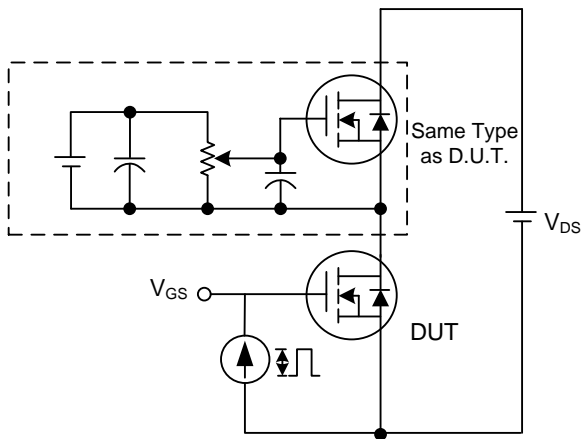
■ TEST CIRCUITS AND WAVEFORMS



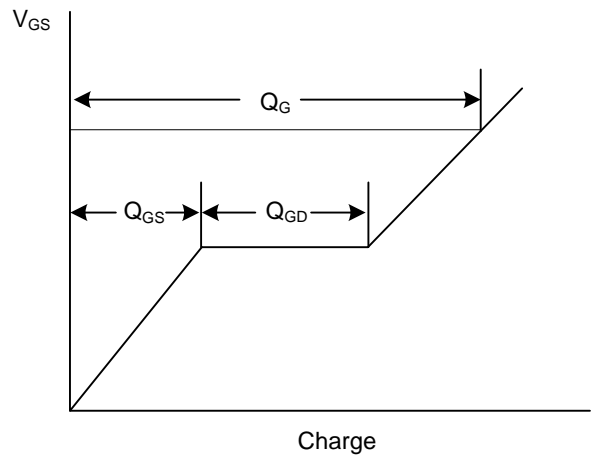
Switching Test Circuit



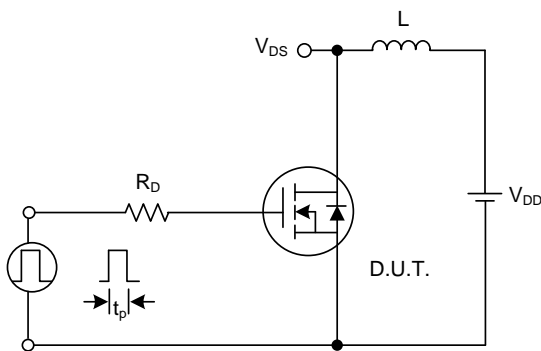
Switching Waveforms



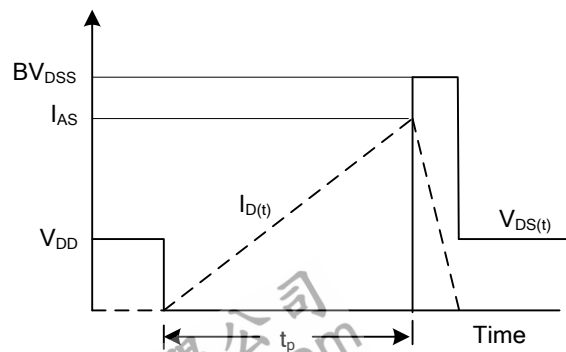
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. UTC reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.