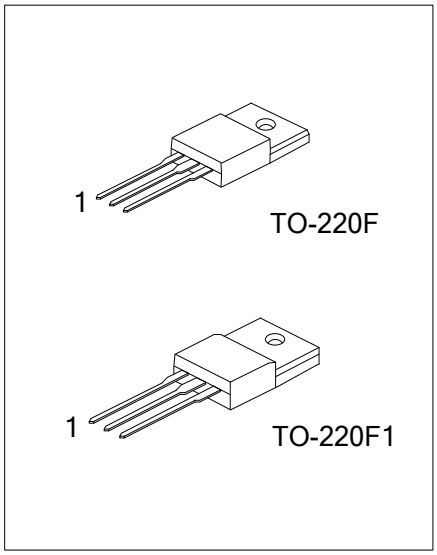
18A, 500V N-CHANNEL POWER MOSFET



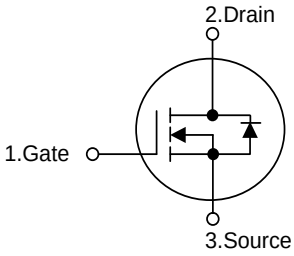
■ DESCRIPTION

The UTC **18N50-ML** is a high voltage power MOSFET combines advanced trench MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

■ FEATURES

* $R_{DS(ON)} \leq 0.3\ \Omega$ @ V_{GS}=10V, I_D=9.0A
* Fast switching capability
* Avalanche energy tested
* Improved dv/dt capability, high ruggedness

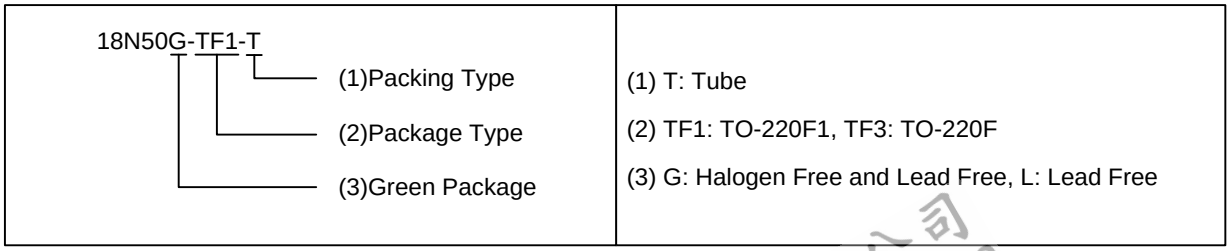
■ SYMBOL



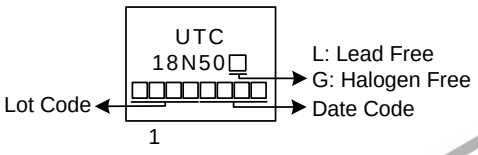
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
18N50L-TF1-T	18N50G-TF1-T	TO-220F1	G	D	S	Tube
18N50L-TF3-T	18N50G-TF3-T	TO-220F	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source



■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	500	V
Gate-Source Voltage		V_{GSS}	±30	V
Continuous Drain Current		I_D	18	A
Pulsed Drain Current (Note 2)		I_{DM}	36	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	1380	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3.9	V/ns
Power Dissipation		P_D	44	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. L = 30mH, I_{AS} = 9.6A, V_{DD} = 100V, R_G = 25 Ω, Starting T_J = 25°C
4. I_{SD} ≤ 9.0A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

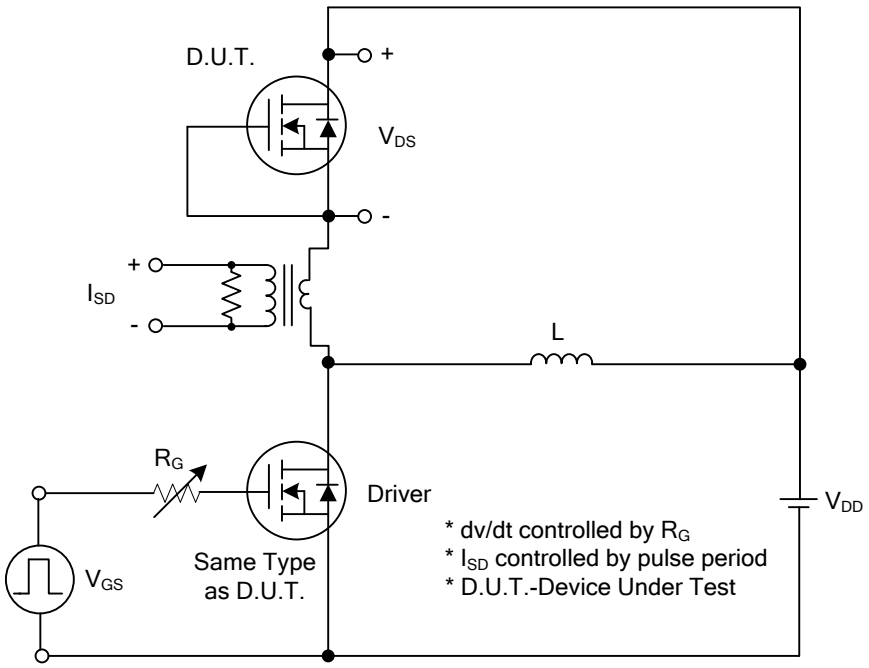
PARAMETER	SYMBOL	RATING	UNIT
Junction to Ambient	θ_{JA}	62.5	°C/W
Junction to Case	θ_{JC}	2.84	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

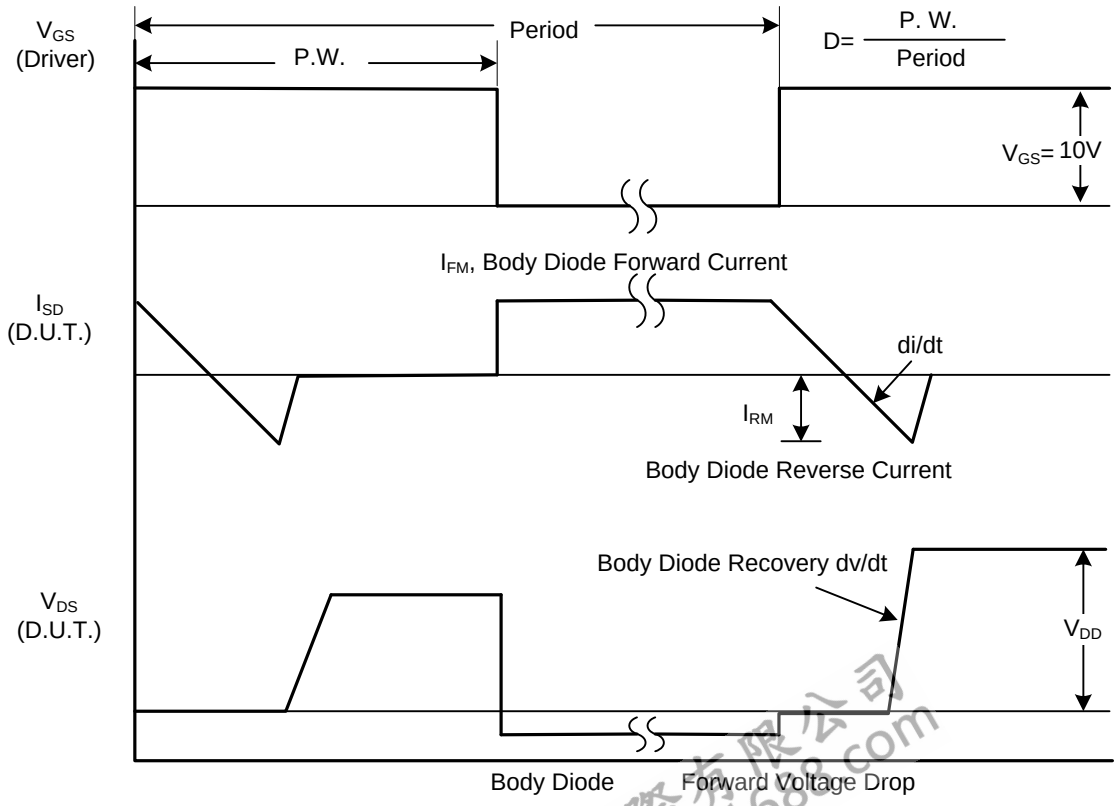
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	V_{GS}=0V, I_D=250μA	500			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=500V, V_{GS}=0V			10	μA
Gate- Source Leakage Current	Forward	I_{GSS}	V_{GS}=30V, V_{DS}=0V			100	nA
	Reverse		V_{GS}=-30V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=9.0A			0.3	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C_{ISS}	V_{DS}=25V, V_{GS}=0V, f=1.0MHz		2880		pF
Output Capacitance		C_{OSS}			307		pF
Reverse Transfer Capacitance		C_{RSS}			39.3		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge (Note 1)		Q_G	V_{DS}=480V, V_{GS}=10V, I_D=18A I_G=1mA (Note 1, 2)		77		nC
Gate-Source Charge		Q_{GS}			13		nC
Gate-Drain Charge		Q_{GD}			27		nC
Turn-On Delay Time (Note 1)		$t_{D(ON)}$	V_{DS}=100V, V_{GS}=10V, I_D=18A, R_G=25Ω (Note 1, 2)		40		ns
Turn-On Rise Time		t_R			45		ns
Turn-Off Delay Time		$t_{D(OFF)}$			232		ns
Turn-Off Fall Time		t_F			55		ns
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS							
Maximum Body-Diode Continuous Current		I_S				18	A
Maximum Body-Diode Pulsed Current		I_{SM}				36	A
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I_S=18A, V_{GS}=0V			1.4	V
Reverse Recovery Time (Note 1)		t_{rr}	I_S=18A, V_{GS}=0V di/dt=100A/μs		400		ns
Reverse Recovery Charge		Q_{rr}			12.5		μC

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

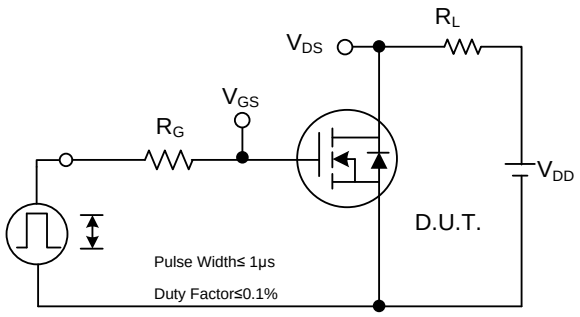


Peak Diode Recovery dv/dt Test Circuit

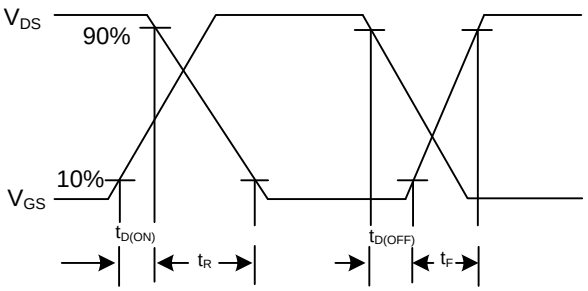


Peak Diode Recovery dv/dt Waveforms

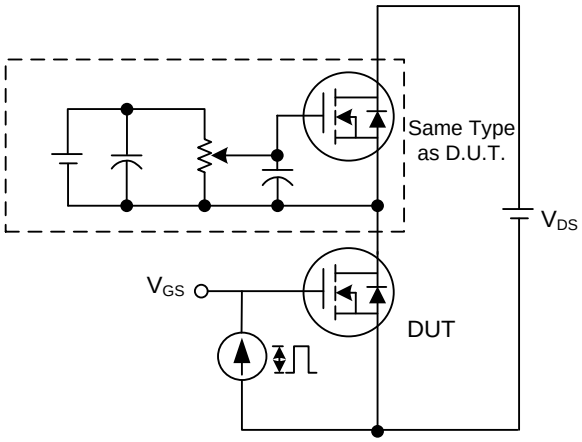
■ TEST CIRCUITS AND WAVEFORMS



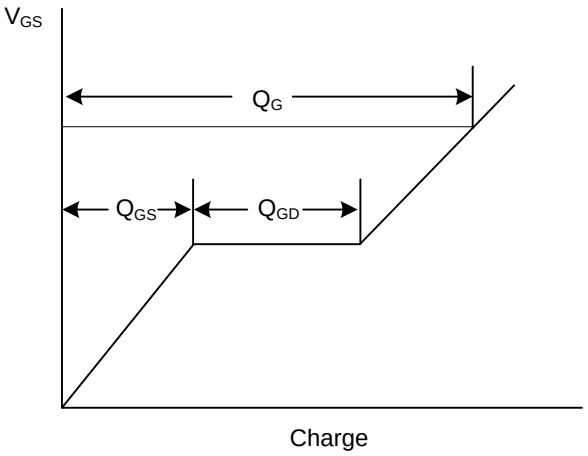
Switching Test Circuit



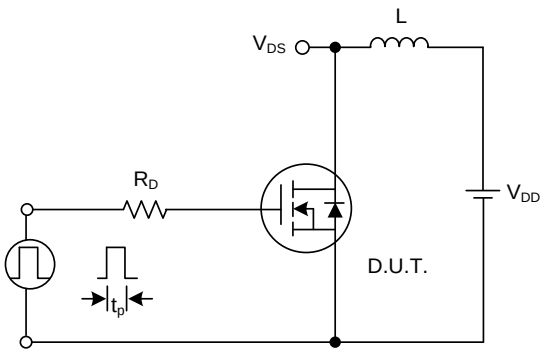
Switching Waveforms



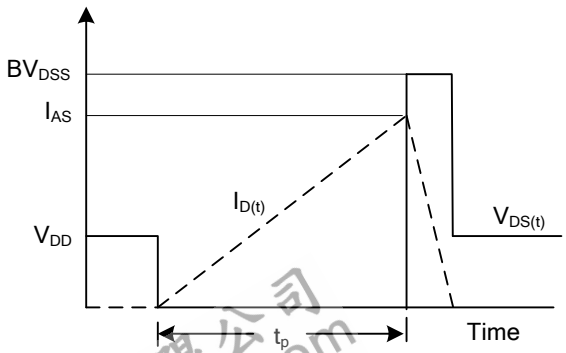
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms