

UNISONIC TECHNOLOGIES CO., LTD

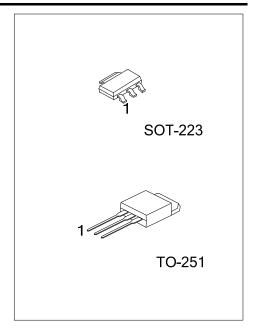
1N40A **Preliminary Power MOSFET**

N-CHANNEL 1A, 400V **POWER MOSFET**

DESCRIPTION

The UTC 1N40A is an N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology is specialized in allowing a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

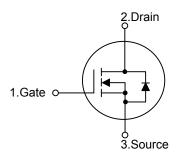
The UTC 1N40A is universally applied in electronic lamp ballast based on half bridge topology and high efficient switched mode power supply.



FEATURES

- * High switching speed
- * $R_{DS(ON)}$ < 6.8 Ω @ V_{GS} = 10V, I_{D} = 0.5A
- * 100% avalanche tested

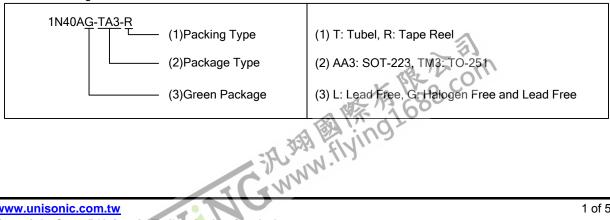
SYMBOL



ORDERING INFORMATION

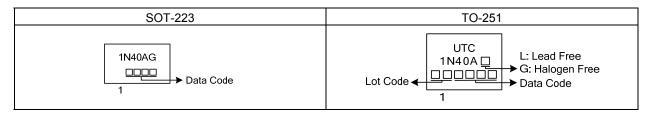
Ordering Number		Doolsons	Pin Assignment			Doolsing	
Lead Free	Halogen Free	Package	1	2	3	Packing	
-	1N40AG-AA3-R	SOT-223	G	D	S	Tape Reel	
1N40AL-TM3-T	1N40AG-TM3-T	TO-251	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



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MARKING





■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	400	٧
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous (T _C =25°C)	I_D	1	Α
	Pulsed (Note 2)	I _{DM}	4	Α
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	40	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	SOT-223	D	1	W
	TO-251	P_D	25	W
Derate above 25°C	SOT-223	D	125	W/°C
	TO-251	P_{D}	0.2	W/°C
Junction Temperature		T_J	+150	°C
Storage Temperature		T _{STG}	-55~+150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature
- 3. L = 80mH, I_{AS} = 1A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}$ C
- 4. $I_{SD} \le 1.8A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient	SOT-223	0	150	°C/W	
	TO-251	θ_{JA}	110	°C/W	
Junction to Case	SOT-223	θЈС	125	°C/W	
	TO-251		5	°C/W	



■ **ELECTRICAL CHARACTERISTICS** (T_C=25°C, unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
BV _{DSS}	I _D =250μA, V _{GS} =0V	400			V
△BV _{DSS} /△T _J	Reference to 25°C, I _D =250µA		0.4		V/°C
I _{DSS}	V _{DS} =400V, V _{GS} =0V			1	μA
I _{GSS}	V _{GS} =+30V, V _{DS} =0V V _{GS} =-30V, V _{DS} =0V			+100 -100	nA nA
			•		
V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.0		4.0	V
R _{DS(ON)}	V _{GS} =10V, I _D =0.5A		4.0	6.8	Ω
		•			
C _{ISS}			125	150	pF
Coss	V_{GS} =0V, V_{DS} =25V, f=1.0MHz		20	30	pF
			17	28	pF
Q_G	V _{GS} =10V, V _{DS} =50V, I _D =1.3A (Note 1, 2)		10	15	nC
Q_GS			3.22		nC
Q_GD			8.0		nC
t _{D(ON)}			33	40	ns
t _R	V_{DD} =30V, I_{D} =0.5A, R_{G} =25 Ω		20	35	ns
t _{D(OFF)}	(Note 1, 2)		58	78	ns
t _F			17	30	ns
CHARACTERIS	STICS				
Is				1.0	Α
I _{SM}				4.0	Α
V_{SD}	I _S =1A, V _{GS} =0V			1.5	V
	BV _{DSS} △BV _{DSS} /△T _J I _{DSS} I _{GSS} V _{GS(TH)} R _{DS(ON)} C _{ISS} C _{OSS} C _{RSS} Q _G Q _{GS} Q _{GD} t _{D(ON)} t _R t _{D(OFF)} t _F CHARACTERIS I _{SM}	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{ c c c c } BV_{DSS} & I_D = 250 \mu A, \ V_{GS} = 0V & 400 \\ \hline \\ \triangle BV_{DSS}/\triangle T_J & \text{Reference to } 25^{\circ}\text{C}, \ I_D = 250 \mu A \\ \hline \\ I_{DSS} & V_{DS} = 400 \text{V}, \ V_{GS} = 0 \text{V} \\ \hline \\ I_{GSS} & V_{GS} = +30 \text{V}, \ V_{DS} = 0 \text{V} \\ \hline \\ V_{GS} = -30 \text{V}, \ V_{DS} = 0 \text{V} \\ \hline \\ V_{GS} = -30 \text{V}, \ V_{DS} = 250 \mu A \\ \hline \\ R_{DS(ON)} & V_{GS} = 10 \text{V}, \ I_D = 0.5 A \\ \hline \\ C_{ISS} & \\ C_{OSS} & \\ C_{OSS} & \\ \hline \\ C_{RSS} & \\ \hline \\ Q_{GS} & \\ \hline \\ Q_{GS} & \\ \hline \\ Q_{GD} & \\ \hline \\ V_{DS} = 10 \text{V}, \ V_{DS} = 25 \text{V}, \ f = 1.0 \text{MHz} \\ \hline \\ C_{RSS} & \\ \hline \\ Q_{GS} & \\ \hline \\ Q_{GS} & \\ \hline \\ V_{OS} = 10 \text{V}, \ V_{DS} = 50 \text{V}, \ I_D = 1.3 \text{A} \\ \hline \\ (Note 1, 2) & \\ \hline \\ t_{D(ON)} & \\ \hline \\ t_{R} & \\ \hline \\ V_{DD} = 30 \text{V}, \ I_D = 0.5 \text{A}, \ R_G = 25 \Omega \\ \hline \\ (Note 1, 2) & \\ \hline \\ t_F & \\ \hline \\ \textbf{CHARACTERISTICS} & \\ \hline \\ I_{SM} & \\ \hline \\ \\ I_{SM} & \\ \hline \end{array}$	$ \begin{array}{ c c c c c } BV_{DSS} & I_D = 250 \mu A, \ V_{GS} = 0V & 400 \\ \hline \triangle BV_{DSS}/\triangle T_J & \text{Reference to } 25^{\circ}\text{C}, \ I_D = 250 \mu A & 0.4 \\ \hline I_{DSS} & V_{DS} = 400 V, \ V_{GS} = 0V & \\ \hline I_{GSS} & V_{DS} = 400 V, \ V_{DS} = 0V & \\ \hline V_{GS} = +30 V, \ V_{DS} = 0V & \\ \hline V_{GS} = -30 V, \ V_{DS} = 0V & 2.0 \\ \hline R_{DS(ON)} & V_{GS} = 10 V, \ I_D = 0.5 A & 4.0 \\ \hline \hline C_{ISS} & & 125 \\ \hline C_{OSS} & V_{GS} = 10 V, \ V_{DS} = 25 V, \ f = 1.0 MHz & 20 \\ \hline C_{RSS} & & 17 \\ \hline \hline Q_G & V_{GS} = 10 V, \ V_{DS} = 50 V, \ I_D = 1.3 A & 3.22 \\ \hline Q_{GD} & (Note 1, 2) & 0.8 \\ \hline t_{D(ON)} & & 33 \\ \hline t_R & V_{DD} = 30 V, \ I_D = 0.5 A, \ R_G = 25 \Omega & 20 \\ \hline t_{D(OFF)} & (Note 1, 2) & 58 \\ \hline t_F & & 17 \\ \hline \textbf{CHARACTERISTICS} & & & & & & & & & & & & & & & & & & &$	$\begin{array}{ c c c c c c } \hline BV_{DSS} & I_D=250\mu A, V_{GS}=0V & 400 \\ \hline \triangle BV_{DSS}/\triangle T_J & Reference to 25^{\circ}C, I_D=250\mu A & 0.4 \\ \hline I_{DSS} & V_{DS}=400V, V_{GS}=0V & 1 \\ \hline I_{GSS} & \hline V_{GS}=+30V, V_{DS}=0V & +100 \\ \hline V_{GS}(TH) & V_{DS}=V_{GS}, I_D=250\mu A & 2.0 & 4.0 \\ \hline R_{DS}(ON) & V_{GS}=10V, I_D=0.5A & 4.0 & 6.8 \\ \hline \hline C_{ISS} & & 125 & 150 \\ \hline C_{OSS} & V_{GS}=0V, V_{DS}=25V, f=1.0MHz & 20 & 30 \\ \hline C_{RSS} & & 17 & 28 \\ \hline \hline Q_{G} & V_{GS}=10V, V_{DS}=50V, I_D=1.3A & 10 & 15 \\ \hline Q_{GS} & (Note 1, 2) & 0.8 \\ \hline t_{D}(ON) & & 33 & 40 \\ \hline t_{R} & V_{DD}=30V, I_D=0.5A, R_{G}=25\Omega & 20 & 35 \\ \hline t_{D}(OFF) & (Note 1, 2) & 58 & 78 \\ \hline t_{F} & & 17 & 30 \\ \hline \hline \textbf{CHARACTERISTICS} & & 1.0 \\ \hline I_{SM} & & 4.0 \\ \hline \end{array}$

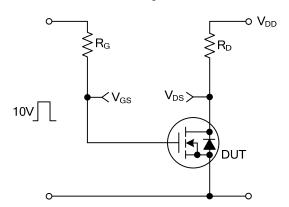
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%

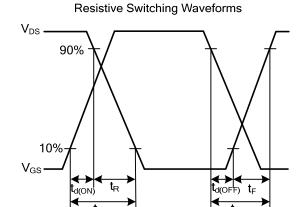


^{2.} Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

Resistive Switching Test Circuit





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