

# 0.5A, 600V N-CHANNEL POWER MOSFET

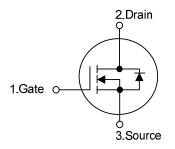
#### DESCRIPTION

The UTC **1N60A** is a high voltage MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

#### FEATURES

- $* R_{DS(ON)} < 15 \Omega @V_{GS} = 10 V.$
- \* Ultra Low gate charge (typical 8.0nC)
- \* Low reverse transfer capacitance (C<sub>RSS</sub> = 3.0 pF(max))
- \* Fast switching capability
- \* Avalanche energy specified
- \* Improved dv/dt capability, high ruggedness

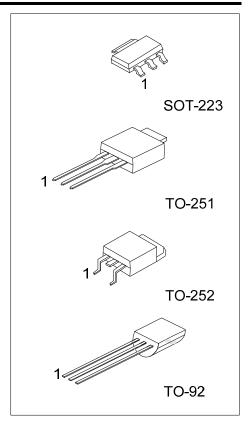
#### SYMBOL



#### ORDERING INFORMATION

		1					
Ordering Number		Package	Pin Assignment			Packing	
Lead Free	Halogen Free	i ackaye	1	2	3	i acking	
1N60AL-AA3-R	1N60AL-AA3-R 1N60AG-AA3-R		G	D	S	Tape Reel	
1N60AL-TM3-T	1N60AG-TM3-T	TO-251	G	D	S	Tube	
1N60AL-TN3-R	1N60AG-TN3-R	TO-252	G	D	S	Tape Reel	
1N60AL-T92-B	1N60AG-T92-B	TO-92	G	D	S	Tape Box	
1N60AL-T92-K	1N60AG-T92-K	TO-92	G	D	S	Bulk	
Note: Pin Assignment: G: Gate D: Drain S: Source							
Note: Pin Assignment: G: Gate D: Drain S: Source   1N60AL-AA3-R (1)Packing Type (2)Package Type (2)Package Type   (3)Lead Free (3)Lead Free (3)L: Lead Free, G: Halogen Free							

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### **Power MOSFET**

#### MARKING INFORMATION

PACKAGE	MARKING			
SOT-223	1N60A → G: Halogen Free → Data Code			
TO-251 TO-252	UTC 1N60A G: Halogen Free Lot Code 1 Lot Code			
TO-92	UTC 1N60A P: Halogen Free Data Code			



PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V <sub>DSS</sub>	600	V
Gate-Source Voltage		V <sub>GSS</sub>	±30	V
Continuous Drain Current		I <sub>D</sub>	0.5	А
Pulsed Drain Current (Note 2)		I <sub>DM</sub>	2	А
Avalanche Energy	Single Pulse(Note 3)	E <sub>AS</sub>	50	mJ
	Repetitive(Note 2)	E <sub>AR</sub>	3.6	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation (T <sub>c</sub> =25°C)	SOT-223	P <sub>D</sub>	6.25	
	TO-251/TO-252		34	W
	TO-92		3	
Derate above 25°C	SOT-223		0.05	
	TO-251/TO-252		0.27	W/°C
	TO-92		0.025	
Junction Temperature	unction Temperature		+150	°C
Operating Temperature		T <sub>OPR</sub>	-55 ~ +150	°C
Storage Temperature		T <sub>STG</sub>	-55 ~ +150	°C

#### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>c</sub> = 25°C, unless otherwise specified.)

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

3. L=92mH, I<sub>AS</sub>=0.8A, V<sub>DD</sub>=50V, R<sub>G</sub>=0 $\Omega$ , Starting T<sub>J</sub>=25°C

4.  $I_{SD} \le 1.0A$ , di/dt  $\le 100A/\mu s$ ,  $V_{DD} \le BV_{DSS}$ , Starting T<sub>J</sub>=25°C

#### THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient	SOT-223	θ <sub>JA</sub>	150	°C/W	
	TO-251/TO-252		110		
	TO-92		160		
Junction to Case	SOT-223	θ <sub>JC</sub>	20	°C/W	
	TO-251/TO-252		5		
	TO-92		80		

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#### ■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub>=25°C, unless otherwise specified.)

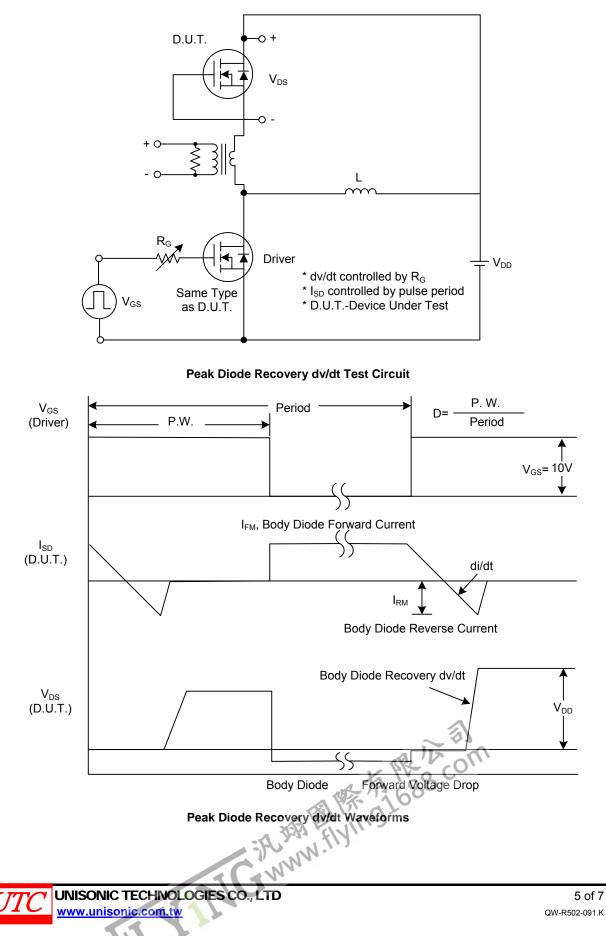
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	МАХ	UNIT
OFF CHARACTERISTICS	0					
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA	600			V
Drain-Source Leakage Current (TJ=25°C)	- I <sub>DSS</sub>				10	
Drain-Source Leakage Current (TJ=125°C)		$V_{DS} = 600V, V_{GS} = 0V$			10	μA
Gate-Source Leakage Current Forward Reverse	- I <sub>GSS</sub>	$V_{GS} = 30V, V_{DS} = 0V$ $V_{GS} = -30V, V_{DS} = 0V$			100 -100	nA nA
Breakdown Voltage Temperature Coefficient	∆BV <sub>DSS</sub> /∆T <sub>J</sub>	$l_{\rm p} = 250 \mu \Delta$		0.4		V/°C
ON CHARACTERISTICS						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.5	V
Static Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 0.5A		11	15	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C <sub>ISS</sub>				100	рF
Output Capacitance	C <sub>OSS</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz			20	рF
Reverse Transfer Capacitance	C <sub>RSS</sub>	]			3	рF
SWITCHING CHARACTERISTICS						_
Turn-On Delay Time	t <sub>D (ON)</sub>			12	34	ns
Turn-On Rise Time	t <sub>R</sub>	$V_{DD}$ =300V, $I_{D}$ =0.5A, $R_{G}$ =5 $\Omega$		11	32	ns
Turn-Off Delay Time	t <sub>D (OFF)</sub>	(Note 1,2)		40	90	ns
Turn-Off Fall Time	t <sub>F</sub>			18	46	ns
Total Gate Charge	$Q_{G}$	V <sub>DS</sub> =480V, V <sub>GS</sub> =10V, I <sub>D</sub> =0.8A		8	10	nC
Gate-Source Charge	Q <sub>GS</sub>	(Note 1.2)		1.8		nC
Gate-Drain Charge	$Q_{GD}$	(Note 1,2)		4.0		nC
SOURCE- DRAIN DIODE RATINGS AND	CHARACTERIS	STICS				
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>SD</sub> = 1.2A			1.6	V
Maximum Continuous Drain-Source Diode	I <sub>S</sub>				1.2	А
Forward Current	IS	-			1.2	~
Maximum Pulsed Drain-Source Diode	lev.				4.8	А
Forward Current	I <sub>SM</sub>				4.0	
Reverse Recovery Time	t <sub>RR</sub>	V <sub>GS</sub> =0V, I <sub>SD</sub> = 1.2A		136		ns
Reverse Recovery Charge	Q <sub>RR</sub>	di/dt = 100A/µs		0.3		μC

Notes: 1. Pulse Test: Pulse Width $\leq$ 300µs, Duty Cycle $\leq$ 2%

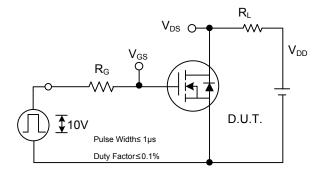
2. Essentially independent of operating temperature.

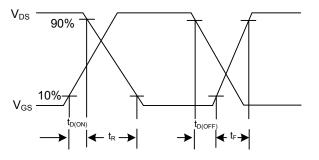


### TEST CIRCUITS AND WAVEFORMS

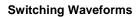


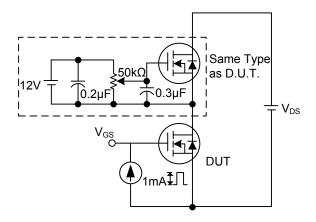
#### **TEST CIRCUITS AND WAVEFORMS (Cont.)**



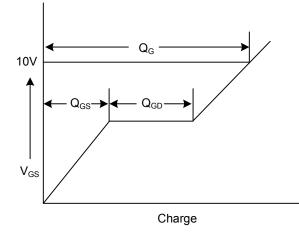


**Switching Test Circuit** 

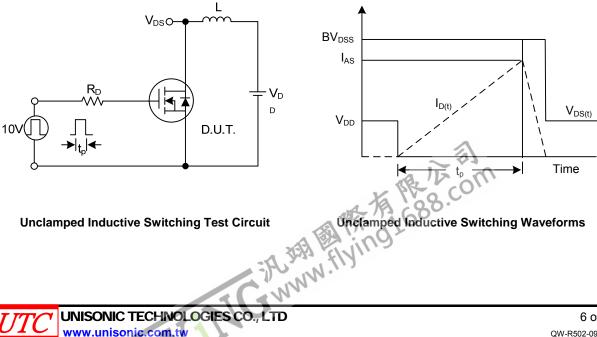




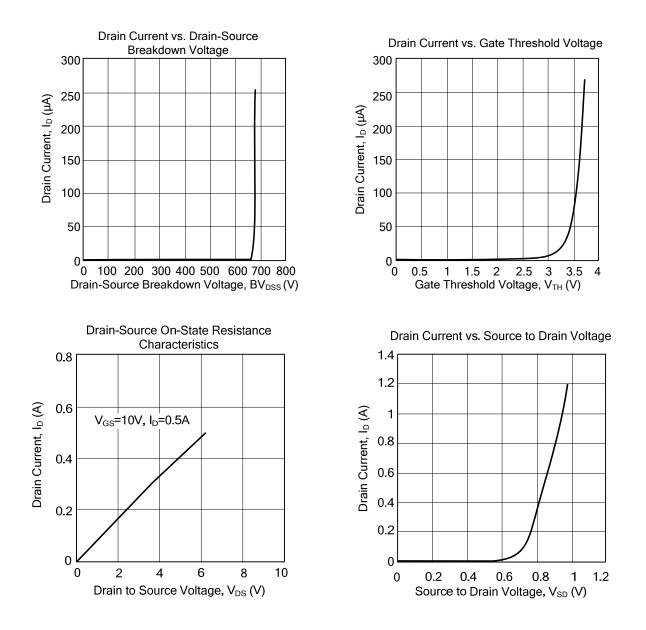
**Gate Charge Test Circuit** 



**Gate Charge Waveform** 



### TYPICAL CHARACTERISTICS



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