UNISONIC TECHNOLOGIES CO., LTD

1N60P **Power MOSFET**

1.2A, 600V N-CHANNEL POWER MOSFET

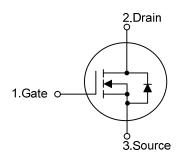
DESCRIPTION

The UTC 1N60P is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristic. This power MOSFET is usually used at high speed switching applications of power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

- * $R_{DS(ON)} = 11.5\Omega@V_{GS} = 10V.$
- * Ultra Low gate charge (typical 5.0nC)
- * Low reverse transfer capacitance (C_{RSS} = typical 3.0 pF)
- * Fast switching capability
- * Avalanche energy specified
- * Improved dv/dt capability, high ruggedness

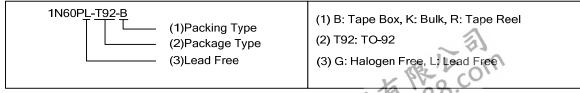
SYMBOL



ORDERING INFORMATION

Ordering Number		Dookaga	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
1N60PL-T92-B	1N60PG-T92-B	TO-92	G	D	S	Tape Box	
1N60PL-T92-K	1N60PG-T92-K	TO-92	G	D	S	Bulk	
1N60PL-T92-R	1N60PG-T92-R	TO-92	G	D	S	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



1 TO-92

■ **ABSOLUTE MAXIMUM RATINGS** (T_C = 25 °C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V _{DSS}	600	V	
Gate-Source Voltage		V _{GSS}	±30	V	
Avalanche Current (Note 2)		I _{AR}	1.2	Α	
Continuous Drain Current		I _D	1.2	Α	
Pulsed Drain Current (Note 2)		I _{DM}	4.8	Α	
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	50	mJ	
	Repetitive (Note 2)	E _{AR}	4.0	mJ	
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns	
Power Dissipation (T _A =25°C)		P _D	1	W	
Junction Temperature		TJ	+150	$^{\circ}$ C	
Operating Temperature		T _{OPR}	-55 ~ +150	$^{\circ}\mathbb{C}$	
Storage Temperature		T _{STG}	-55 ~ +150	$^{\circ}\!\mathbb{C}$	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature
- 3. L = 60mH, I_{AS} = 1A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C
- 4. $I_{SD} \le 1.2A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	140	°C/W	



ELECTRICAL CHARACTERISTICS (Tc=25°C, unless otherwise specified.)

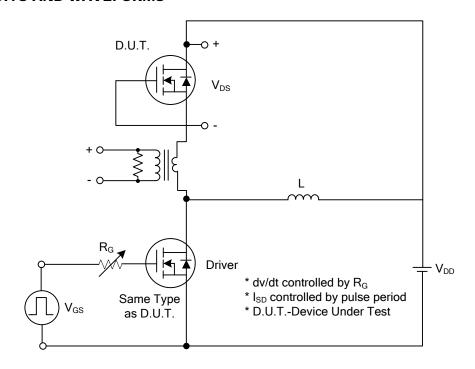
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250μA	600			V	
Drain-Source Leakage Current		I _{DSS}	V _{DS} =600V, V _{GS} =0V			10	μΑ	
Cata Sauras Laskaga Current For	ward	1	V_{GS} =30V, V_{DS} =0V			100	nA	
Gate-Source Leakage Current Rev	verse	I _{GSS}	V_{GS} =-30V, V_{DS} =0V			-100	nA	
Breakdown Voltage Temperature Coefficient		$\triangle BV_{DSS}/\triangle T_{J}$	I _D =250μA		0.4		V/°C	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu A$			4.0	V	
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =0.6A		9.3	11.5	Ω	
DYNAMIC CHARACTERISTICS								
Input Capacitance		C _{ISS}			120	150	pF	
Output Capacitance		Coss	V _{DS} =25V, V _{GS} =0V, f=1MHz		20	25	pF	
Reverse Transfer Capacitance		C_{RSS}			3.0	4.0	pF	
SWITCHING CHARACTERISTICS								
Turn-On Delay Time		t _{D(ON)}			5	20	ns	
Turn-On Rise Time		t_R	V_{DD} =300V, I_{D} =1.2A, R_{G} =50 Ω		25	60	ns	
Turn-Off Delay Time		$t_{D(OFF)}$	(Note 2, 3)		7	25	ns	
Turn-Off Fall Time		t _F			25	60	ns	
Total Gate Charge		Q_G	V _{DS} =480V, V _{GS} =10V, I _D =1.2A		5.0	6.0	nC	
Gate-Source Charge		Q_GS	(Note 2, 3)		1.0		nC	
Gate-Drain Charge		Q_GD	(Note 2, 3)		2.6		nC	
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS								
Drain-Source Diode Forward Voltage		V_{SD}	V _{GS} =0V, I _S =1.2A			1.4	V	
Maximum Continuous Drain-Source Diode		1				1.2	Α	
Forward Current		I _S				1.2	A	
Maximum Pulsed Drain-Source Diode		lou				4.8	Α	
Forward Current		I _{SM}				4.0	^	
Reverse Recovery Time		t _{rr}	V _{GS} =0V, I _S =1.2A dI _F /dt=100A/µs (Note 1)		160		ns	
Reverse Recovery Charge		Q_{RR}			0.3		μC	

Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature

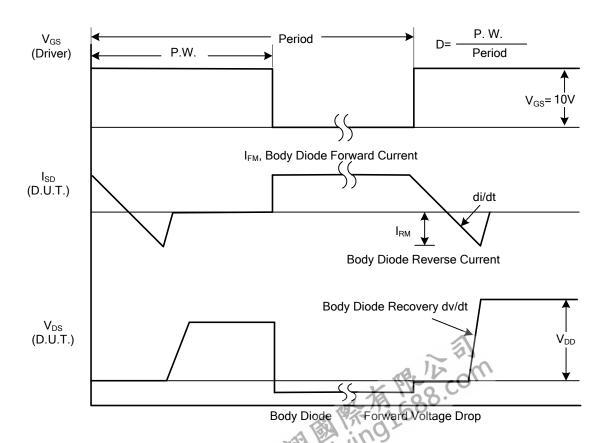
- 2. Pulse Test: Pulse Width ≤300µs, Duty Cycle≤2%
- 3. Essentially Independent of Operating Temperature



■ TEST CIRCUITS AND WAVEFORMS

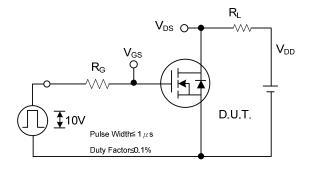


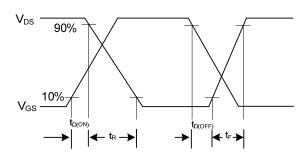
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

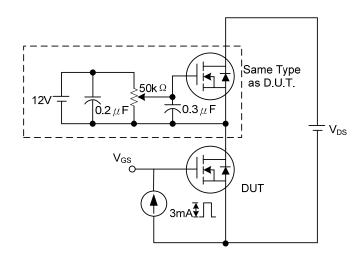
TEST CIRCUITS AND WAVEFORMS (Cont.)

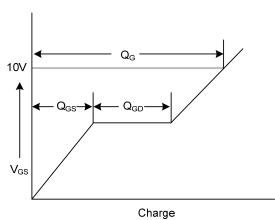




Switching Test Circuit

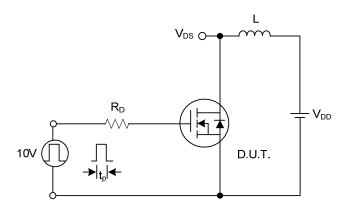
Switching Waveforms

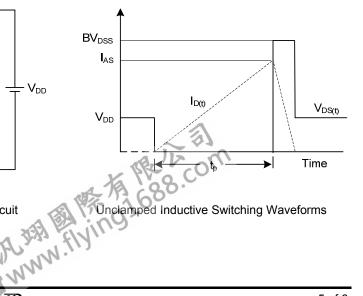




Gate Charge Test Circuit

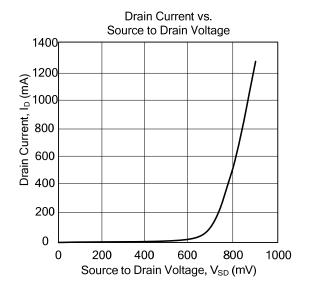
Gate Charge Waveform

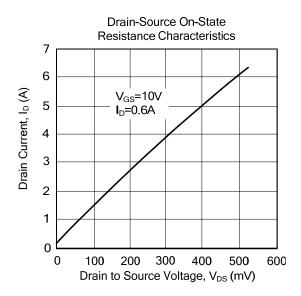


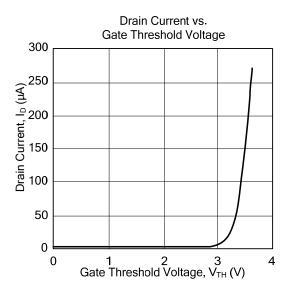


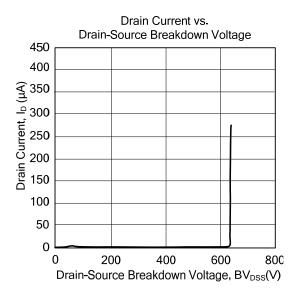
Unclamped Inductive Switching Test Circuit

■ TYPICAL CHARACTERISTICS









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