UTC UNISONIC TECHNOLOGIES CO., LTD

1N70-CBS **Preliminary** Power MOSFET

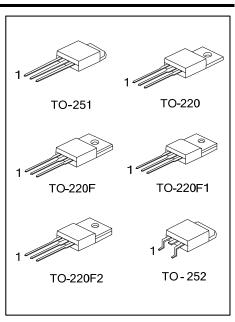
1.0A, 700V N-CHANNEL **POWER MOSFET**

DESCRIPTION

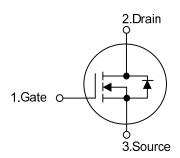
The UTC 1N70-CBS is a high voltage MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

- * $R_{DS(ON)}$ < 16.8 Ω @ V_{GS} = 10V , I_D = 0.5A
- * Fast switching capability
- * Avalanche energy specified
- * Improved dv/dt capability, high ruggedness



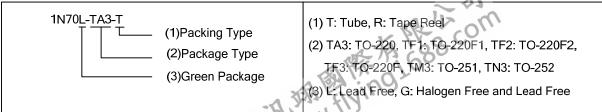
SYMBOL



ORDERING INFORMATION

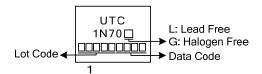
Ordering Number		Daokago	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
1N70L-TA3-T	1N70G-TA3-T	TO-220	G	D	S	Tube	
1N70L-TF1-T	1N70G-TF1-T	TO-220F1	G	D	S	Tube	
1N70L-TF2-T	1N70G-TF2-T	TO-220F2	G	D	S	Tube	
1N70L-TF3-T	1N70G-TF3-T	TO-220F	G	D	S	Tube	
1N70L-TM3-T	1N70G-TM3-T	TO-251	G	D	S	Tube	
1N70L-TN3-R	1N70G-TN3-R	TO-252	G	D	S	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



www.unisonic.com.tw 1 of 7

MARKING





ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	700	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous	I_{D}	1.0	Α
	Pulsed (Note 2)	I_{DM}	4.0	Α
Avalanche Current (Note 2)		I_{AR}	1.2	Α
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	7.0	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.8	V/ns
Power Dissipation	TO-220		40	W
	TO-220F/TO-220F1 TO-220F2	P_D	21	W
	TO-251/TO-252		28	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L=10mH, I_{AS} =1.2A, V_{DD} =50V, R_{G} =25 Ω , Starting T_{J} = 25°C
- 4. $I_{SD} \le 1.0A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2	θ_{JA}	62.5	°C/W
	TO-251/TO-252		110	°C/W
Junction to Case	TO-220		3.13	°C/W
	TO-220F/TO-220F1 TO-220F2	θ_{JC}	5.95	°C/W
	TO-251/TO-252		4.46	°C/W



ELECTRICAL CHARACTERISTICS (T_J =25°C, unless otherwise specified)

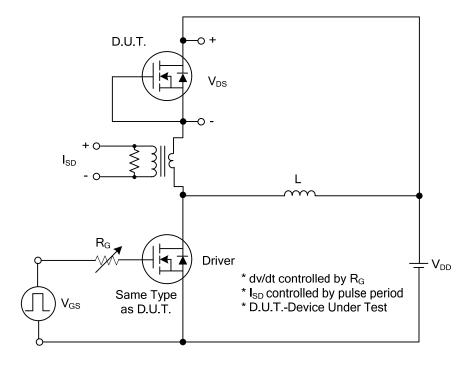
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS					ı			
Drain-Source Breakdown Voltage		BV _{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	700			V	
Drain-Source Leakage Current		I _{DSS}	V _{DS} = 700V, V _{GS} = 0V			1	μA	
Gate-Source Leakage Current	Forward	GSS	V _{GS} = 30V, V _{DS} = 0V			100	nA	
	Reverse		V _{GS} = -30V, V _{DS} = 0V			-100	nA	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V	
Static Drain-Source On-State Resistance		R _{DS(ON)}	$V_{GS} = 10V, I_D = 0.5A$			16.8	Ω	
DYNAMIC CHARACTERISTICS				ā.				
Input Capacitance		C_{ISS}			158		pF	
Output Capacitance		Coss	V_{DS} =25V, V_{GS} =0V, f =1MHz		29		pF	
Reverse Transfer Capacitance		C_{RSS}			14		pF	
SWITCHING CHARACTERISTICS								
Total Gate Charge (Note 1)		Q_{G}	\/ -E0\/ \/ -10\/ -0.83A		12		nC	
Gate to Source Charge		Q_GS	V _{DS} =50V, V _{GS} =10V, I _D =0.83A I _G =100µA (Note 1, 2)		1.7		nC	
Gate to Drain Charge		Q_GD	IG-100μΑ (Note 1, 2)		1.2		nC	
Turn-ON Delay Time (Note 1)		t _{D (ON)}			36		ns	
Rise Time		t_R	V_{DS} =30V, V_{GS} =10V, I_{D} =0.5A,		13		ns	
Turn-OFF Delay Time		$t_{D(OFF)}$	$R_G = 25\Omega$ (Note 1, 2)		74		ns	
Fall-Time	all-Time				12		ns	
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Maximum Body-Diode Continuous	Current	I_{SD}				1.0	Α	
Maximum Body-Diode Pulsed Current		I _{SM}				4.0	Α	
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I _S =1.0A, V _{GS} =0V			1.4	V	
Body Diode Reverse Recovery Time (Note 1)		t _{rr}	I _S =1.0A, V _{GS} =0V,		270		nS	
Body Diode Reverse Recovery Charge		Q_{rr}	dI _F /dt=100A/μs		0.43		μC	

Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%.

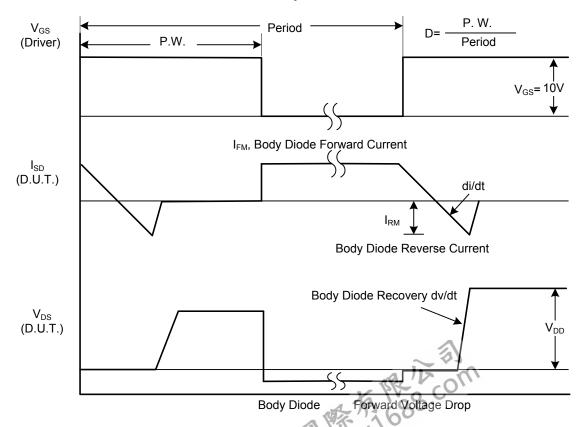


^{2.} Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

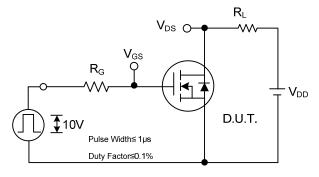


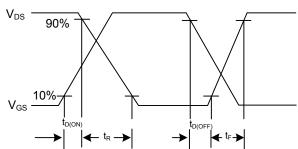
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dwdt Waveforms

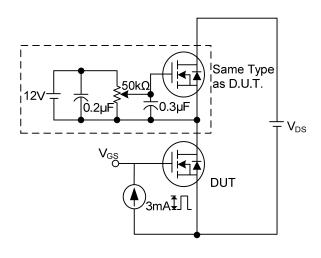
■ TEST CIRCUITS AND WAVEFORMS (Cont.)

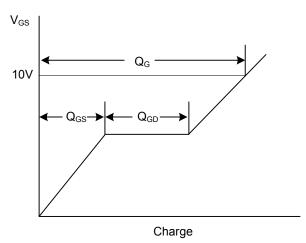




Switching Test Circuit

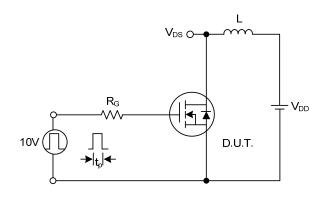
Switching Waveforms

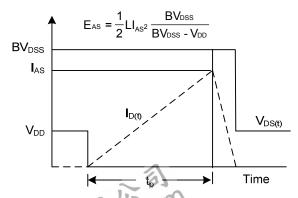




Gate Charge Test Circuit

Gate Charge Waveform





Unclamped Inductive Switching Test Circuit

cuit Unclamped Inductive Switching Waveforms

UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice.

