UNISONIC TECHNOLOGIES CO., LTD

1NM70-Q **Preliminary** Power MOSFET

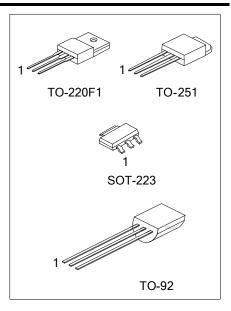
1A, 700V N-CHANNEL SUPER-JUNCTION MOSFET

DESCRIPTION

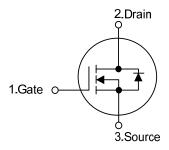
The UTC 1NM70-Q is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at DC-DC, AC-DC converters for power applications.

FEATURES

- * $R_{DS(ON)}$ < 5.40 @ V_{GS} = 10V, I_{D} =0.5A
- * Fast switching capability
- * Avalanche energy specified
- * Improved dv/dt capability, high ruggedness



SYMBOL



ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Doolsing	
Lead Free	Halogen Free	Package	1	2	3	Packing	
-	1NM70G-AA3-R	SOT-223	G	D	S	Tape Reel	
1NM70L-TF1-R	1NM70G-TF1-R	TO-220F1	G	D	S	Tape Reel	
1NM70L-TM3-R	1NM70G-TM3-R	TO-251	G	D	S	Tape Reel	
1NM70L-T92-B	1NM70G-T92-B	TO-92	G	D	S	Tape Box	
1NM70L-T92-K	1NM70G-T92-K	TO-92	G	D	S	Bulk	

Note: Pin Assignment: G: Gate D: Drain S: Source



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MARKING

Package	Marking
SOT-223	Lot Code
TO-220F1 / TO-251	UTC 1NM70□ G: Halogen Free Lot Code
TO-92	UTC 1NN70□ L: Lead Free G: Halogen Free Lot Code Data Code



■ **ABSOLUTE MAXIMUM RATINGS** (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage	Source Voltage		700	V
Gate-Source Voltage		V _{GSS} ±30		V
Drain Current	Continuous	I_{D}	1.0	Α
	Pulsed (Note 2)	I_{DM}	4.0	Α
Avalanche Current (Note 2)		I_{AR}	0.7	Α
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	34	mJ
Peak Diode Recovery dv/	dt (Note 4)	dv/dt 3.5		V/ns
,	SOT-223		8	W
Dower Dissipation	TO-220F1	В	21	W
Power Dissipation	TO-251	P_{D}	28	W
	TO-92		1.6	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ + 150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L=138mH, I_{AS} =0.7A, V_{DD} =50V, R_{G} =25 Ω , Starting T_{J} = 25°C
- 4. $I_{SD} \le 1.0A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL CHARACTERISTICS

PARAM	ETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-223		150	°C/W
	TO-220F1	$ heta_{JA}$	62.5	°C/W
	TO-251		110	°C/W
	TO-92		180	°C/W
Junction to Case	SOT-223	θυс	15.6	°C/W
	TO-220F1		5.95	°C/W
	TO-251		4.46	°C/W
	TO-92		78	°C/W



ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

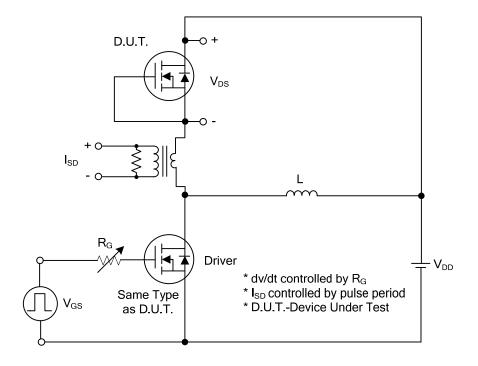
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV_{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	700			V	
Drain-Source Leakage Current		I_{DSS}	$V_{DS} = 700V, V_{GS} = 0V$			10	μΑ	
Gate-Source Leakage Current	Forward	I _{GSS}	$V_{GS} = 30V, V_{DS} = 0V$			100	nA	
	Reverse		$V_{GS} = -30V, V_{DS} = 0V$			-100	nA	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$ 2			4.5	V	
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} = 10V, I _D =0.5A			5.4	Ω	
DYNAMIC CHARACTERISTICS								
Input Capacitance	nput Capacitance				83		pF	
Output Capacitance		C _{ISS}	V_{DS} =25V, V_{GS} =0V, f =1MHz		37		pF	
Reverse Transfer Capacitance		C_{RSS}			5		pF	
SWITCHING CHARACTERISTICS								
Total Gate Charge (Note 1)		Q_G	V _{DS} =50V, V _{GS} =10V, I _D =0.5A I _G =100µA (Note 1, 2)		12.3		nC	
Gate-Source Charge		Q_GS			2		nC	
Gate-Drain Charge		Q_GD	IG-100μΑ (Note 1, 2)		4		nC	
Turn-On Delay Time (Note 1)		t _{D (ON)}			35		ns	
Turn-On Rise Time		t_R	V_{DD} =30V, V_{GS} =10V, I_{D} =0.5A,		21		ns	
Turn-Off Delay Time		$t_{D(OFF)}$	$R_G = 25\Omega$ (Note 1, 2)		53		ns	
Turn-Off Fall Time	urn-Off Fall Time				18		ns	
DRAIN-SOURCE DIODE CHARACTERISTICS								
Continuous Drain-Source Current		Is				1.0	Α	
Maximum Body-Diode Pulsed Current		I _{SM}				4.0	Α	
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I _S =1.0A, V _{GS} =0V			1.4	V	
Body Diode Reverse Recovery Time (Note 1)		t _{rr}	I _S =1.0A, V _{GS} =0V,		200		nS	
Body Diode Reverse Recovery Charge		Q_{rr}	dI _F /dt=100A/μs		0.63		μC	

Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle≤2%.

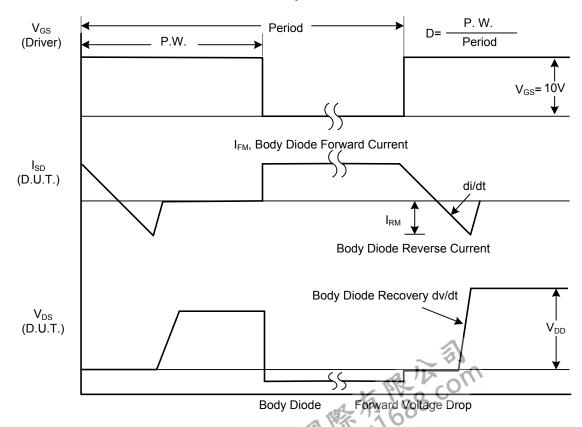


^{2.} Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

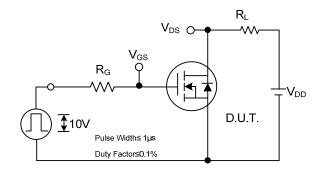


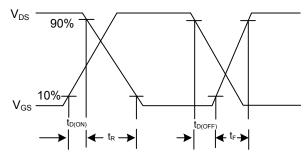
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

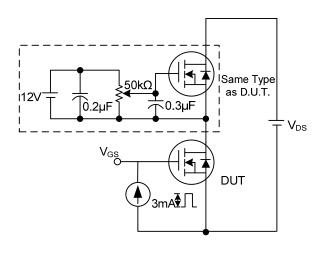
TEST CIRCUITS AND WAVEFORMS (Cont.)

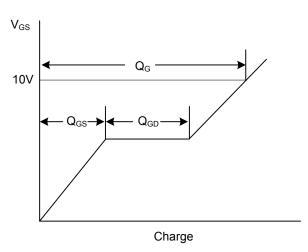




Switching Test Circuit

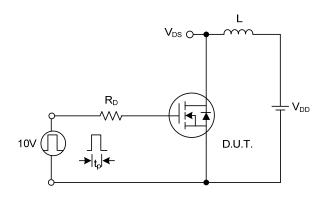
Switching Waveforms

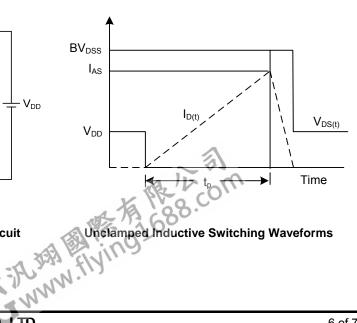




Gate Charge Test Circuit

Gate Charge Waveform





Unclamped Inductive Switching Test Circuit

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