UNISONIC TECHNOLOGIES CO., LTD

20N70K-MT Power MOSFET

20A, 700V N-CHANNEL POWER MOSFET

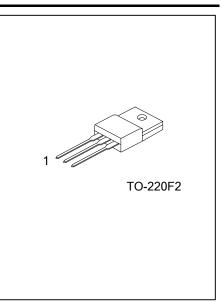
■ DESCRIPTION

The UTC **20N70K-MT** is an N-channel Power MOSFET using UTC's advanced technology to provide customers a minimum on-state resistance and superior switching performance, etc.

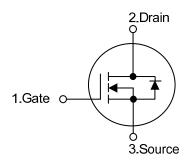
The UTC **20N70K-MT** is generally applied in high efficient DC to DC converters, PWM motor controls and bridge circuits, etc.

■ FEATURES

- * $R_{DS(ON)} \le 0.65\Omega$ @ $V_{GS}=10V$, $I_{D}=10A$
- * High Switching Speed
- * Improved dv/dt capability



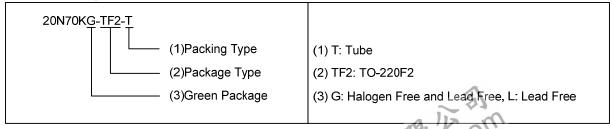
■ SYMBOL



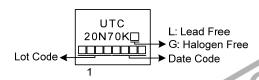
■ ORDERING INFORMATION

Ordering Number		Doolsono	Pin Assignment			Daakina	
Lead Free	Halogen Free	Package	1	2	3	Packing	
20N70KL-TF2-T	20N70KG-TF2-T	TO-220F2	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



■ MARKING



<u>www.unisonic.com.tw</u> 1 of 7

20N70K-MT Power MOSFET

■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V_{DSS}	700	V	
Gate-Source Voltage		V_{GSS}	±30	V	
Drain Current	Continuous	I_{D}	20	Α	
	Pulsed (Note 2)	I_{DM}	40	Α	
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	360	mJ	
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.58	V/ns	
Power Dissipation		P_{D}	40	W	
Junction Temperature		T_J	+150	°C	
Storage Temperature		T_{STG}	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L = 10mH, I_{AS} = 8.48A, V_{DD} = 50V, R_{G} = 25 Ω Starting T_{J} = 25°C
- 4. $I_{SD} \le 20A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT	
Junction to Ambient	θ_{JA}	62.5	°C/W	
Junction to Case	θ_{JC}	3.125	°C/W	

■ **ELECTRICAL CHARACTERISTICS** (T_J = 25°C, unless otherwise specified)

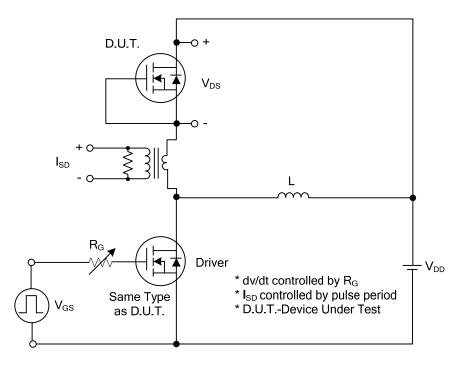
PARAMETER		SYMBOL	TEST CONDITIONS		TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D = 250μA				V	
Drain-Source Leakage Current		I_{DSS}	V _{DS} =700V, V _{GS} =0V			10	μΑ	
Gate-Source Leakage Current	Forward	I _{GSS}	V_{GS} =30V, V_{DS} =0V			100	nΑ	
	Reverse		V_{GS} =-30V, V_{DS} =0V			-100	nΑ	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu A$			4.0	V	
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =10A			0.65	Ω	
DYNAMIC CHARACTERISTICS								
Input Capacitance		C_{ISS}			2508		pF	
Output Capacitance		Coss	V_{GS} =0V, V_{DS} =25V, f=1.0MHz		220		pF	
Reverse Transfer Capacitance		C_{RSS}			14		pF	
SWITCHING CHARACTERISTICS								
Total Gate Charge (Note 1)		Q_G	\\ -100\\ \\ -10\\ \ \ -20\\		54		nC	
Gateource Charge		Q_GS	V_{DS} =100V, V_{GS} =10V, I_{D} =20A I_{G} =1mA (Note 1, 2)		11		nC	
Gate-Drain Charge		Q_GD	Tig That (Note 1, 2)		12		nC	
Turn-on Delay Time (Note 1)		t _{D(ON)}	V _{DS} =100V, V _{GS} =10V, I _D =20A, R _G =25Ω (Note 1, 2)		28		ns	
Rise Time		t_R			35		ns	
Turn-off Delay Time		t _{D(OFF)}			135		ns	
Fall-Time		t_{F}			73		ns	
SOURCE- DRAIN DIODE RATING	GS AND CH	ARACTERIS	TICS					
Maximum Body-Diode Continuous Current		I _S	10. 1V OS			20	Α	
Maximum Body-Diode Pulsed Current		I_{SM}	K NO CO			40	Α	
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	V _{GS} =0V, I _S =20A			1.4	V	
Reverse Recovery Time (Note 1)		t _{rr}	V_{GS} =0V, I_S =20A,		482		ns	
Reverse Recovery Charge		Q _{rr}	dt _F /dt=100A/µs (Note1)		8.5		μC	

Notes: 1. Pulse Test : Pulse width ≤ 300µs, Duty cycle ≤ 2%.

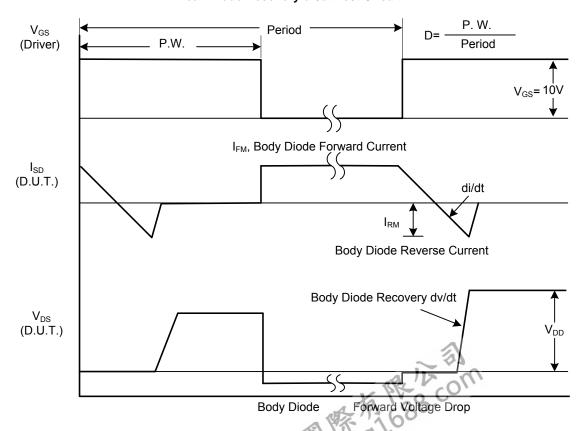
2. Essentially independent of operating temperature.

20N70K-MT Power MOSFET

■ TEST CIRCUITS AND WAVEFORMS



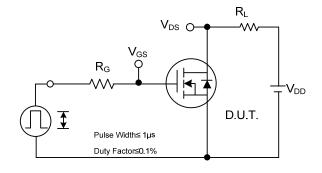
Peak Diode Recovery dv/dt Test Circuit

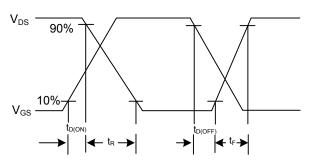


Peak Diode Recovery dv/dt Waveforms

20N70K-MT **Power MOSFET**

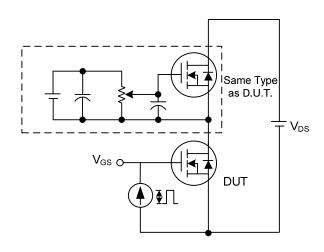
TEST CIRCUITS AND WAVEFORMS

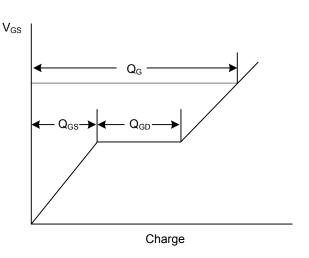




Switching Test Circuit

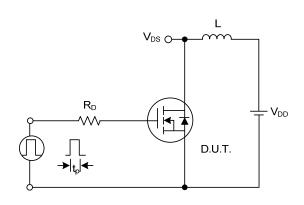
Switching Waveforms

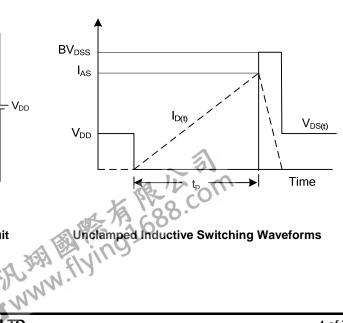




Gate Charge Test Circuit

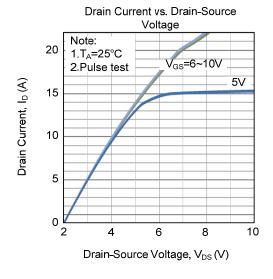
Gate Charge Waveform

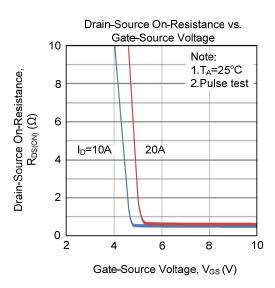


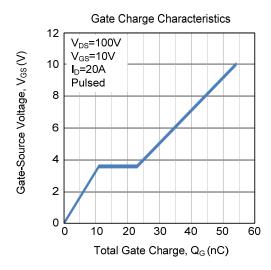


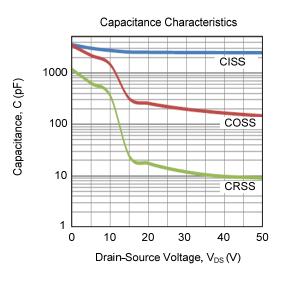
Unclamped Inductive Switching Test Circuit

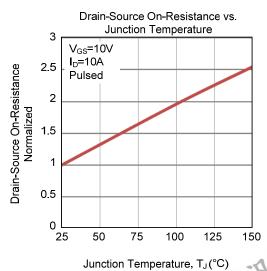
■ TYPICAL CHARACTERISTICS

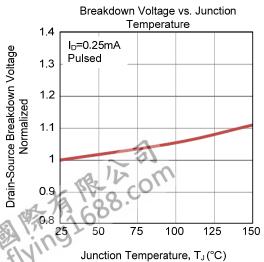




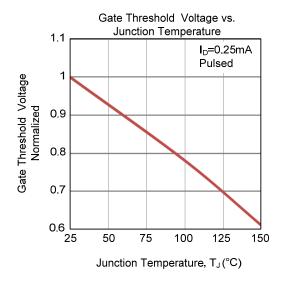


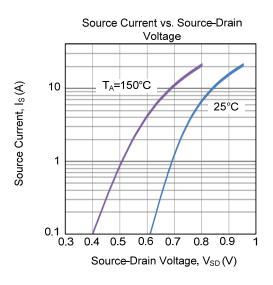


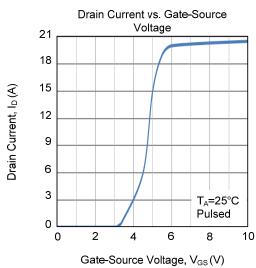


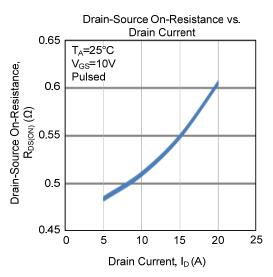


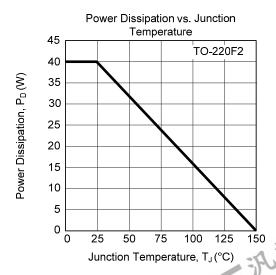
■ TYPICAL CHARACTERISTICS (Cont.)

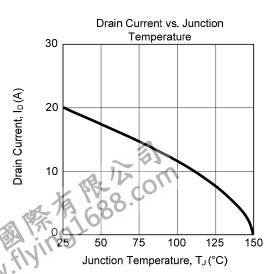




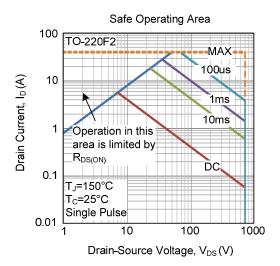








■ TYPICAL CHARACTERISTICS (Cont.)



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