UNISONIC TECHNOLOGIES CO., LTD

21NM50 Preliminary Power MOSFET

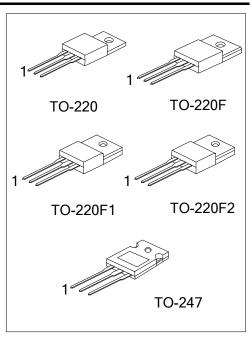
21A, 500V N-CHANNEL SUPER-JUNCTION MOSFET

DESCRIPTION

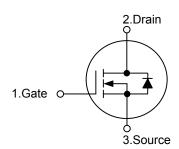
The **UTC 21NM50** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at DC-DC, AC-DC converters for power applications.

■ FEATURES

- * $R_{DS(ON)}$ < 0.22 Ω @ V_{GS} =10V, I_{D} =10.5A
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability, High Ruggedness



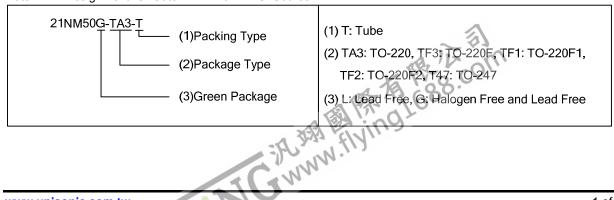
■ SYMBOL



■ ORDERING INFORMATION

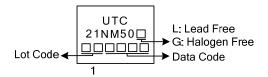
Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
21NM50L-TA3-T	21NM50G-TA3-T	TO-220	G	D	S	Tube	
21NM50L-TF3-T	21NM50G-TF3-T	TO-220F	G	D	S	Tube	
21NM50L-TF1-T	21NM50G-TF1-T	TO-220F1	G	D	S	Tube	
21NM50L-TF2-T	21NM50G-TF2-T	TO-220F2	G	D	S	Tube	
21NM50L-T47-T	21NM50G-T47-T	TO-247	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



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MARKING





ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	500	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous	Ι _D	21	Α
	Pulsed (Note 2)	I_{DM}	84	Α
Avalanche Current (Note 2)		I_{AR}	6.6	Α
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	370	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	10	V/ns
Power Dissipation	TO-220		235	W
	TO-220F/TO-220F1 TO-220F2	P_D	390	W
	TO-247		400	W
Junction Temperature		Τ _J	+150	°C
Storage Temperature		T_{STG}	-55 ~ + 150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L = 17mH, I_{AS} = 6.6A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C
- 4. $I_{SD} \le 21A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2	θ_{JA}	62.5	°C/W
	TO-247		40	°C/W
Junction to Case	TO-220		0.53	°C/W
	TO-220F/TO-220F1 TO-220F2	θЈС	5	°C/W
	TO-247		0.31	°C/W



ELECTRICAL CHARACTERISTICS (T_J =25°C, unless otherwise specified)

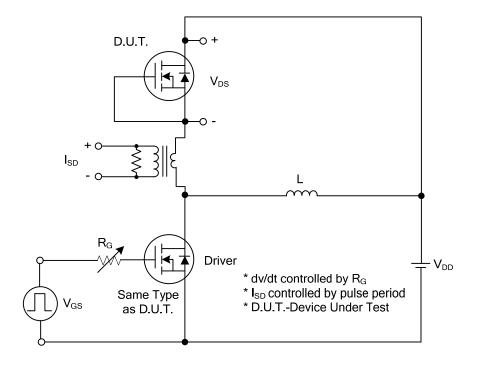
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250μA	500			V	
Drain-Source Leakage Current		I_{DSS}	V _{DS} =500V, V _{GS} =0V			25	μA	
Gate-Body Leakage Current	Forward	_	V_{DS} =0V, V_{GS} =30V			+100	nA	
	Reverse	I_{GSS}	V_{DS} =0V, V_{GS} =-30V			-100	nA	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2.5		4.5	V	
Static Drain-Source On-Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =10.5A			0.22	Ω	
DYNAMIC PARAMETERS					-	-	_	
Input Capacitance		C_{ISS}			1030		pF	
Output Capacitance		Coss	V_{GS} =0V, V_{DS} =25V, f=1MHz		850		pF	
Reverse Transfer Capacitance		C_{RSS}			78		pF	
SWITCHING PARAMETERS				-	-	_		
Total Gate Charge (Note 1)		Q_G	V _{GS} =10V, V _{DS} =50V,		120		nC	
Gate to Source Charge		Q_GS	I _D =1.3A, I _G =100μA		10		nC	
Gate to Drain Charge		Q_GD	(Note 1, 2)		40		nC	
Turn-ON Delay Time (Note 1)		$t_{D(ON)}$	\/ =10\/ \/ =20\/		70		ns	
Rise Time		t_R	V_{GS} =10V, V_{DS} =30V, I_{D} =0.5A, R_{G} =25 Ω		180		ns	
Turn-OFF Delay Time		$t_{D(OFF)}$	(Note 1, 2)		330		ns	
Fall-Time		t_{F}	(14016-1, 2)		200		ns	
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Maximum Body-Diode Continuous Current		I_S	V _{GS} =0V			21	Α	
Maximum Body-Diode Pulsed Cu	urrent	I_{SM}	Repetitive			84	Α	
Drain-Source Diode Forward Vol	rain-Source Diode Forward Voltage (Note 1)		I _F =I _S ,V _{GS} =0V			1.4	V	
Body Diode Reverse Recovery T	ime (Note 1)	t _{rr}	V_{GS} =0V, dI_F/dt =100A/ μ s,		420		ns	
Body Diode Reverse Recovery Charge		Qrr	I _S =21A, V _R =100V 7.1		7.1		μC	

Notes: 1. Pulse Test : Pulse width ≤ 300µs, Duty cycle ≤ 2%.

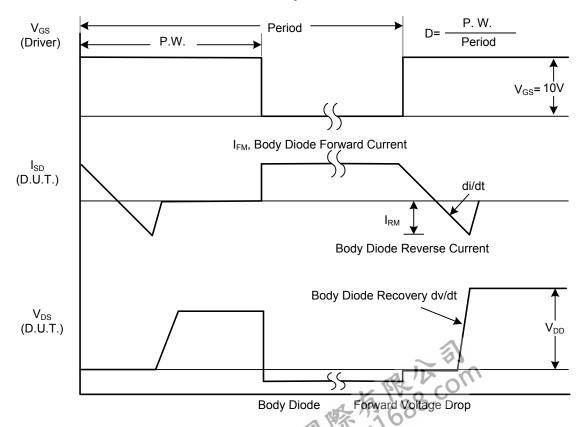


^{2.} Essentially independent of operating ambient temperature.

TEST CIRCUITS AND WAVEFORMS

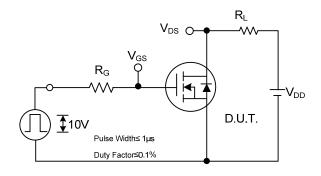


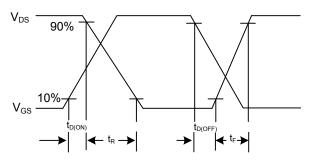
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

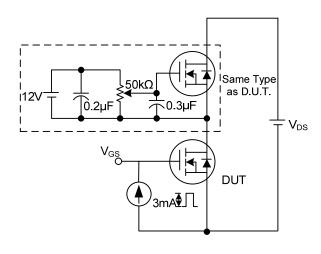
TEST CIRCUITS AND WAVEFORMS (Cont.)

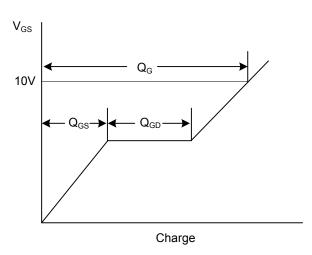




Switching Test Circuit

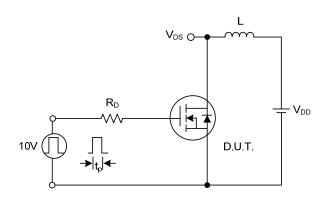
Switching Waveforms

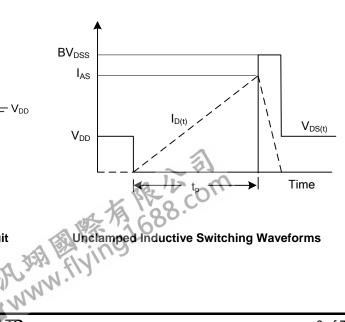




Gate Charge Test Circuit

Gate Charge Waveform





Unclamped Inductive Switching Test Circuit

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