

2N7002

N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
60V	7.5Ω @ V _{GS} = 5V	210mA

Description and Applications

This MOSFET has been designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor Control
- Power Management Functions

Features and Benefits

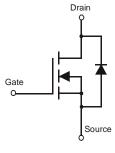
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- · Fast Switching Speed
- Small Surface Mount Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Notes 3)
- The 2N7002Q is suitable for automotive applications requiring specific change control; it is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.
- https://www.diodes.com/quality/product-definitions/

Mechanical Data

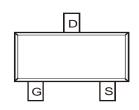
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads. Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.009 grams (Approximate)







Equivalent Circuit



Top View

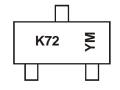
Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
2N7002-7-F	Standard	SOT23	3,000/Tape & Reel
2N7002-13-F	Standard	SOT23	10,000/Tape & Reel
2N7002Q-7-F	Automotive	SOT23	3,000/Tape & Reel

Notes

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- $4. For packaging details, go to our website at \ https://www.diodes.com/design/support/packaging/diodes-packaging/.$

Marking Information



K72 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: G = 2019) M = Month (ex: 9 = September)

Date Code Key

Year	2002	~	2018	2019	2020	202	21 2	2022	2023	2024	2025	2026
Code	N	~	F	G	Н			J	K	L	М	Ν
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage			V _{DSS}	60	V
Drain-Gate Voltage R _{GS} ≤ 1.0MΩ			V_{DGR}	60	V
Gate-Source Voltage	V _{GSS}	±20 ±40	V		
Continuous Drain Current (Note 5) V _{GS} = 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +85^{\circ}C$ $T_A = +100^{\circ}C$	I _D	170 120 105	mA
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +85^{\circ}C$ $T_A = +100^{\circ}C$	l _D	210 150 135	mA
Maximum Continuous Body Diode Forward Current (Note 6) Pulsed Continuous			Is	0.5 2	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 19	%)		I _{DM}	800	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Power Dissipation	(Note 5))	370	mW	
Total Fower Dissipation	(Note 6)	P _D	540		
Thermal Desigtance, Junction to Ambient	(Note 5)	7	348		
Thermal Resistance, Junction to Ambient	(Note 6)	R _{0JA}	241	°C/W	
Thermal Resistance, Junction to Case	(Note 6)	R _{0JC}	91		
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C	

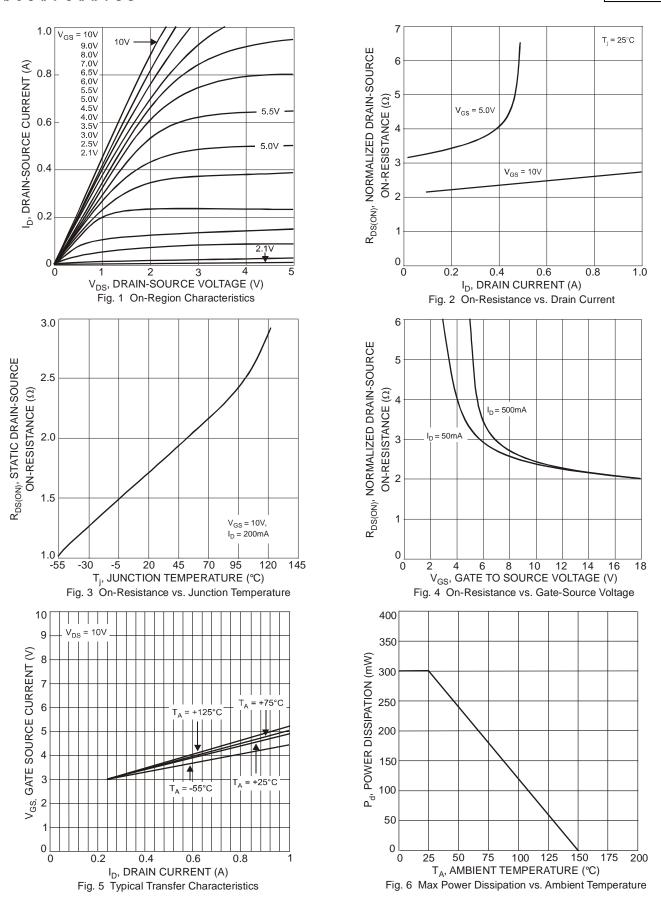
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)					l		•
Drain-Source Breakdown Voltage		BV _{DSS}	60	70		V	$V_{GS} = 0V, I_{D} = 10\mu A$
Zero Gate Voltage Drain Current	@ T _C = +25°C @ T _C = +125°C	I _{DSS}	_	_	1.0 500	μΑ	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Body Leakage		I _{GSS}	_	_	±10	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage		V _{GS(TH)}	1.0	_	2.5	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance	 @ T_J = +25°C @ T_J = +25°C @ T_J = +125°C 	R _{DS(ON)}	_	3.2 — 4.4	7.5 5.0 13.5	Ω	$V_{GS} = 5.0V$, $I_D = 0.05A$ $V_{GS} = 10V$, $I_D = 0.5A$ $V_{GS} = 10V$, $I_D = 0.5A$
On-State Drain Current		I _{D(ON)}	0.5	1.0	_	Α	$V_{GS} = 10V, V_{DS} = 7.5V$
Forward Transconductance		g _{FS}	80	_	_	mS	$V_{DS} = 10V, I_D = 0.2A$
Diode Forward Voltage		V _{SD}	_	0.78	1.5	V	$V_{GS} = 0V, I_{S} = 115mA$
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance		C _{iss}		22	50	pF)/ OF)/)/ O)/
Output Capacitance		Coss		11	25	pF	$V_{DS} = 25V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance		C_{rss}		2.0	5.0	pF	1 = 1.0W112
Gate Resistance		R_{g}	_	120	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$
Total Gate Charge (V _{GS} = 4.5V)		Qg	_	223	_		
Gate-Source Charge		Q _{gs}	_	82	_	рC	$V_{DS} = 10V, I_{D} = 250mA$
Gate-Drain Charge		Q _{gd}	_	178	_		
SWITCHING CHARACTERISTICS (Note 8)							
Turn-On Delay Time		t _{D(ON)}	_	2.8	_		V 20V I 0.2A
Turn-On Rise Time		t_R		3.0	_	ns	$V_{DD} = 30V$, $I_D = 0.2A$, $R_L = 150\Omega$, $V_{GEN} = 10V$,
Turn-Off Delay Time		t _{D(OFF)}		7.6	_	113	$R_{\text{GEN}} = 15002$, $V_{\text{GEN}} = 10V$,
Turn-Off Fall Time		t _F	_	5.6	_		11GEN - 2022

Notes:

Device mounted on FR-4 PCB, with minimum recommended pad layout.
 Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.



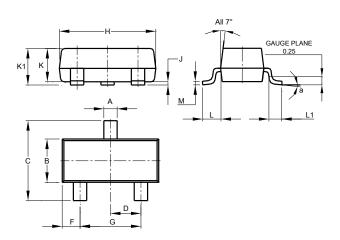




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

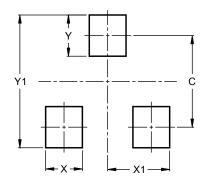


SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
M	0.085	0.150	0.110			
а	0°	8°				
All	All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9



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