UNISONIC TECHNOLOGIES CO., LTD

3N70 **Power MOSFET**

3A, 700V N-CHANNEL **POWER MOSFET**

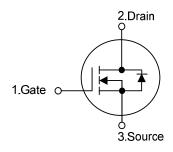
DESCRIPTION

The UTC 3N70 is a high voltage and high current power MOSFET, designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

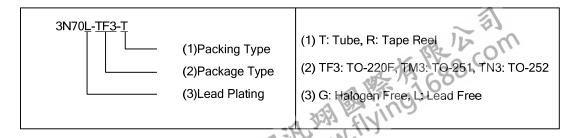
- * $R_{DS(ON)} \le 4.0\Omega$ @ $V_{GS} = 10 \text{ V}$
- * Ultra low gate charge (typical 10 nC)
- * Low reverse transfer capacitance
- * Fast switching capability
- * Avalanche energy specified
- * Improved dv/dt capability, high ruggedness

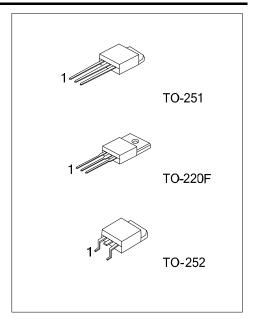
SYMBOL



ORDERING INFORMATION

Ordering Number		Dealtage	Pin Assignment			Deakins	
Lead Free	Halogen Free	Package	1	2	3	Packing	
3N70L-TF3-T	3N70G-TF3-T	TO-220F	G	D	S	Tube	
3N70L-TM3-T	3N70G-TM3-T	TO-251	G	D	S	Tube	
3N70L-TN3-T	3N70G-TN3-T	TO-252	G	D	S	Tube	
3N70L-TN3-R	3N70G-TN3-R	TO-252	G	D	S	Tape Reel	





■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V_{DSS}	700	٧	
Gate-Source Voltage		V_{GSS}	±30	V	
Avalanche Current (Note 2)		I _{AR}	3.0	Α	
Continuous Drain Current		I _D	3.0	Α	
Pulsed Drain Current (Note 2)		I _{DM}	12	Α	
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	200	mJ	
	Repetitive (Note 2)	E _{AR}	7.5	mJ	
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns	
Power Dissipation	TO-220F	ם	33	W	
	TO-251/TO-252	P _D	50	W	
Junction Temperature		T_J	+150	°C	
Operating Temperature		T _{OPR}	-55 ~ +150	°C	
Storage Temperature		T _{STG}	-55 ~ + 150	Ŝ	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature
- 3. L = 64mH, I_{AS} = 3A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C
- 4. $I_{SD} \le 3.0A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25$ °C

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT	
Junction to Ambient	TO-220F	0	62.5	°C/W	
	TO-251/TO-252	θ_{JA}	110		
Junction to Case	TO-220F	0	3.68	°C/W	
	TO-251/TO-252	θις	2.5		



ELECTRICAL CHARACTERISTICS (T_C =25°C, unless otherwise specified)

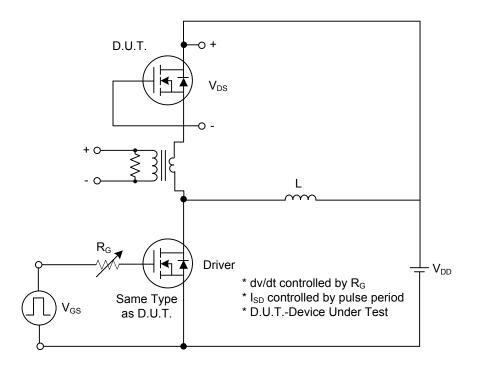
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	700			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} = 700 V, V _{GS} = 0 V			10	μΑ
	Forward		V _{GS} = 30 V, V _{DS} = 0 V			100	nΑ
Gate-Source Leakage Current	Reverse	I_{GSS}	$V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nΑ
Breakdown Voltage Temperature Coefficient		$\triangle BV_{DSS}/\triangle T_{J}$	I_D = 250µA,Referenced to 25°C		0.6		V/°C
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
Static Drain-Source On-State Resi	stance	R _{DS(ON)}	$V_{GS} = 10 \text{ V}, I_D = 1.5 \text{A}$		3.1	4.0	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C _{ISS}	.,		350	450	pF
Output Capacitance		Coss	V _{DS} = 25 V, V _{GS} = 0 V, If = 1MHz		50	65	pF
Reverse Transfer Capacitance		C_{RSS}	T = TMH2		5.5	32	pF
SWITCHING CHARACTERISTICS	3						
Turn-On Delay Time		$t_{D(ON)}$]		10	40	ns
Turn-On Rise Time		t_R	$V_{DD} = 30V, I_D = 3.0A,$		30	70	ns
Turn-Off Delay Time		t _{D(OFF)}	$R_G = 25\Omega$ (Note 1, 2)		20	100	ns
Turn-Off Fall Time		t_{F}			30	70	ns
Total Gate Charge		Q_G	V _{DS} = 480V,I _D = 3.0A,		10	13	nC
Gate-Source Charge		Q_GS			2.7		nC
Gate-Drain Charge		Q_{DD}	V _{GS} = 10 V (Note 1, 2)		4.9		nC
SOURCE- DRAIN DIODE RATING	S AND CH	HARACTERIS	STICS				
Drain-Source Diode Forward Voltage		V_{SD}	$V_{GS} = 0 \text{ V}, I_{S} = 3.0 \text{ A}$			1.4	V
Maximum Continuous Drain-Source Diode						3.0	Α
Forward Current		Is				3.0	А
Maximum Pulsed Drain-Source Diode		I _{SM}				12	Α
Forward Current						12	А
Reverse Recovery Time		t _{rr}	$V_{GS} = 0 \text{ V}, I_{S} = 3.0 \text{ A},$		210		ns
Reverse Recovery Charge		Q_{RR}	dI _F /dt = 100 A/μs (Note 1)		1.2		μC

Notes: 1. Pulse Test: Pulse width≤300µs, Duty cycle≤2%

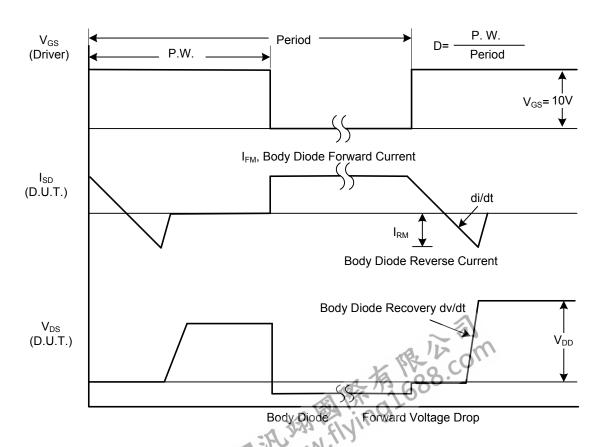


^{2.} Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

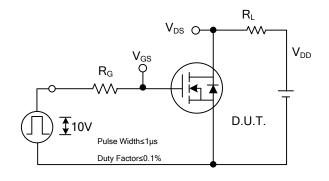


Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

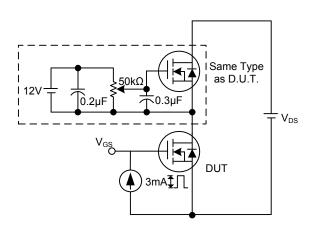
TEST CIRCUITS AND WAVEFORMS (Cont.)

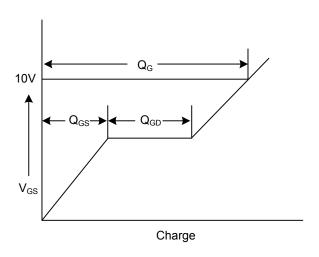


 $V_{\text{DS}} \\$ 90% 10%

Switching Test Circuit

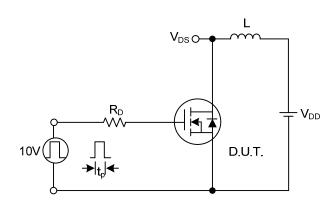
Switching Waveforms

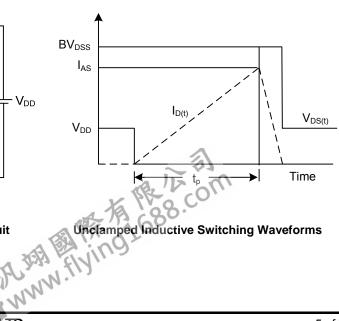




Gate Charge Test Circuit

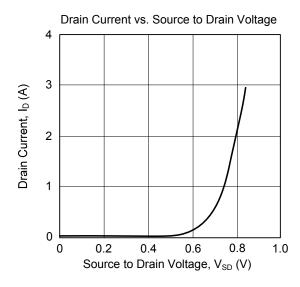
Gate Charge Waveform

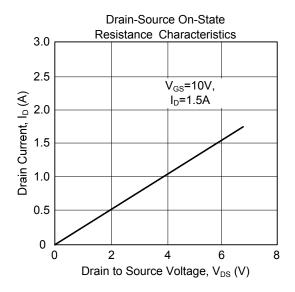


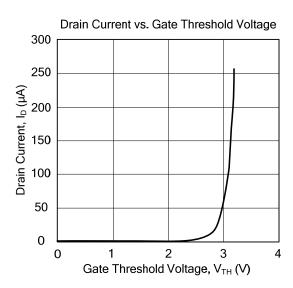


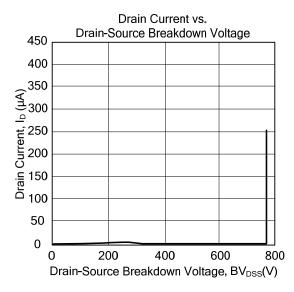
Unclamped Inductive Switching Test Circuit

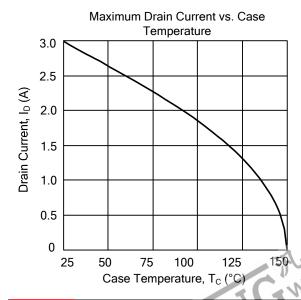
■ TYPICAL CHARACTERISTICS











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