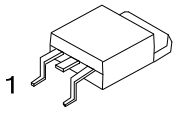
3NM65-FD

Power MOSFET

3A, 650V N-CHANNEL SUPER-JUNCTION MOSFET



TO-252

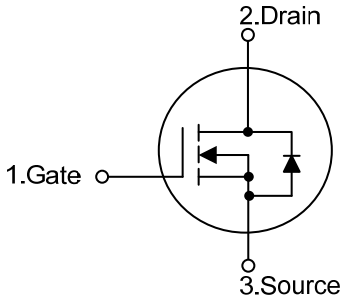
■ DESCRIPTION

The UTC **3NM65-FD** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

■ FEATURES

* $R_{DS(ON)}$ < 2.0Ω @ V_{GS}=10V, I_D=1.5A
* Fast switching capability
* Avalanche energy specified
* Improved dv/dt capability, high ruggedness

■ SYMBOL



■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
3NM65L-TN3-R	3NM65G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

3NM65G-TN3-R (1)Packing Type (2)Package Type (3)Green Package	(1) R: Tape Reel (2) TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free

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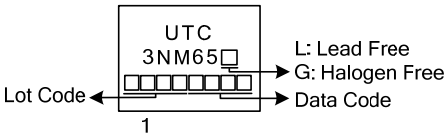
■ MARKING



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■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	650	V
Gate-Source Voltage		V_{GSS}	±30	V
Continuous Drain Current		I_D	3	A
Pulsed Drain Current (Note 2)		I_{DM}	9	A
Avalanche Energy (Note 3)	Single Pulsed	E_{AS}	100	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	6	V/ns
Power Dissipation		P_D	50	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. L=138mH, I_{AS}=1.2A, V_{DD}=50V, R_G=25 Ω, Starting T_J = 25°C
4. I_{SD}≤3.0A, di/dt≤200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	110	°C/W
Junction to Case	θ_{JC}	2.5	°C/W

■ ELECTRICAL CHARACTERISTICS (T_C =25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	V_{GS} = 0V, I_D = 250μA	650			V
Drain-Source Leakage Current		I_{DSS}	V_{DS} = 650V, V_{GS} = 0V			10	μA
Gate-Source Leakage Current	Forward	I_{GSS}	V_{GS} = 30V, V_{DS} = 0V			100	nA
	Reverse		V_{GS} = -30V, V_{DS} = 0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS} = V_{GS}, I_D = 250μA	2.5		4.5	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS} = 10V, I_D =1.5A			2.0	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C_{ISS}	V_{DS} =25V, V_{GS} =0V, f =1MHz		210		pF
Output Capacitance		C_{OSS}			170		pF
Reverse Transfer Capacitance		C_{RSS}			20		pF
SWITCHING CHARACTERISTICS							
Turn-On Delay Time		$t_{D\ (ON)}$	V_{DD} =300V, V_{GS}=10V, I_D =3.0A, R_G=25Ω (Note 1, 2)		6.4		ns
Turn-On Rise Time		t_R			90		ns
Turn-Off Delay Time		$t_{D(OFF)}$			20		ns
Turn-Off Fall Time		t_F			18		ns
DRAIN-SOURCE DIODE CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I_S				3	A
Continuous Drain-Source Current		I_{SD}				9	A
Drain-Source Diode Forward Voltage		V_{SD}	I_S=3.0A, V_{GS}=0V			1.4	V
Reverse Recovery Time		t_{rr}	I_F=3.0A, V_{DD}=100V di/dt = 100A/μs		120		ns
Reverse Recovery Charge		Q_{rr}			0.6		μC

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle≤2%.
2. Essentially independent of operating temperature.

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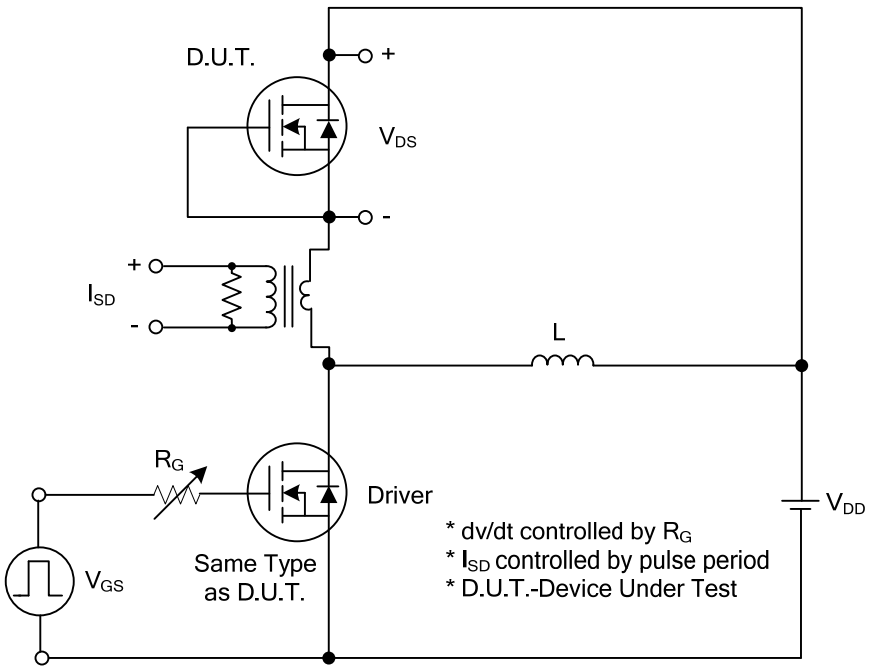
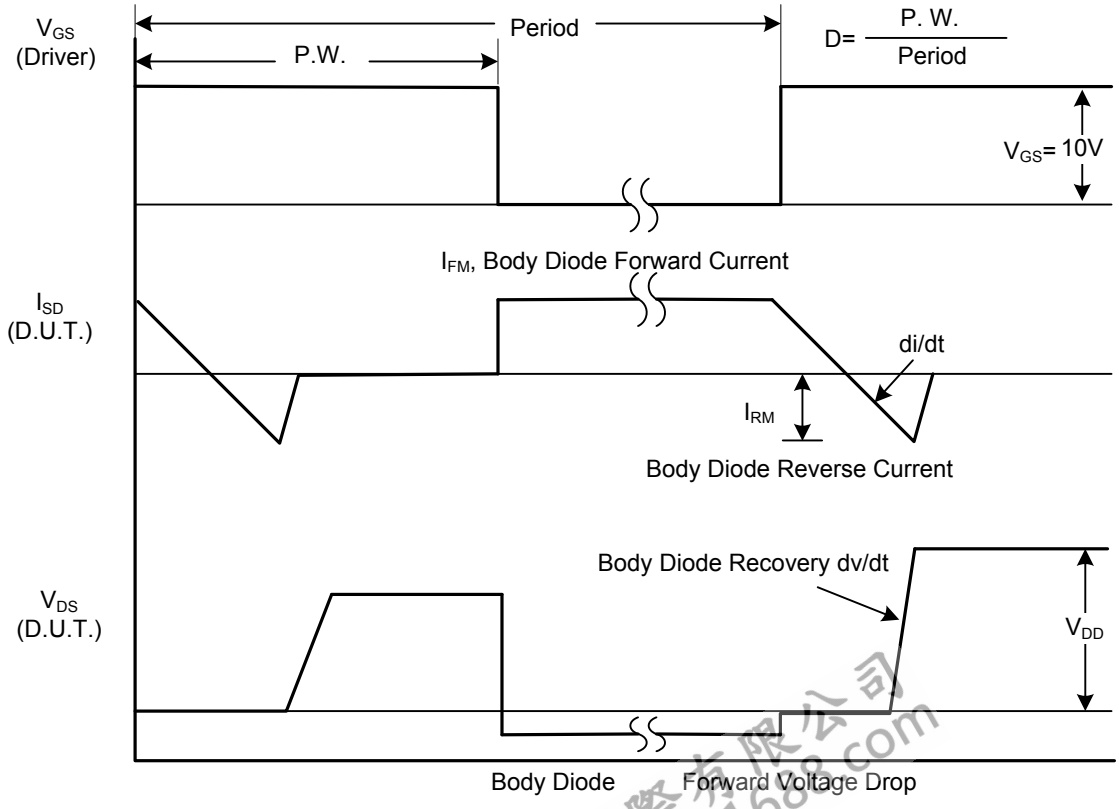
■ TEST CIRCUITS AND WAVEFORMS



Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

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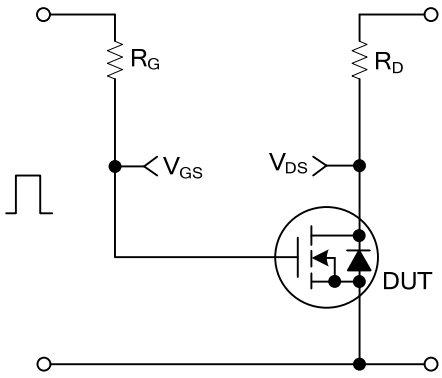
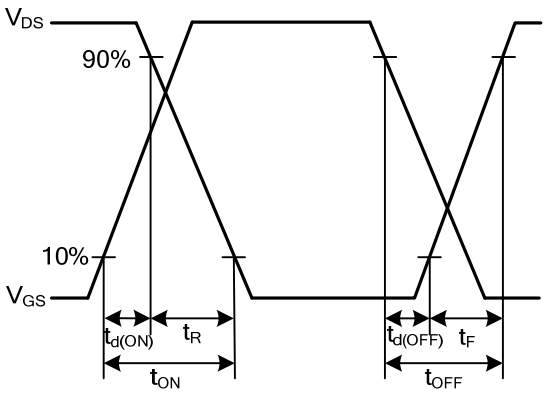
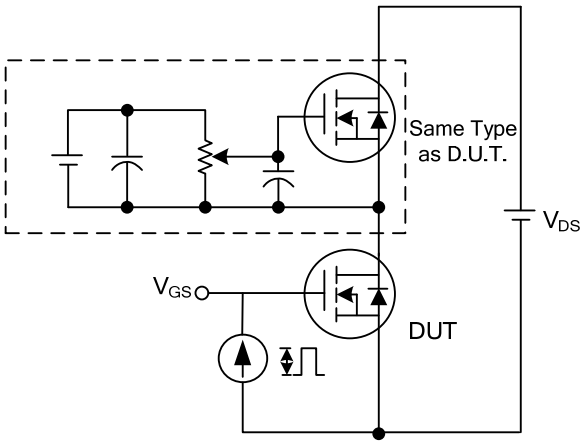
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



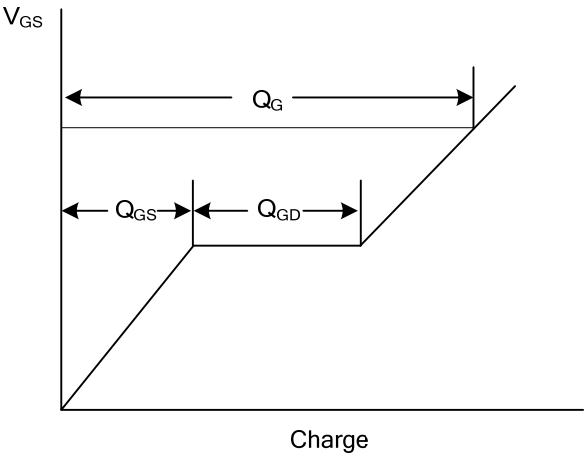
itching Test Circuit



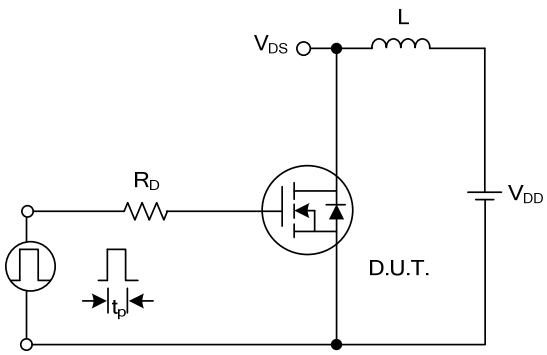
Switching Waveforms



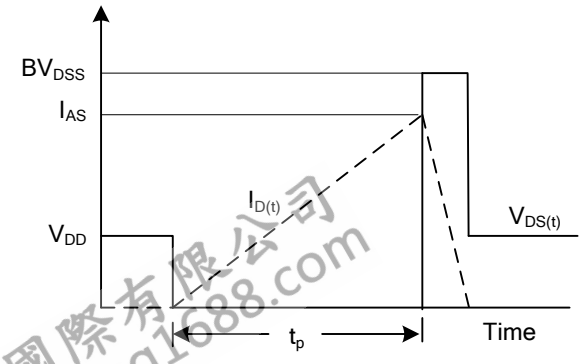
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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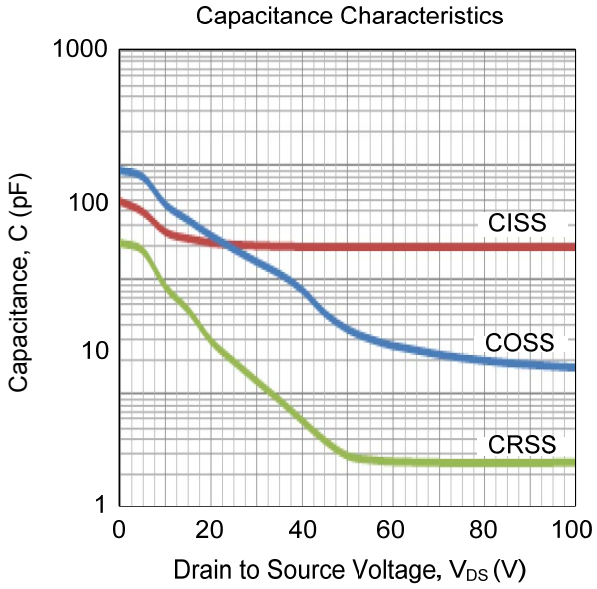
■ TYPICAL CHARACTERISTICS



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