

UTC UNISONIC TECHNOLOGIES CO., LTD

4N60-C

4A, 600V N-CHANNEL **POWER MOSFET**

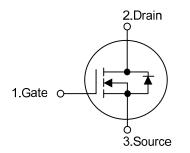
DESCRIPTION

The UTC 4N60-C is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

- * $R_{DS(ON)}$ < 2.5 Ω @V_{GS} = 10 V
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability, high Ruggedness

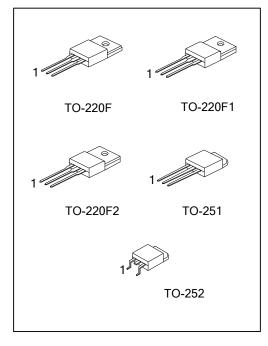
SYMBOL



ORDERING INFORMATION

Lead Free Halogen Free Package 1 2 3 Packing 4N60L-TF3-T 4N60G-TF3-T TO-220F G D S Tube 4N60L-TF1-T 4N60G-TF1-T TO-220F1 G D S Tube 4N60L-TF2-T 4N60G-TF2-T TO-220F2 G D S Tube 4N60L-TM3-T 4N60G-TM3-T TO-220F2 G D S Tube 4N60L-TM3-T 4N60G-TM3-T TO-251 G D S Tube 4N60L-TN3-R 4N60G-TN3-R TO-252 G D S Tape Reel Note: Pin Assignment: G: Gate D: Drain S: Source (1) T: Tube (2) Package Type (2) Package Type (2) Package Type (3) Lead Free (3) L: Lead Free, G: Halogen Free	Ordering Number		Deekage	Pin Assignment			Deeking	
4N60L-TF1-T 4N60G-TF1-T TO-220F1 G D S Tube 4N60L-TF2-T 4N60G-TF2-T TO-220F2 G D S Tube 4N60L-TM3-T 4N60G-TM3-T TO-251 G D S Tube 4N60L-TM3-T 4N60G-TM3-T TO-251 G D S Tube 4N60L-TN3-R 4N60G-TN3-R TO-252 G D S Tape Reel Note: Pin Assignment: G: Gate D: Drain S: Source (1) T: Tube (2) TF3: TO-220F, TF1: TO-220F1, TF2: TO-220F2 (2)Package Type (3)Lead Free (3)Lead Free (1) T: Lead Free, G: Halogen Free	Lead Free	Halogen Free	Package	1	2	3	Packing	
ANGOL +TF2-T ANGOG-TF2-T TO-220F2 G D S Tube 4N60L-TM3-T 4N60G-TM3-T TO-251 G D S Tube 4N60L-TM3-T 4N60G-TM3-T TO-251 G D S Tube 4N60L-TM3-R 4N60G-TN3-R TO-252 G D S Tape Reel Note: Pin Assignment: G: Gate D: Drain S: Source (1) T: Tube (2) TF3: TO-220F, TF1; TO-220F1, TF2: TO-220F2 4N60L-TF3-T (1)Packing Type (2)Package Type (3)Lead Free (3)Lead Free (1) T: Lead Free, G: Halogen Free	4N60L-TF3-T	4N60G-TF3-T	TO-220F	G	D S ⁻		Tube	
ANGOL H12 H HIGGE H12 H HIGGE H12 H HIGGE H12 H HIGGE H12 H 4N60L-TM3-T 4N60G-TM3-T TO-251 G D S Tube 4N60L-TN3-R 4N60G-TN3-R TO-252 G D S Tape Reel Note: Pin Assignment: G: Gate D: Drain S: Source (1) T: Tube (2) TF3: TO-220F, TF1: TO-220F1, TF2: TO-220F2 (2) Package Type (3) Lead Free (3) Lead Free (3) L: Lead Free, G: Halogen Free	4N60L-TF1-T	4N60G-TF1-T	TO-220F1	G	D	S	Tube	
4N60L-TN3-R 4N60G-TN3-R TO-252 G D S Tape Reel Note: Pin Assignment: G: Gate D: Drain S: Source (1) T: Tube (2) TF3: TO-220F, TF1: TO-220F1, TF2: TO-220F2 (1) T: Tube (2) TF3: TO-251, TN3: TO-251, TN3: TO-252 (3) Lead Free (3) Lead Free	4N60L-TF2-T	4N60G-TF2-T	TO-220F2	G	D	S	Tube	
Note: Pin Assignment: G: Gate D: Drain S: Source 4N60L-TF3-T (1)Packing Type (1)Package Type (2)Package Type (2)Package Type (3)Lead Free (3)Lead Free (3)Lead Free (3)Lead Free	4N60L-TM3-T	4N60G-TM3-T	TO-251	G	D	S	Tube	
4N60L-TF3-T (1)Packing Type (2)Package Type (2)Package Type (3)Lead Free (3)Lead Free	4N60L-TN3-R	4N60G-TN3-R	TO-252	G	D	S	Tape Reel	
(1)Packing Type (2)Package Type (3)Lead Free (3)Lead Free (1)Packing Type (2) TF3: TO-220F, TF1: TO-220F1, TF2: TO-220F2 TM3: TO-251, TN3: TO-252 (3) L: Lead Free, G: Halogen Free	Note: Pin Assignment: G: G	ate D: Drain S: Source			~			
	4N60L-TF3-T (1)Packing Type (2)Package Type (3)Lead Free (1) T: Tube (1) T: Tube (2) TF3: TO-220F, TF1; TO-220F1, TF2: TO-220F2 TM3: TO-251, TN3: TO-252 (3) L: Lead Free, G: Halogen Free							

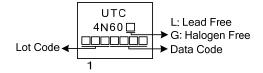
Power MOSFET



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4N60-C

MARKING





PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	600	V
Gate-Source Voltage		V _{GSS}	±30	V
Avalanche Current (No	te 2)	I _{AR}	4.4	А
Desire Ourset	Continuous	I _D	4.0	A
Drain Current	Pulsed (Note 2)	I _{DM}	16	А
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	240	mJ
	Repetitive (Note 2)	E _{AR}	10.6	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220F/TO-220F1 TO-220F2	PD	36	W
	TO-251/TO-252		50	W
Junction Temperature		TJ	+150	°C
Operating Temperature		T _{OPR}	-55 ~ +150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

■ ABSOLUTE MAXIMUM RATINGS (T_c = 25°C, unless otherwise specified)

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied. 2. Repetitive Rating : Pulse width limited by maximum junction temperature

- 3. L = 30mH, I_{AS} = 4A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C
- 4. $I_{SD} \le 4.4A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient	TO-220F/TO-220F1 TO-220F2	θ _{JA}	62.5	°C/W	
	TO-251/TO-252		110	°C/W	
Junction to Case	TO-220F/TO-220F1		3.47	°C/W	
	TO-220F2	θ」	3.28	°C/W	
	TO-251/TO-252		2.5	°C/W	



ELECTRICAL CHARACTERISTICS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} = 0V, I _D = 250µA	600			V
¥			$V_{DS} = 600V, V_{GS} = 0V$			10	μA
Drain-Source Leakage Current		I _{DSS}	$V_{DS} = 480V, V_{GS} = 0V,$			100	
		-200	$T_{C} = 125^{\circ}C$			100	μA
	Forward		V _{GS} = 30V, V _{DS} = 0V			100	nA
Gate-Source Leakage Current	Reverse	I _{GSS}	V _{GS} = -30V, V _{DS} = 0V			-100	nA
Breakdown Voltage Temperature	Coefficient	$\bigtriangleup BV_{DSS} / \bigtriangleup T_J$	I _D =250µA,Referenced to 25°C		0.6		V/°C
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2.0		4.0	V
Static Drain-Source On-State Res	istance	R _{DS(ON)}	V _{GS} = 10 V, I _D = 2.2A		1.9	2.5	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C _{ISS}			600	700	pF
Output Capacitance		C _{OSS}	V _{DS} = 25V, V _{GS} = 0V, f = 1MHz		60	80	pF
Reverse Transfer Capacitance		C _{RSS}			6	15	рF
SWITCHING CHARACTERISTIC	S						
Turn-On Delay Time		t _{D(ON)}			35	55	ns
Turn-On Rise Time		t _R	$V_{DD} = 300V, I_D = 4.0A,$		55	80	ns
Turn-Off Delay Time		$t_{D(OFF)}$	R _G = 25Ω (Note 1, 2)		100	130	ns
Turn-Off Fall Time		t _F			40	60	ns
Total Gate Charge		Q_{G}	V _{DS} = 480V,I _D = 4.0A,		20	50	nC
Gate-Source Charge		Q _{GS}	V_{GS} = 10V (Note 1, 2)		5		nC
Gate-Drain Charge		Q_{GD}	v_{GS} = 100 (Note 1, 2)		3		nC
SOURCE- DRAIN DIODE RATIN	GS AND CI	HARACTERIS	rics			i	•
Drain-Source Diode Forward Voltage		V _{SD}	V _{GS} = 0V, I _S = 4.4A			1.4	V
Maximum Continuous Drain-Source Diode		I _{SD}				4.4	А
Forward Current		ISD				т.т	~
Maximum Pulsed Drain-Source Diode		I _{SM}				17.6	А
Forward Current		-				17.5	~
Reverse Recovery Time		trr	$V_{GS} = 0 V, I_S = 4.4A,$		250		ns
Reverse Recovery Charge		Q _{RR}	dI _F /dt = 100 A/µs (Note 1)		1.5		μC

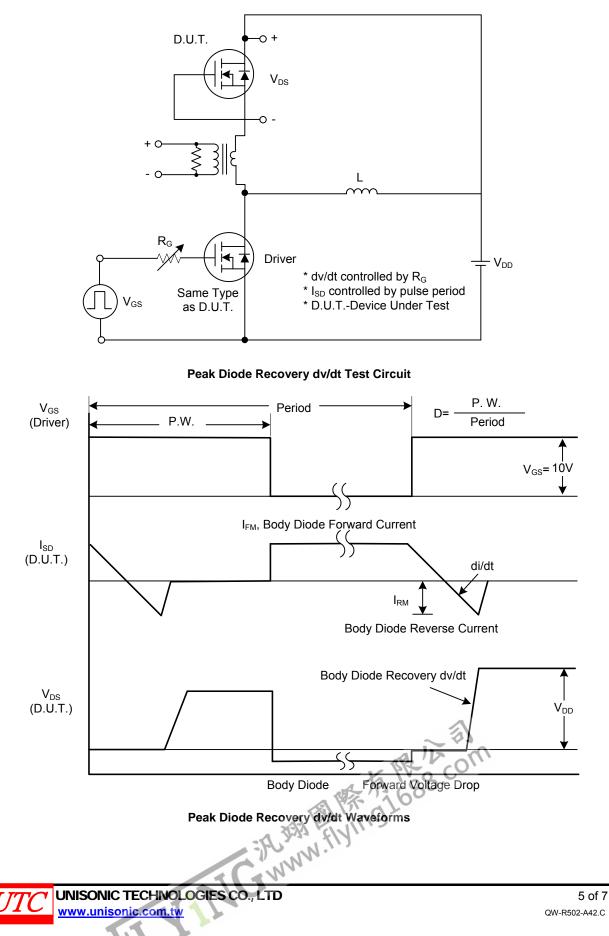
Notes: 1. Pulse Test: Pulse width≤300µs, Duty cycle≤2%

2. Essentially independent of operating temperature



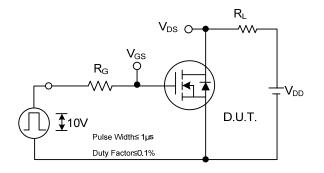
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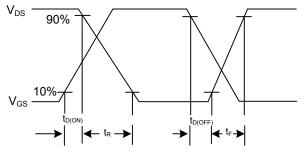
TEST CIRCUITS AND WAVEFORMS



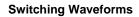
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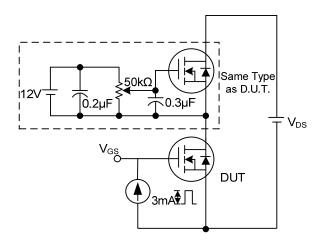
TEST CIRCUITS AND WAVEFORMS (Cont.)



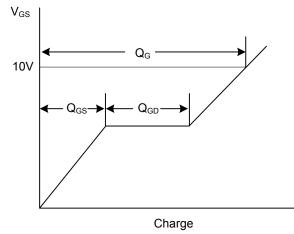


Switching Test Circuit

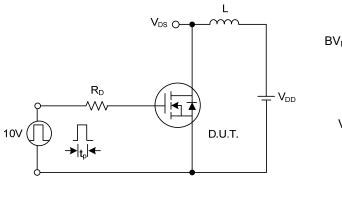


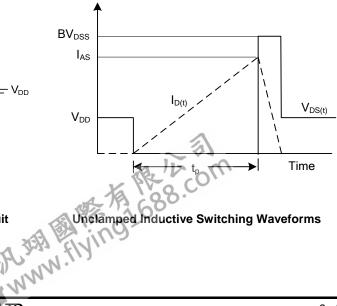


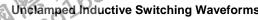
Gate Charge Test Circuit



Gate Charge Waveform



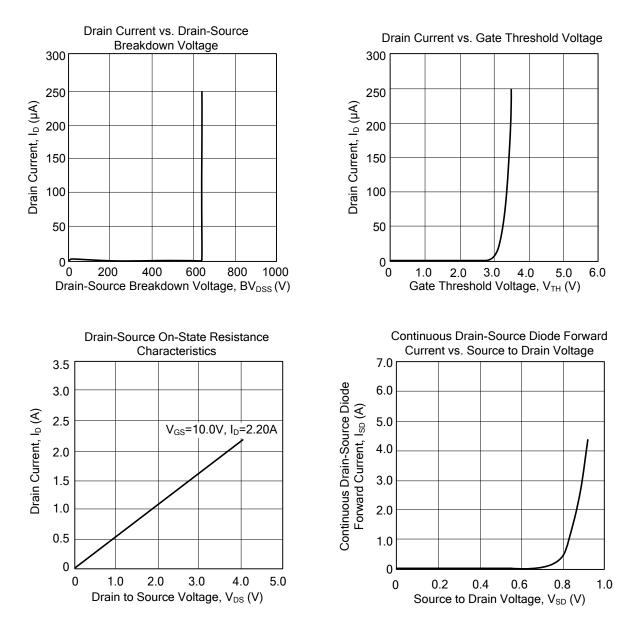




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Unclamped Inductive Switching Test Circuit

TYPICAL CHARACTERISTICS



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