UTC UNISONIC TECHNOLOGIES CO., LTD

4N60-CB Power MOSFET

4.0A, 600V N-CHANNEL **POWER MOSFET**

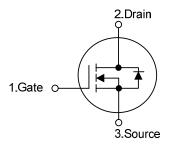
DESCRIPTION

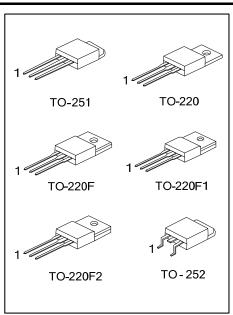
The UTC 4N60-CB is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

- * $R_{DS(ON)}$ < 2.2 Ω @ V_{GS} = 10 V, I_D = 2.0A
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability, high Ruggedness



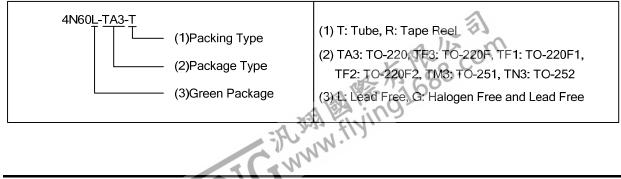




ORDERING INFORMATION

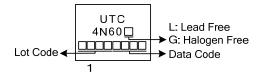
Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
4N60L-TA3-T	4N60G-TA3-T	TO-220	G	D	S	Tube	
4N60L-TF1-T	4N60G-TF1-T	TO-220F1	G	D	S	Tube	
4N60L-TF3-T	4N60G-TF3-T	TO-220F2	G	D	S	Tube	
4N60L-TF3-T	4N60G-TF3-T	TO-220F	G	D	S	Tube	
4N60L-TM3-T	4N60G-TM3-T	TO-251	G	D	S	Tube	
4N60L-TN3-R	4N60G-TN3-R	TO-252	G	D	S	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



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MARKING





■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL RATINGS		UNIT
Drain-Source Voltage		V _{DSS} 600		V
Gate-Source Voltage		V_{GSS}	±30	V
Avalanche Current (Note 2)		I_{AR}	4.0	Α
Drain Current	Continuous	I_D	4.0	Α
	Pulsed (Note 2)	I _{DM}	16	Α
Avalanche Energy	Single Pulsed (Note 3) E _{AS} 128		mJ	
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation (T _C =25°C)	TO-220		50	W
	TO-220F/TO-220F1	Б.	36	W
	TO-220F2	P_D	38	W
	TO-251/TO-252		50	W
Junction Temperature		TJ	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature
- 3. L = 16mH, I_{AS} = 4.0A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25°C
- 4. $I_{SD} \le 4A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

PARAMETER	PACKAGE	SYMBOL	RATINGS	UNIT	
Junction to Ambient	TO-220/TO-220F		62.5	°C/W	
	TO-220F1/TO-220F2	θ _{JA}	62.5		
	TO-251/TO-252		110	°C/W	
Junction to Case	TO-220		2.5	°C/W	
	TO-220F/TO-220F1	0	3.47	°C/W	
	TO-220F2	θις	3.28	°C/W	
	TO-251/TO-252		2.5	°C/W	



ELECTRICAL CHARACTERISTICS (T_C =25°C, unless otherwise specified)

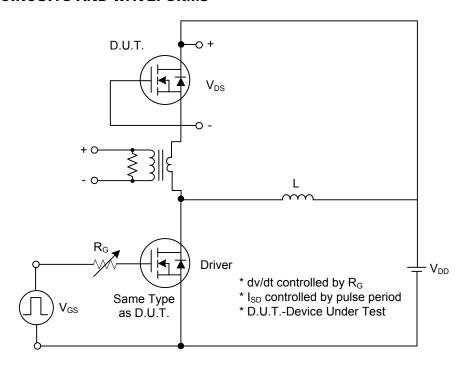
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS				ı			
Drain-Source Breakdown Voltage		BV_{DSS}	V _{GS} =0V, I _D =250μA	600			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =600V, V _{GS} =0V			10	μA
			V _{DS} =600V, V _{GS} =0V, T _C =125°C			10	μA
Gate-Source Leakage Current	Forward ,		V _{GS} =30V, V _{DS} =0V			100	nA
	Reverse	I _{GSS}	V _{GS} = -30V, V _{DS} =0V			-100	nA
Breakdown Voltage Temperature	Coefficient	$\triangle BV_{DSS}/\triangle T_{J}$	I _D =250μA,Referenced to 25°C		0.6		V/°C
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2.0		4.0	V
Static Drain-Source On-State Res	sistance	R _{DS(ON)}	V _{GS} =10 V, I _D =2A			2.2	Ω
DYNAMIC CHARACTERISTICS				-		-	
Input Capacitance		C_{ISS}	$V_{DS} = 25V, V_{GS} = 0V,$		606		pF
Output Capacitance	Output Capacitance		f = 1MHz		54		pF
Reverse Transfer Capacitance		C _{OSS}	1 - 1101112		5		рF
SWITCHING CHARACTERISTIC	S						
Total Gate Charge		Q_G	V _{DS} =50V, V _{GS} =10V, I _D =1.3A ,		37		nC
Gate-Source Charge		Q_GS	I_{G} =100µA (Note 1, 2)		3.0		nC
Gate-Drain Charge	Sate-Drain Charge		IG-100μΑ (Note 1, 2)		2.0		nC
Turn-On Delay Time		$t_{D(ON)}$			40		ns
Turn-On Rise Time		t_R	V_{DD} =30V, V_{GS} =10V, I_{D} =0.5A,		9		ns
Turn-Off Delay Time		$t_{D(OFF)}$	R _G =25Ω (Note 1, 2)		145		ns
Turn-Off Fall Time		t_{F}			26		ns
SOURCE- DRAIN DIODE RATIN	GS AND C	HARACTERIS'	rics				
Maximum Continuous Drain-Source Diode Forward Current		Is				4	Α
						7	^
	Maximum Pulsed Drain-Source Diode					16	Α
Forward Current		I _{SM}					
Drain-Source Diode Forward Voltage		V _{SD}	$V_{GS} = 0V$, $I_S = 4A$			1.4	V
Reverse Recovery Time		t _{rr}	V _{GS} =0V, I _S =4A		250		ns
Reverse Recovery Charge		Q_{rr}	dI _F /dt=100A/μs (Note 1)		0.2		μC

Notes: 1. Pulse Test: Pulse width≤300µs, Duty cycle≤2%

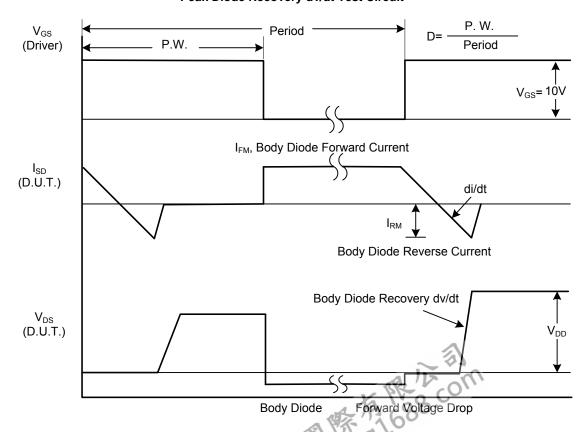
2. Essentially independent of operating temperature



■ TEST CIRCUITS AND WAVEFORMS

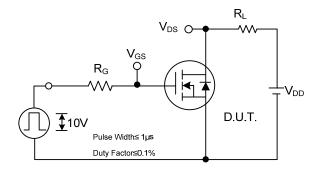


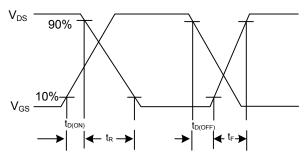
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

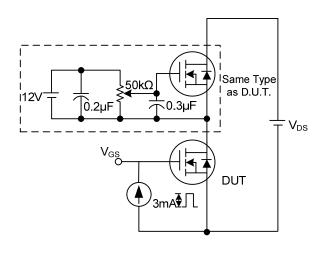
TEST CIRCUITS AND WAVEFORMS (Cont.)

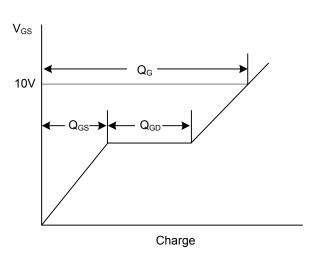




Switching Test Circuit

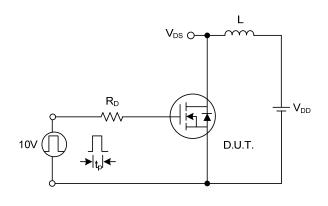
Switching Waveforms

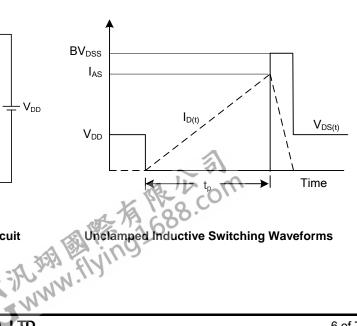




Gate Charge Test Circuit

Gate Charge Waveform





Unclamped Inductive Switching Test Circuit

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