

UNISONIC TECHNOLOGIES CO., LTD

4N60K-MT **Preliminary** Power MOSFET

4.0A, 600V **N-CHANNEL POWER MOSFET**

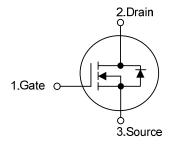
DESCRIPTION

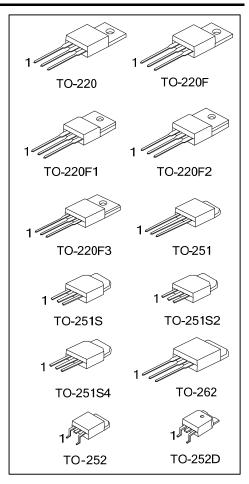
The UTC 4N60K-MT is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

- * $R_{DS(ON)}$ < 2.5 Ω @ V_{GS} = 10 V, I_D = 2.2 A
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability, high Ruggedness

SYMBOL



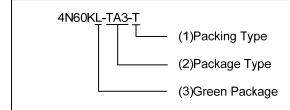


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ORDERING INFORMATION

Ordering Number		Dookago	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
4N60KL-TA3-T	4N60KG-TA3-T	TO-220	G	D	S	Tube	
4N60KL-TF3-T	4N60KG-TF3-T	TO-220F	G	D	S	Tube	
4N60KL-TF1-T	4N60KG-TF1-T	TO-220F1	G	D	S	Tube	
4N60KL-TF2-T	4N60KG-TF2-T	TO-220F2	G	D	S	Tube	
4N60KL-TF3-T	4N60KG-TF3-T	TO-220F3	G	D	S	Tube	
4N60KL-TM3-T	4N60KG-TM3-T	TO-251	G	D	S	Tube	
4N60KL-TMS-T	4N60KG-TMS-T	TO-251S	G	D	S	Tube	
4N60KL-TMS2-T	4N60KG-TMS2-T	TO-251S2	G	D	S	Tube	
4N60KL-TMS4-T	4N60KG-TMS4-T	TO-251S4	G	D	S	Tube	
4N60KL-TN3-R	4N60KG-TN3-R	TO-252	G	D	S	Tape Reel	
4N60KL-TND-R	4N60KG-TND-R	TO-252D	G	D	S	Tape Reel	
4N60KL-T2Q-T	4N60KG-T2Q-T	TO-262	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



- (1) T: Tube, R: Tape Reel
- (2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1, TF2: TO-220F2, TF3: TO-220F3, TM3: TO-251

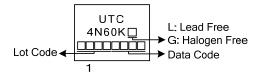
TMS: TO-251S, TMS2: TO-251S2,

TMS4: TO-251S4, TN3: TO-252, TND: TO-252D

T2Q: TO-262

(3) L: Lead Free, G: Halogen Free and Lead Free

MARKING





■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V_{DSS}	600	V	
Gate-Source Voltage		V_{GSS}	±30	V	
Avalanche Current (Note 2)		I_{AR}	4.4	Α	
Drain Current	Continuous	I _D	4.0	Α	
	Pulsed (Note 2)	I_{DM}	16	Α	
Avalanche Energy	Single Pulsed (Note 3)	Single Pulsed (Note 3) E _{AS} 210		mJ	
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns	
Power Dissipation	TO-220/TO-262		106		
	TO-220F/TO-220F1		36	W	
	TO-220F2/TO-220F3	P _D	36		
	TO-251/TO-251S	FD			
	TO-251S2/TO-251S4		50		
	TO-252/TO-252D				
Junction Temperature		T_J	+150	°C	
Operating Temperature		T_OPR	-55 ~ +150	°C	
Storage Temperature		T _{STG}	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating : Pulse width limited by maximum junction temperature
- 3. L = 26.25mH, I_{AS} = 4A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C
- 4. $I_{SD} \le 4.4A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2 TO-220F3/TO-262	0	62.5	°C/W	
	TO-251/TO-251S TO-251S2/TO-251S4 TO-252/TO-252D	$ heta_{ m JA}$	110	°C/W	
Junction to Case	TO-220/TO-262		1.18	°C/W	
	TO-220F/TO-220F1 TO-220F3		3.47	°C/W	
	TO-220F2	θ_{JC}	3.4	°C/W	
	TO-251/TO-251S TO-251S2/TO-251S4 TO-252/TO-252D		2.50	°C/W	



ELECTRICAL CHARACTERISTICS (T_C =25°C, unless otherwise specified)

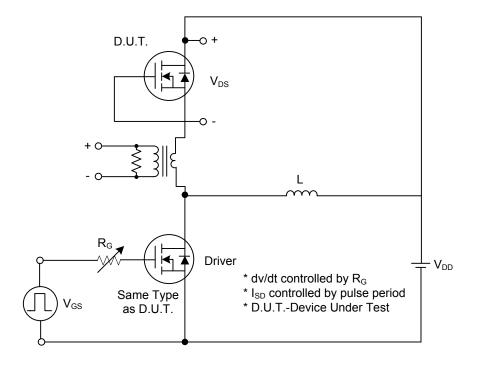
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250μA				V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =600V, V _{GS} =0V			10	μΑ
			V _{DS} =600V, V _{GS} =0V, T _C =125°C			10	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{GS} =30V, V _{DS} =0V			100	nA
	Reverse		V _{GS} = -30V, V _{DS} =0V			-100	nA
Breakdown Voltage Temperature	eakdown Voltage Temperature Coefficient		I _D =250μA,Referenced to 25°C		0.6		V/°C
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_{D}=250\mu A$ 2.0			4.0	V
Static Drain-Source On-State Res	Static Drain-Source On-State Resistance		V _{GS} =10 V, I _D =2.2A		1.79	2.5	Ω
DYNAMIC CHARACTERISTICS				-	-		-
Input Capacitance	Input Capacitance		\\ - 25\\ \\ - 0\\		425	575	pF
Output Capacitance		C _{ISS}	V _{DS} = 25V, V _{GS} = 0V, If = 1MHz		55	75	pF
Reverse Transfer Capacitance		C_{RSS}	T = TIVITZ		6	11	pF
SWITCHING CHARACTERISTIC	S						-
Total Gate Charge		Q_G	V _{DS} = 50V,I _D = 1.3A,		20		nC
Gate-Source Charge		Q_GS	V _{GS} = 50V,I _D = 1.3A, V _{GS} = 10V (Note 1, 2)		5.6		nC
Gate-Drain Charge		Q_GD	VGS- 10V (NOte 1, 2)		4.0		nC
Turn-On Delay Time		t _{D(ON)}			45		ns
Furn-On Rise Time		t_R	$V_{DD} = 30V, I_D = 0.5A,$		49		ns
Turn-Off Delay Time	Furn-Off Delay Time		$R_G = 25\Omega \text{ (Note 1, 2)}$		80		ns
Turn-Off Fall Time		t _F	1		43		ns
SOURCE- DRAIN DIODE RATIN	GS AND CI	HARACTERIS	rics				-
Maximum Continuous Drain-Source	ximum Continuous Drain-Source Diode					4.4	Α
Forward Current		I _S				4.4	А
Maximum Pulsed Drain-Source Diode Forward Current		I _{SM}				17.6	Α
						17.0	^
Drain-Source Diode Forward Voltage		V_{SD}	$V_{GS} = 0V, I_{S} = 4.4A$			1.4	V

Notes: 1. Pulse Test: Pulse width≤300µs, Duty cycle≤2%

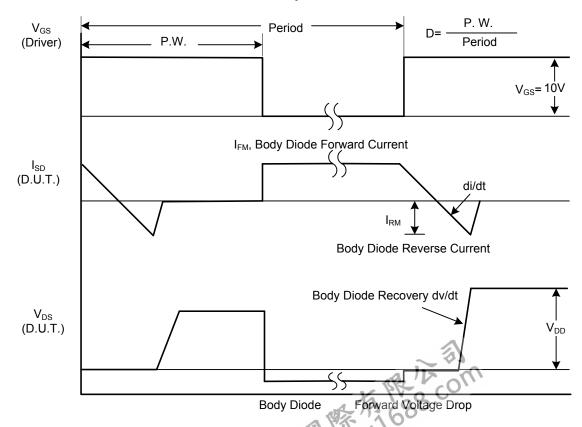


^{2.} Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

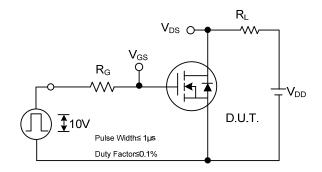


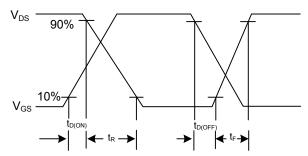
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

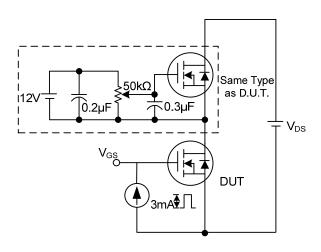
TEST CIRCUITS AND WAVEFORMS (Cont.)

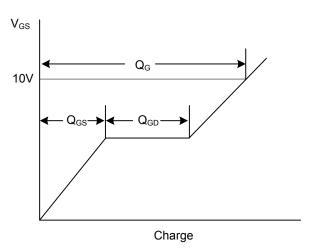




Switching Test Circuit

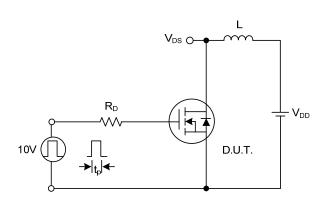
Switching Waveforms

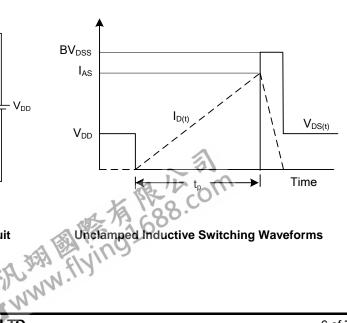




Gate Charge Test Circuit

Gate Charge Waveform





Unclamped Inductive Switching Test Circuit

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