UNISONIC TECHNOLOGIES CO., LTD

4N70K-TA Power MOSFET

4.0A, 700V N-CHANNEL **POWER MOSFET**

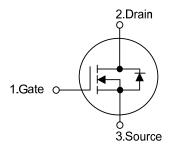
DESCRIPTION

The UTC 4N70K-TA is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche. This high speed switching power MOSFET is usually used in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

- * $R_{DS(ON)}$ < 3.5 Ω @ V_{GS} = 10 V, I_D = 2 A
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability, High Ruggedness

SYMBOL

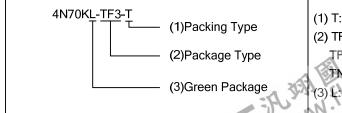


TO-220F TO-220F1 TO-220F2 TO-220F3 TO-251 TO-251S TO-252D TO-262

ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Dealing	
Lead Free	Halogen Free	Package	1	2	3	Packing	
4N70KL-TF3-T	4N70KG-TF3-T	TO-220F	G	D	S	Tube	
4N70KL-TF1-T	4N70KG-TF1-T	TO-220F1	G	D	S	Tube	
4N70KL-TF2-T	4N70KG-TF2-T	TO-220F2	G	D	S	Tube	
4N70KL-TF3T-T	4N70KG-TF3T-T	TO-220F3	G	D	S	Tube	
4N70KL-TM3-T	4N70KG-TM3-T	TO-251	G	D	S	Tube	
4N70KL-TMS-T	4N70KG-TMS-T	TO-251S	G	D	S	Tube	
4N70KL-TN3-R	4N70KG-TN3-R	TO-252	G	D	S	Tape Reel	
4N70KL-TND-R	4N70KG-TND-R	TO-252D	G	D	S	Tape Reel	
4N70KL-T2Q-T	4N70KG-T2Q-T	TO-262	G	D	S	Tube	

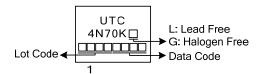
Pin Assignment: G: Gate D: Drain S: Source Note:



- (1) T: Tube, R: Tape Reel
- (2) TF3: TO-220F, TF1: TO-220F1, TF1: TO-220F2 TF3T: TO-220F3, TM3: TO-251, TMS: TO-251S TN3: TO-252, TND: TO-252D, T2Q: TO-262
- (3) L: Lead Free, G: Halogen Free and Lead Free

www.unisonic.com.tw 1 of 7

MARKING





■ **ABSOLUTE MAXIMUM RATINGS** (T_A = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	700	V
Gate-Source Voltage		V_{GSS}	±30	V
Avalanche Current (Note 2)		I _{AR}	4.0	Α
Drain Current	Continuous	I _D	4.0	Α
	Pulsed (Note 2)	I _{DM}	17.6	Α
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	216	mJ
	Repetitive (Note 2)	E _{AR}	10.6	mJ
Power Dissipation	TO-220F/TO-220F1/ TO-220F2/TO-220F3		36	W
	TO-251/TO-251S TO-252/TO-252D	P_D	49	W
	TO-262		106	W
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Junction Temperature		TJ	+150	°C
Operating Temperature		T _{OPR}	-55 ~ +150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature
- 3. L = 27mH, I_{AS} = 4 A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C
- 4. $I_{SD} \le 4.0A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

PARAMETER		SYMBOL RATINGS		UNIT	
Junction to Ambient	TO-220F/TO-220F1/ TO-220F2/TO-220F3 TO-262	$ heta_{ extsf{JA}}$	62.5	°C/W	
	TO-251/TO-251S TO-252/TO-252D		110	°C/W	
Junction to Case	TO-220F/TO-220F1 TO-220F3		3.47	°C/W	
	TO-220F2	0	3.28	°C/W	
	TO-251/TO-251S TO-252/TO-252D	θЈС	2.55	°C/W	
	TO-262		1.18	°C/W	



ELECTRICAL CHARACTERISTICS (T_A =25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS				•	•		
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} = 0 V, I _D = 250 μA	700			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} = 700 V, V _{GS} = 0 V			10	μΑ
Gate-Source Leakage Current	Forward	lcee -	$V_{GS} = 30 \text{ V}, V_{DS} = 0 \text{ V}$			100	nA
	Reverse		$V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$			-100	ΠA
Breakdown Voltage Temperature Coefficient		$\triangle BV_{DSS}/\triangle T_{J}$	$I_D = 250\mu A$, Referenced to 25°C		0.6		V/°C
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$			4.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	$V_{GS} = 10 \text{ V}, I_D = 2 \text{ A}$			3.5	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance	nput Capacitance		-V _{DS} = 25 V, V _{GS} = 0 V, -f = 1MHz		660	760	pF
Output Capacitance		Coss			48	90	pF
Reverse Transfer Capacitance		C_{RSS}			5	11	pF
SWITCHING CHARACTERISTIC	S						
Turn-On Delay Time		$t_{D(ON)}$	$V_{DD} = 30V, I_D = 0.5A,$ $R_G = 25\Omega \text{ (Note 1, 2)}$		45		ns
Turn-On Rise Time		t _R			32		ns
Turn-Off Delay Time		$t_{D(OFF)}$			80		ns
Turn-Off Fall Time		t_{F}			24		ns
Total Gate Charge		Q_G	V _{DS} = 50V, I _D = 1.3A, -V _{GS} = 10 V (Note 1, 2)		37		nC
Gate-Source Charge		Q_GS			4.8		nC
Gate-Drain Charge		Q_GD			6.0		nC
SOURCE- DRAIN DIODE RATIN	GS AND CI	HARACTERIST	rics				
Drain-Source Diode Forward Voltage		V_{SD}	$V_{GS} = 0 \text{ V}, I_{S} = 4.0 \text{ A}$			1.4	V
Maximum Continuous Drain-Source Diode		I _S				4.0	Α
Forward Current		.5					'`
Maximum Pulsed Drain-Source Diode		I _{SM}				17.6	Α
Forward Current							

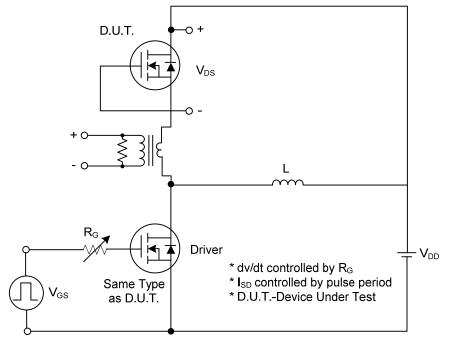
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%



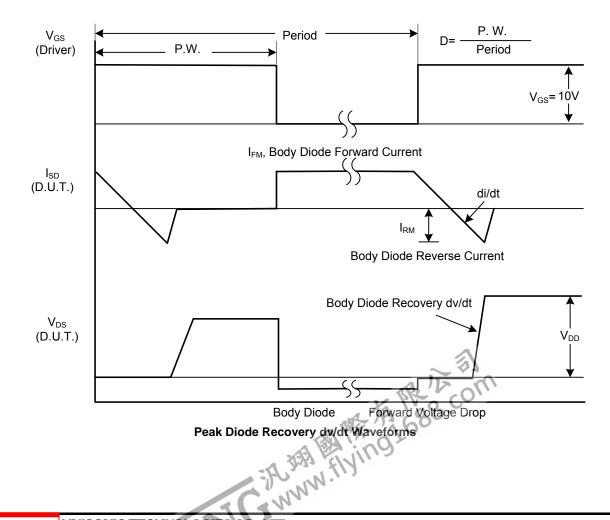
^{2.} Essentially independent of operating temperature

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■ TEST CIRCUITS AND WAVEFORMS

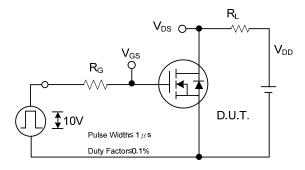


Peak Diode Recovery dv/dt Test Circuit

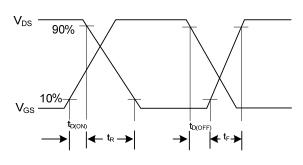


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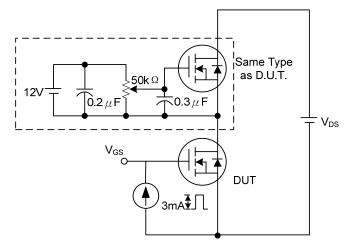
TEST CIRCUITS AND WAVEFORMS (Cont.)



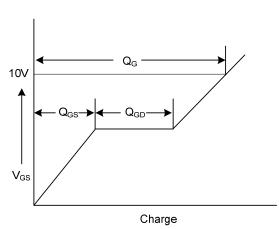
Switching Test Circuit



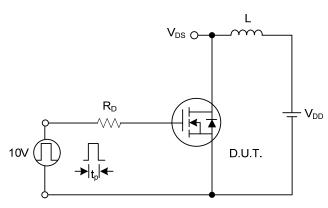
Switching Waveforms



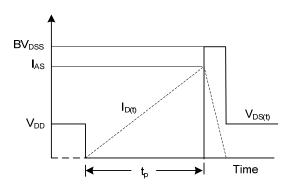
Gate Charge Test Circuit



Gate Charge Waveform

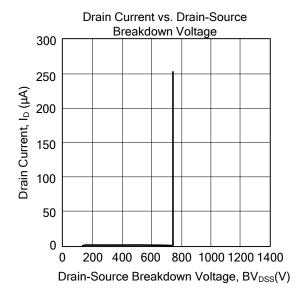


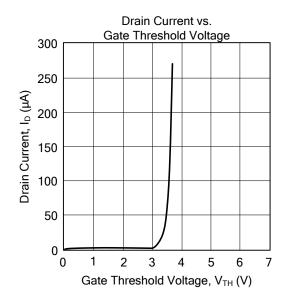
Unclamped Inductive Switching Test Circuit

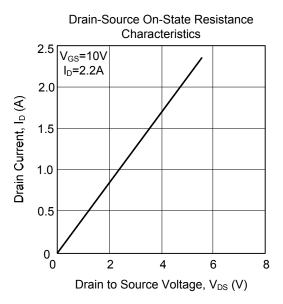


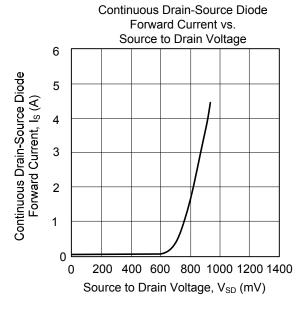
Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS









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