



4NM70-U2

Preliminary

Power MOSFET

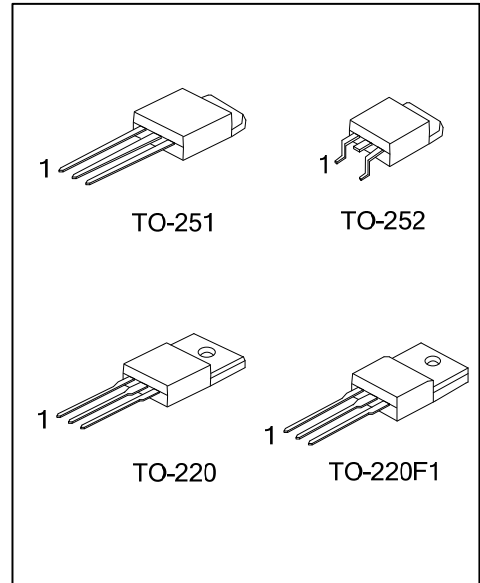
4A, 700V N-CHANNEL SUPER-JUNCTION MOSFET

DESCRIPTION

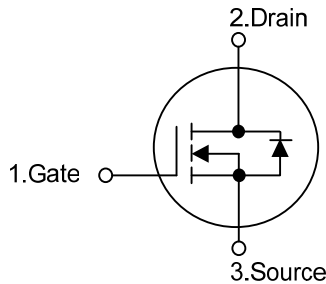
The UTC **4NM70-U2** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications at power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

FEATURES

- * $R_{DS(ON)} < 2.2\Omega @ V_{GS} = 10V, I_D = 2.0A$
- * Fast Switching Capability
- * Improved dv/dt Capability, High Ruggedness



SYMBOL



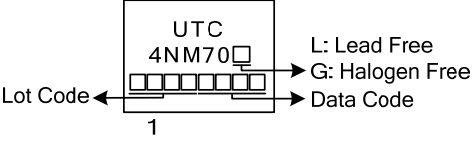
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
4NM70L-TA3-T	4NM70G-TA3-T	TO-220	G	D	S	Tube
4NM70L-TF1-T	4NM70G-TF1-T	TO-220F1	G	D	S	Tube
4NM70L-TM3-T	4NM70G-TM3-T	TO-251	G	D	S	Tube
4NM70L-TN3-R	4NM70G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>4NM70L-TA3-T</p> <p>(1) Packing Type (2) Package Type (3) Green Package</p>	<p>(1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF1: TO-220F1, TM3: TO-251, TN3: TO-252 (3) L: Lead Free, G: Halogen Free and Lead Free</p>
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MARKING



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■ ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	700	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	Continuous	I_D	4.0	A
	Pulsed (Note 2)	I_{DM}	16	A
Avalanche Current (Note 2)		I_{AR}	1.2	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	104	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4	V/ns
Power Dissipation	TO-220	P_D	106	W
	TO-220F1		36	W
	TO-251/TO-252		49	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature.

3. $L = 144 \text{ mH}$, $I_{AS} = 1.2 \text{ A}$, $V_{DD} = 50 \text{ V}$, $R_G = 25 \Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 4.0 \text{ A}$, $di/dt \leq 200 \text{ A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction-to-Ambient	TO-220/TO-220F1	θ_{JA}	62.5	$^\circ\text{C}/\text{W}$
	TO-252		110	$^\circ\text{C}/\text{W}$
Junction-to-Case	TO-220	θ_{JC}	1.18	$^\circ\text{C}/\text{W}$
	TO-220F1		3.47	$^\circ\text{C}/\text{W}$
	TO-251/TO-252		2.55	$^\circ\text{C}/\text{W}$

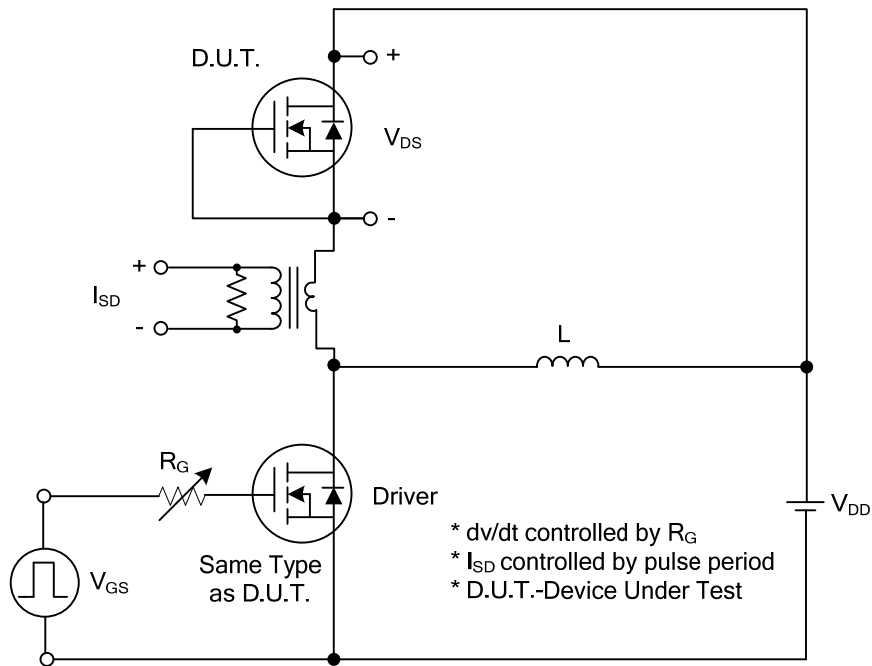
■ ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	700			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=700V, V_{GS}=0V$			1	μA
Gate-Source Leakage Current	Forward	$V_{GS}=30V, V_{DS}=0V$ $V_{GS}=-30V, V_{DS}=0V$			100	nA
	Reverse				-100	
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.5		4.5	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=2.0A$			2.2	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$V_{GS}=0V, V_{DS}=25V, f=1.0MHz$		204		pF
Output Capacitance	C_{OSS}			120		pF
Reverse Transfer Capacitance	C_{RSS}			13		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge (Note 1)	Q_G	$V_{DS}=50V, V_{GS}=10V, I_D=1.3A,$ $I_G=100\mu A$ (Note 1, 2)		32		nC
Gate to Source Charge	Q_{GS}			4		nC
Gate to Drain Charge	Q_{GD}			7		nC
Turn-ON Delay Time (Note 1)	$t_{D(ON)}$	$V_{DD}=30V, V_{GS}=10V, I_D=0.5A,$ $R_G=25\Omega$ (Note 1, 2)		36		ns
Rise Time	t_R			56		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			90		ns
Fall-Time	t_F			24		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				4.0	A
Maximum Body-Diode Pulsed Current	I_{SM}				16	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$I_S=4.0A, V_{GS}=0V$			1.4	V
Body Diode Reverse Recovery Time (Note 1)	t_{rr}	$I_S=4.0A, V_{GS}=0V,$ $dI_F/dt=100A/\mu s$		280		ns
Body Diode Reverse Recovery Charge	Q_{rr}			2.14		μC

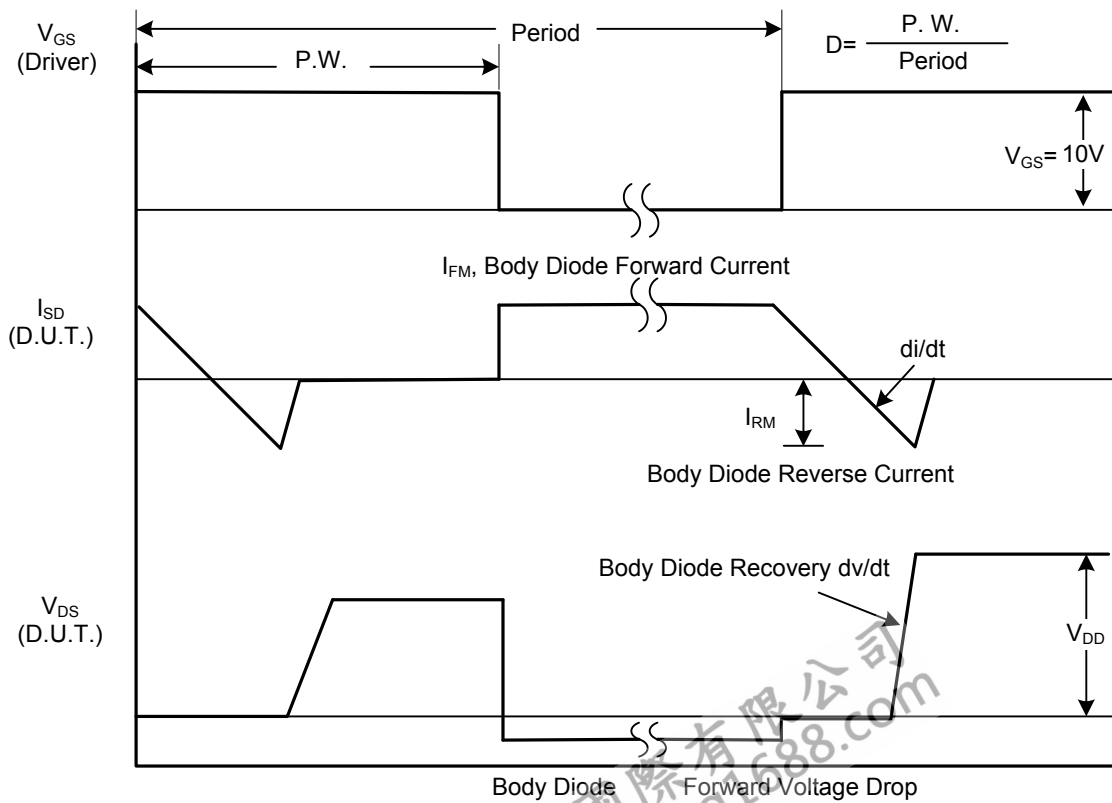
Notes: 1. Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

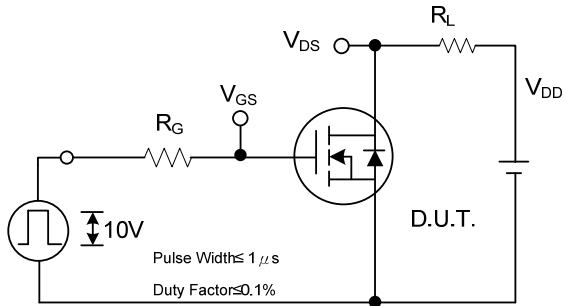


Peak Diode Recovery dv/dt Test Circuit

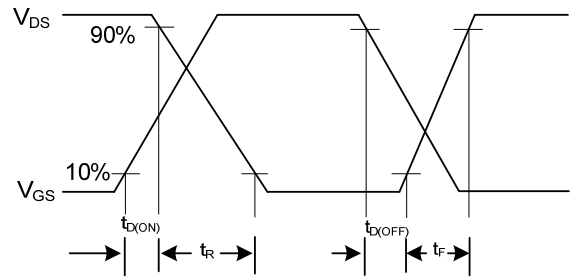


Peak Diode Recovery dv/dt Waveforms

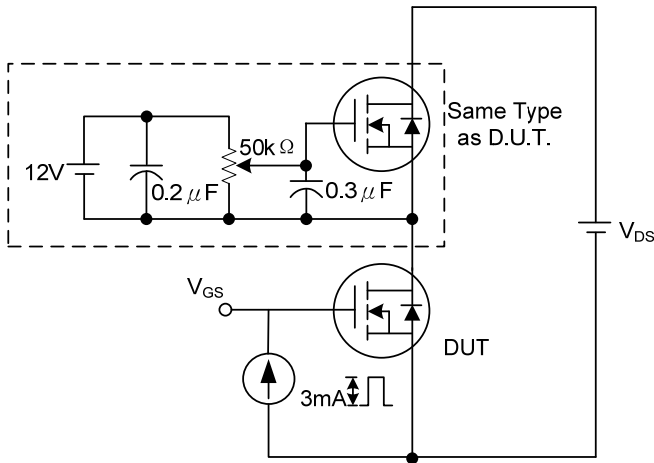
TEST CIRCUITS AND WAVEFORMS (Cont.)



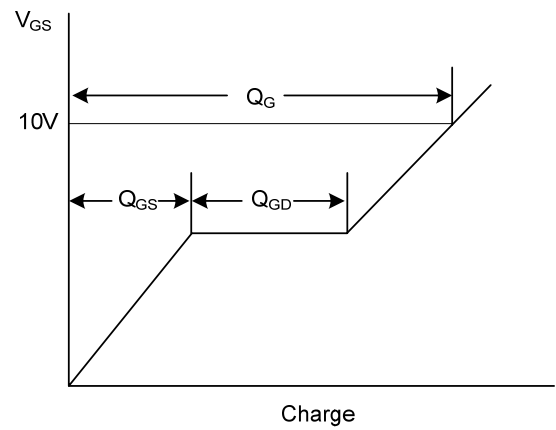
Switching Test Circuit



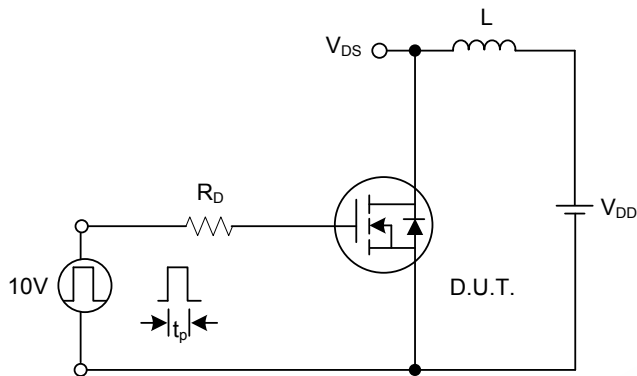
Switching Waveforms



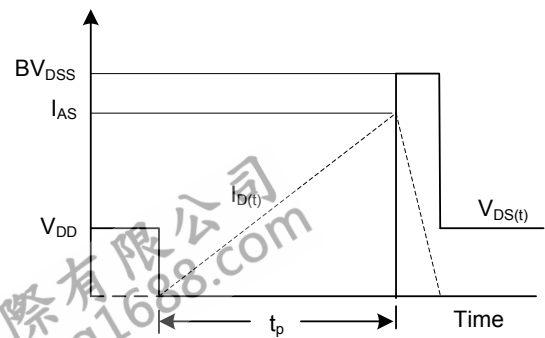
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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