UNISONIC TECHNOLOGIES CO., LTD

5N60-CB **Preliminary** Power MOSFET

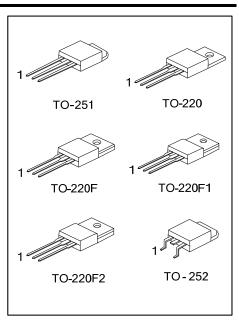
5.0A, 600V N-CHANNEL **POWER MOSFET**

DESCRIPTION

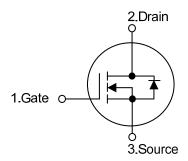
The UTC 5N60-CB is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

- * $R_{DS(ON)}$ < 2.2 Ω @ V_{GS} =10V, I_{D} = 2.5A
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability, High Ruggedness



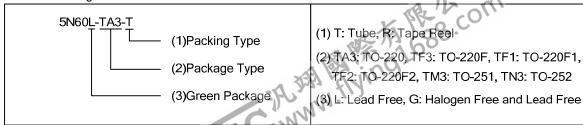
SYMBOL



ORDERING INFORMATION

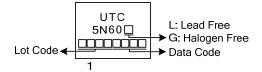
Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package 1		2	3	Packing	
5N60L-TA3-T	5N60G-TA3-T	5N60G-TA3-T TO-220 G		D	S	Tube	
5N60L-TF1-T	5N60G-TF1-T	TO-220F1	G	D	S	Tube	
5N60L-TF2-T	5N60G-TF2-T	TO-220F2	G	D	S	Tube	
5N60L-TF3-T	5N60G-TF3-T	TO-220F	G	D	S	Tube	
5N60L-TM3-T	5N60G-TM3-T	TO-251	G	D	S	Tube	
5N60L-TN3-R	5N60G-TN3-R	3-R TO-252		D	S	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



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MARKING





■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	YMBOL RATINGS	
Drain-Source Voltage		V_{DSS}	600	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous	I_{D}	5	Α
	Pulsed (Note 2)	I_{DM}	20	Α
Avalanche Current (Note 3)		I _{AR}	3.6	Α
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	65	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220		100	W
	TO-220F/TO-220F1 TO-220F2	P _D 36		W
	TO-251/TO-252		54	W
Junction Temperature		TJ	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L = 10mH, I_{AS} = 3.6A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25°C
- 4. $I_{SD} \le 5.0$ A, di/dt ≤ 200 A/ μ s, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25$ °C

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2	θ_{JA}	62.5	°C/W
	TO-251/TO-252		110	°C/W
Junction to Case	TO-220		1.25	°C/W
	TO-220F TO-220F1/TO-220F2	θ_{JC}	3.47	°C/W
	TO-251/TO-252		2.3	°C/W



ELECTRICAL CHARACTERISTICS (T_J = 25°C, unless otherwise specified)

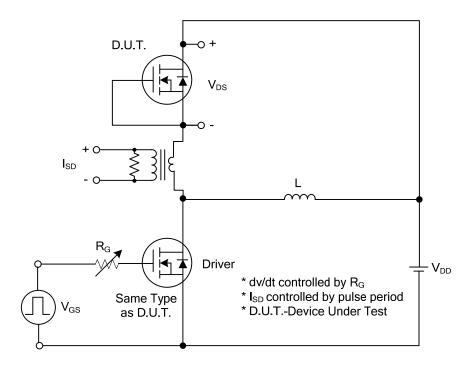
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV _{DSS}	V_{GS} =0V, I_{D} = 250 μ A	600			V	
Drain-Source Leakage Current		I _{DSS}	V_{DS} =600V, V_{GS} = 0V			1	μΑ	
Gate-Source Leakage Current	Forward		V_{GS} =30V, V_{DS} = 0V			100	nA	
	Reverse	I _{GSS}	V_{GS} =-30V, V_{DS} = 0V			-100	IIA	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V	
Static Drain-Source On-State Resistance		R _{DS(ON)}	$V_{GS} = 10V, I_D = 2.5A$			2.2	Ω	
DYNAMIC CHARACTERISTICS								
nput Capacitance		C _{ISS}			760		pF	
Output Capacitance		Coss	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		72		pF	
Reverse Transfer Capacitance		C_{RSS}			5.5		pF	
SWITCHING CHARACTERISTICS								
Total Gate Charge (Note 1)		Q_G	V _{DS} =50V, V _{GS} =10V, I _D =1.3A ,		19		nC	
Gate to Source Charge		Q_{GS}	$I_{G}=100\mu A$ (Note 1, 2)		3.5		nC	
Gate to Drain Charge		Q_{GD}	IG-100µA (Note 1, 2)		2.4		nC	
Turn-ON Delay Time (Note 1)		$t_{D(ON)}$			56		ns	
Rise Time		t_R	V_{DD} =30V, V_{GS} =10V, I_{D} =0.5A, R_{G} =25 Ω (Note 1, 2)		22		ns	
Turn-OFF Delay Time		t _{D(OFF)}			119		ns	
Fall-Time		t_{F}			28		ns	
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Maximum Body-Diode Continuous Current		Is				5	Α	
Maximum Body-Diode Pulsed Current		I _{SM}				20	Α	
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I _S =5.0A, V _{GS} =0V			1.4	V	
Body Diode Reverse Recovery Time (Note 1)		t _{rr}	I _S =5.0A, V _{GS} =0V,		366		ns	
Body Diode Reverse Recovery Charge		Q_{rr}	dI _F /dt=100A/μs		1.67		μC	

Note: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%

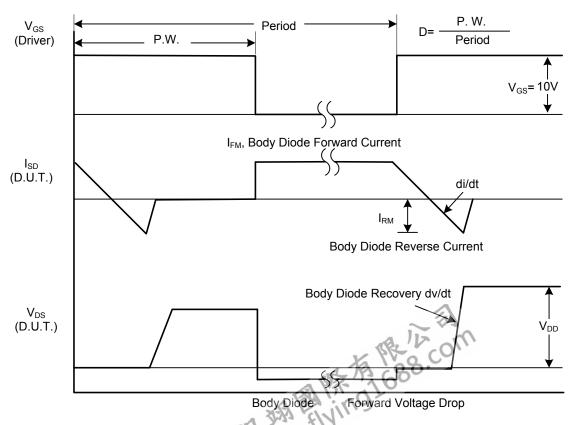


^{2.} Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS



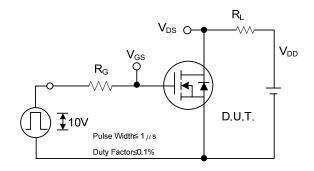
Peak Diode Recovery dv/dt Test Circuit



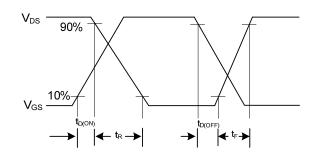
Peak Diode Recovery dv/dt Waveforms

 V_{GS}

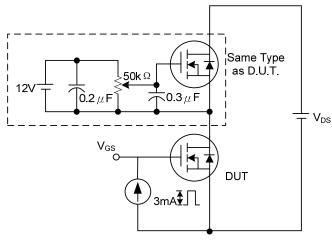
TEST CIRCUITS AND WAVEFORMS (Cont.)



Switching Test Circuit



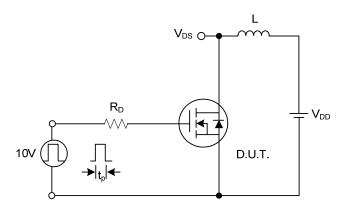
Switching Waveforms



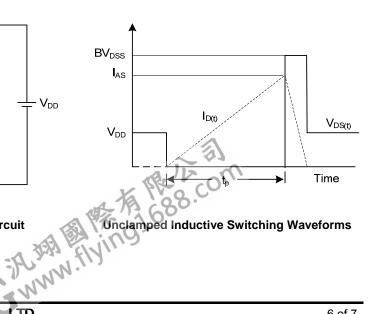
 Q_G 10V Charge

Gate Charge Test Circuit

Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



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