



## 6N60-CB

Preliminary

Power MOSFET

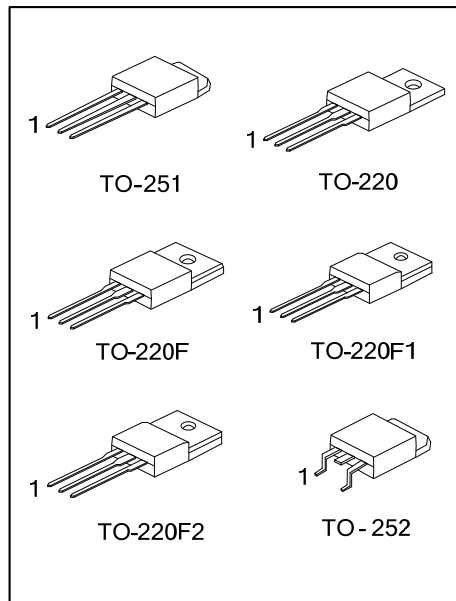
### 6.0A, 600V N-CHANNEL POWER MOSFET

#### DESCRIPTION

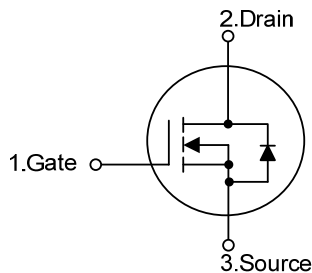
The UTC **6N60-CB** is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in switching power supplies and adaptors.

#### FEATURES

- \*  $R_{DS(ON)} < 1.44\Omega @ V_{GS} = 10V, I_D = 3.0A$
- \* Fast switching capability
- \* Avalanche energy tested
- \* Improved dv/dt capability, high ruggedness



#### SYMBOL



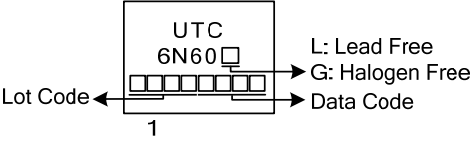
#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
6N60L-TA3-T	6N60G-TA3-T	TO-220	G	D	S	Tube
6N60L-TF1-T	6N60G-TF1-T	TO-220F1	G	D	S	Tube
6N60L-TF2-T	6N60G-TF2-T	TO-220F2	G	D	S	Tube
6N60L-TF3-T	6N60G-TF3-T	TO-220F	G	D	S	Tube
6N60L-TM3-T	6N60G-TM3-T	TO-251	G	D	S	Tube
6N60L-TN3-R	6N60G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>6N60L-TA3-T</p> <ul style="list-style-type: none"> <li>(1) Packing Type</li> <li>(2) Package Type</li> <li>(3) Green Package</li> </ul>	<ul style="list-style-type: none"> <li>(1) T: Tube, R: Tape Reel</li> <li>(2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1, TF2: TO-220F2, TM3: TO-251, TN3: TO-252</li> <li>(3) L: Lead Free, G: Halogen Free and Lead Free</li> </ul>
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MARKING



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■ ABSOLUTE MAXIMUM RATING ( $T_C=25^{\circ}\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	150	V
Gate-Source Voltage		$V_{GSS}$	$\pm 20$	V
Drain Current	Continuous	$I_D$	6	A
	Pulsed (Note 2)	$I_{DM}$	24	A
Avalanche Current (Note 2)		$I_{AR}$	3.7	A
Avalanche Energy (Note 3)	Single Pulsed (Note 3)	$E_{AS}$	68	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3	V/ns
Power Dissipation	TO-220	$P_D$	125	W
	TO-220F/TO-220F1		40	W
	TO-220F2		42	W
	TO-251/TO-252		55	W
Junction Temperature		$T_J$	+150	$^{\circ}\text{C}$
Storage Temperature Range		$T_{STG}$	-55 ~ +150	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3.  $L = 10\text{mH}$ ,  $I_{AS} = 3.7\text{A}$ ,  $V_{DD} = 50\text{V}$ ,  $R_G = 25\Omega$ , Starting  $T_J = 25^{\circ}\text{C}$

4.  $I_{SD} \leq 6.0\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq V_{(BR)DSS}$ ,  $T_J = 25^{\circ}\text{C}$

■ THERMAL DATA

PARAMETER	PACKAGE	SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F	$\theta_{JA}$	62.5	$^{\circ}\text{C}/\text{W}$
	TO-220F1/TO-220F2			
	TO-251/TO-252			
Junction to Case	TO-220	$\theta_{JC}$	1.0	$^{\circ}\text{C}/\text{W}$
	TO-220F/TO-220F1		3.2	$^{\circ}\text{C}/\text{W}$
	TO-220F2		2.97	$^{\circ}\text{C}/\text{W}$
	TO-251/TO-252		2.27	$^{\circ}\text{C}/\text{W}$

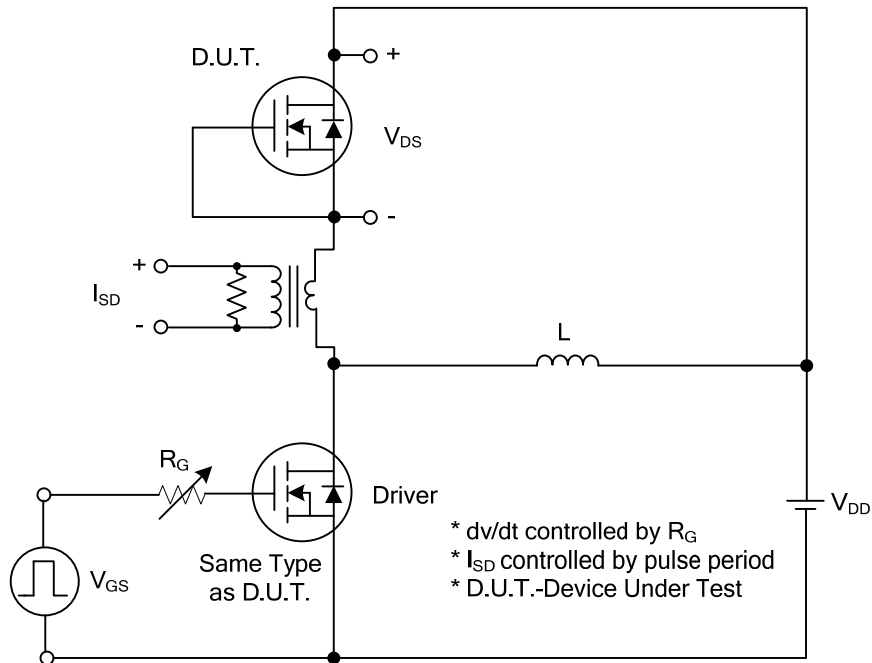
■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	600			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =600V, V <sub>GS</sub> =0V			1	μA
Gate- Source Leakage Current	Forward	I <sub>GSS</sub> V <sub>G</sub> =30V, V <sub>DS</sub> =0V			100	nA
	Reverse		V <sub>GS</sub> =-30V, V <sub>DS</sub> =0V			-100
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0		4.0	V
Static Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =3.0A			1.44	Ω
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1.0 MHz		900		pF
Output Capacitance	C <sub>OSS</sub>			85		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			4		pF
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge (Note 1)	Q <sub>G</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =1.3A I <sub>G</sub> =100μA (Note 1, 2)		59		nC
Gate to Source Charge	Q <sub>GS</sub>			5		nC
Gate to Drain Charge	Q <sub>GD</sub>			4		nC
Turn-on Delay Time (Note 1)	t <sub>D(ON)</sub>	V <sub>DD</sub> =30V, V <sub>GS</sub> =10V, I <sub>D</sub> =0.5A, R <sub>G</sub> =25Ω (Note 1, 2)		46		ns
Rise Time	t <sub>R</sub>			30		ns
Turn-off Delay Time	t <sub>D(OFF)</sub>			164		ns
Fall-Time	t <sub>F</sub>			26		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Body-Diode Continuous Current	I <sub>S</sub>				6.0	A
Maximum Body-Diode Pulsed Current	I <sub>SM</sub>				24.8	A
Drain-Source Diode Forward Voltage (Note 1)	V <sub>SD</sub>	I <sub>S</sub> =6A, V <sub>GS</sub> =0V			1.4	V
Reverse Recovery Time (Note 1)	t <sub>rr</sub>	I <sub>S</sub> =6A, V <sub>GS</sub> =0V, dI <sub>F</sub> /dt=100A/μs		360		ns
Reverse Recovery Charge	Q <sub>rr</sub>				1.85	

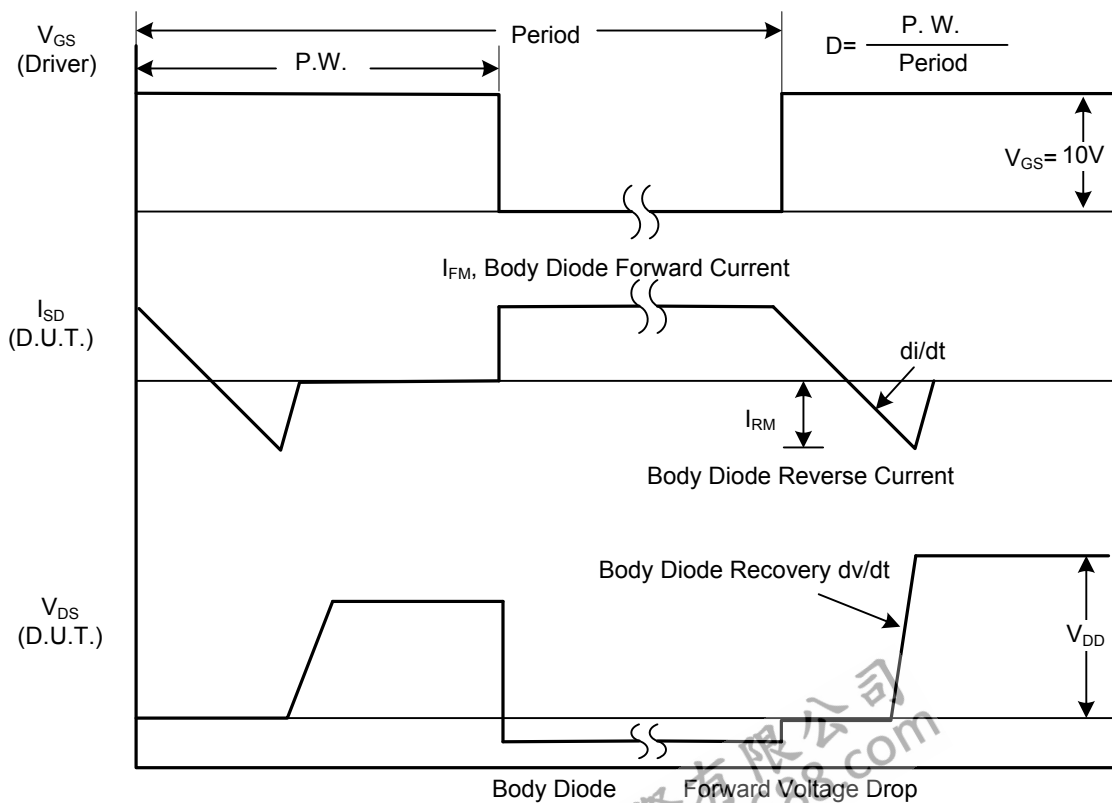
Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

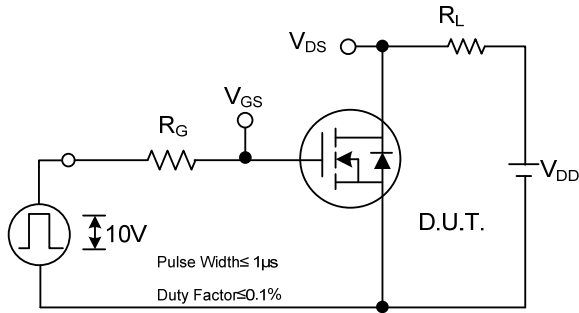


Peak Diode Recovery  $dv/dt$  Test Circuit

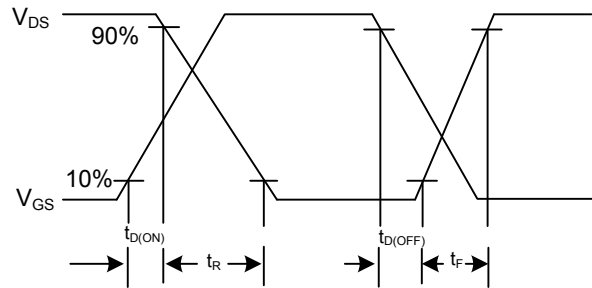


Peak Diode Recovery  $dv/dt$  Waveforms

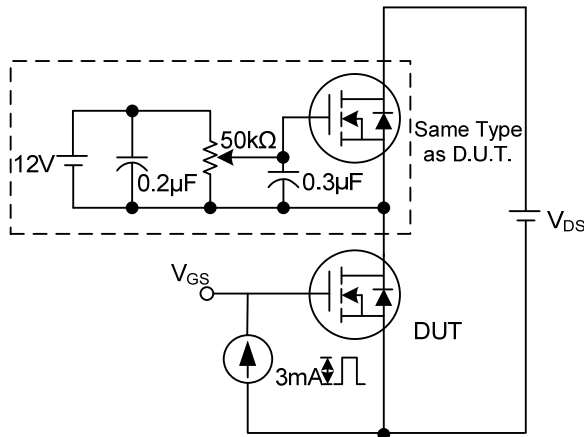
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



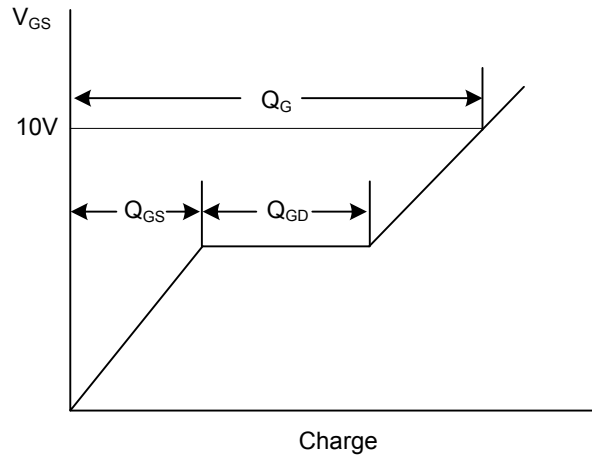
Switching Test Circuit



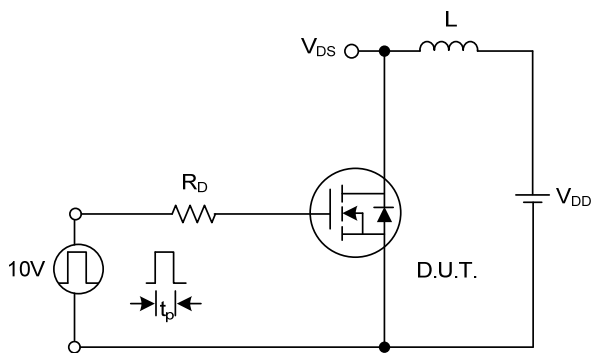
Switching Waveforms



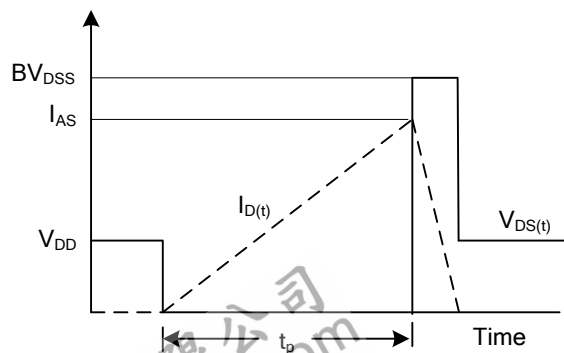
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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