

UTC UNISONIC TECHNOLOGIES CO., LTD

6N60-CBS **Preliminary** Power MOSFET

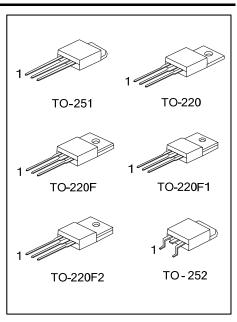
N-CHANNEL 6.0A, 600V **POWER MOSFET**

DESCRIPTION

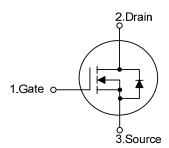
The UTC 6N60-CBS is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in switching power supplies and adaptors.

FEATURES

- * $R_{DS(ON)}$ < 1.5 Ω @ V_{GS} = 10V, I_{D} = 3.0A
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness



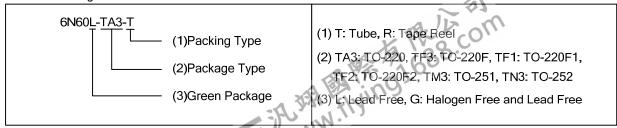
SYMBOL



ORDERING INFORMATION

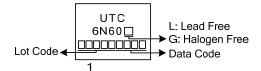
Ordering Number		Deelsene	Pin Assignment			Dealing	
Lead Free	Halogen Free	Package		2	3	Packing	
6N60L-TA3-T	6N60G-TA3-T	TO-220	G	D	S	Tube	
6N60L-TF1-T	6N60G-TF1-T	TO-220F1	G	D	S	Tube	
6N60L-TF2-T	6N60G-TF2-T	TO-220F2	G	D	S	Tube	
6N60L-TF3-T	6N60G-TF3-T	TO-220F	G	D	S	Tube	
6N60L-TM3-R	6N60G-TM3-R	TO-251	G	D	S	Tape Reel	
6N60L-TN3-R	6N60G-TN3-R	TO-252	G	D	S	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



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MARKING





ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	600	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous	I _D	6	Α
	Pulsed (Note 2)	I _{DM}	24	Α
Avalanche Current (Note 3)		I _{AR}	2.7	Α
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	36	mJ
Peak Diode Recovery dv/d	Diode Recovery dv/dt (Note 4)		3	V/ns
Power Dissipation	TO-220		125	W
	TO-220F/TO-220F1		40	W
	TO-220F2	P _D	42	W
	TO-251/TO-252		55	W
Junction Temperature		TJ	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L = 10mH, I_{AS} = 2.7A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C
- 4. $I_{SD} \le 6.0A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

THERMAL DATA

PARAMETER	PACKAGE	SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2 θ _{JA}		62.5	°C/W
	TO-251/TO-252		110	°C/W
	TO-220		1.0	°C/W
lunation to Coop	TO-220F/TO-220F1	0	3.13	°C/W
Junction to Case	TO-220F2	θ _{JC}	2.97	°C/W
	TO-251/TO-252		2.27	°C/W



ELECTRICAL CHARACTERISTICS (T_J =25°C, unless otherwise specified)

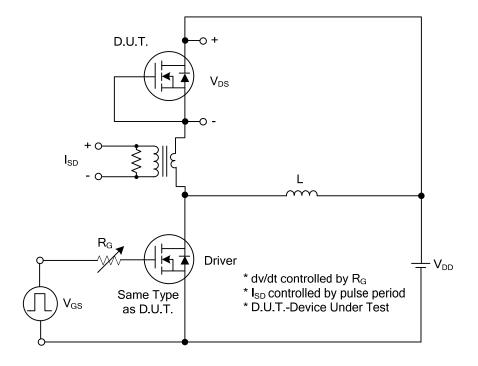
PARAMETER		SYMBOL	TEST CONDITIONS		TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV_{DSS}	V_{GS} =0V, I_D =250 μ A	600			V	
Drain-Source Leakage Current		I_{DSS}	V _{DS} =600V, V _{GS} =0V			1	μA	
Gate- Source Leakage Current	Forward	- I _{GSS}	$V_{G=}30V$, $V_{DS}=0V$			100	nA	
	Reverse	IGSS	V_{GS} =-30V, V_{DS} =0V			-100	nA	
ON CHARACTERISTICS								
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2.0		4.0	V		
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =3.0A			1.5	Ω	
DYNAMIC CHARACTERISTICS								
Input Capacitance		C _{ISS}			900		pF	
Output Capacitance		Coss	V _{DS} =25V, V _{GS} =0V, f=1.0 MHz		82		pF	
Reverse Transfer Capacitance	C_{RSS}			63		pF		
SWITCHING CHARACTERISTICS								
Total Gate Charge (Note 1)		Q_G	V _{DS} =50V, V _{GS} =10V, I _D =1.3A		67		nC	
Gate to Source Charge		Q_GS	I_{G} =100µA (Note 1, 2)		5		nC	
Gate to Drain Charge		Q_GD	ig-100μA (Note 1, 2)		5.8		nC	
Turn-ON Delay Time (Note 1)		$t_{D(ON)}$			52		ns	
Rise Time		t_{R}	V_{DD} =30V, V_{GS} =10V, I_{D} =0.5A		30		ns	
Turn-OFF Delay Time		$t_{D(OFF)}$	$R_G = 25\Omega$ (Note 1, 2)		158		ns	
Fall-Time		t_{F}			36		ns	
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS								
Maximum Body-Diode Continuous Current		Is				6.0	Α	
Maximum Body-Diode Pulsed Current (Note 1)		I _{SM}				24	Α	
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I _S =6.0A, V _{GS} =0V			1.4	V	
Body Diode Reverse Recovery Time		t _{rr}	I _S =6.0A, V _{GS} =0V,		370		ns	
Body Diode Reverse Recovery Charge	Q_{rr}	di/dt=100A/µs		1.9		μC		

Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%.

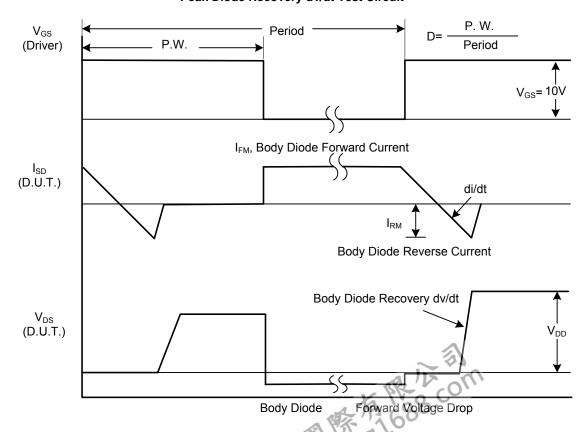


^{2.} Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

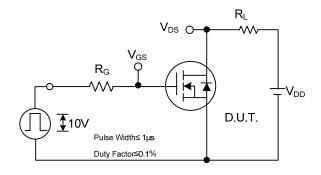


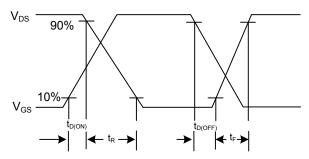
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

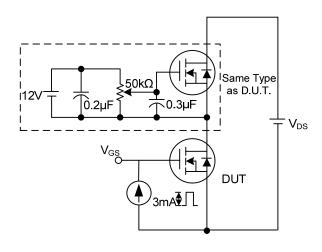
TEST CIRCUITS AND WAVEFORMS (Cont.)

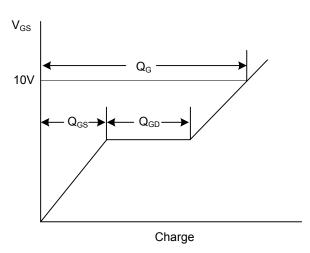




Switching Test Circuit

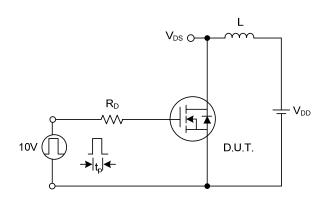
Switching Waveforms

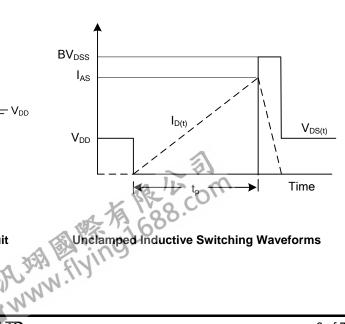




Gate Charge Test Circuit

Gate Charge Waveform





Unclamped Inductive Switching Test Circuit

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