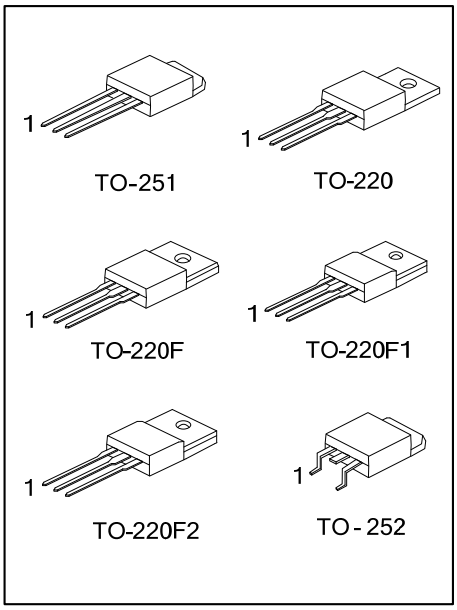
6N60-CBS

Preliminary | ***Power MOSFET***

6.0A, 600V N-CHANNEL POWER MOSFET



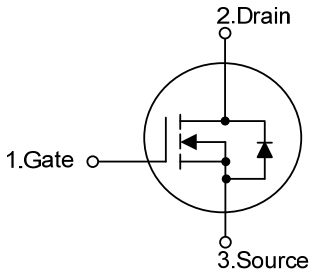
■ DESCRIPTION

The UTC **6N60-CBS** is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in switching power supplies and adaptors.

■ FEATURES

* $R_{DS(ON)}$ < 1.5Ω @ V_{GS} = 10V, I_D = 3.0A
* Fast switching capability
* Avalanche energy tested
* Improved dv/dt capability, high ruggedness

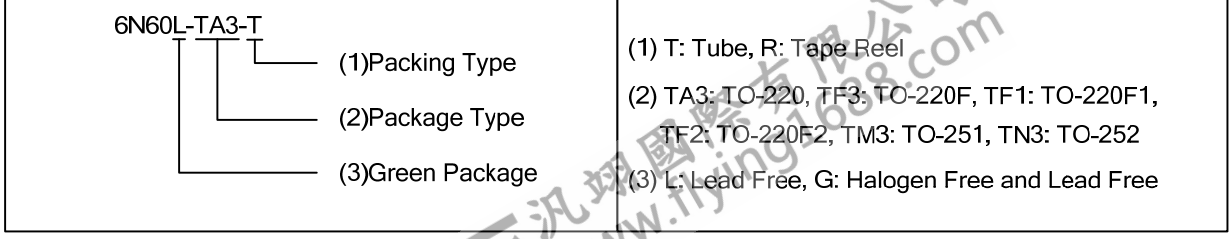
■ SYMBOL



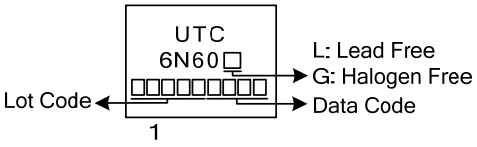
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
6N60L-TA3-T	6N60G-TA3-T	TO-220	G	D	S	Tube
6N60L-TF1-T	6N60G-TF1-T	TO-220F1	G	D	S	Tube
6N60L-TF2-T	6N60G-TF2-T	TO-220F2	G	D	S	Tube
6N60L-TF3-T	6N60G-TF3-T	TO-220F	G	D	S	Tube
6N60L-TM3-R	6N60G-TM3-R	TO-251	G	D	S	Tape Reel
6N60L-TN3-R	6N60G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source



■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	600	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous	I_D	6	A
	Pulsed (Note 2)	I_{DM}	24	A
Avalanche Current (Note 3)		I_{AR}	2.7	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	36	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3	V/ns
Power Dissipation	TO-220	P_D	125	W
	TO-220F/TO-220F1		40	W
	TO-220F2		42	W
	TO-251/TO-252		55	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating : Pulse width limited by maximum junction temperature.
3. L = 10mH, I_{AS} = 2.7A, V_{DD} = 50V, R_G= 25Ω ,Starting T_J = 25°C
4. I_{SD} ≤ 6.0A, di/dt ≤200A/μs, V_{DD}≤ BV_{DSS}, Starting T_J= 25°C

■ THERMAL DATA

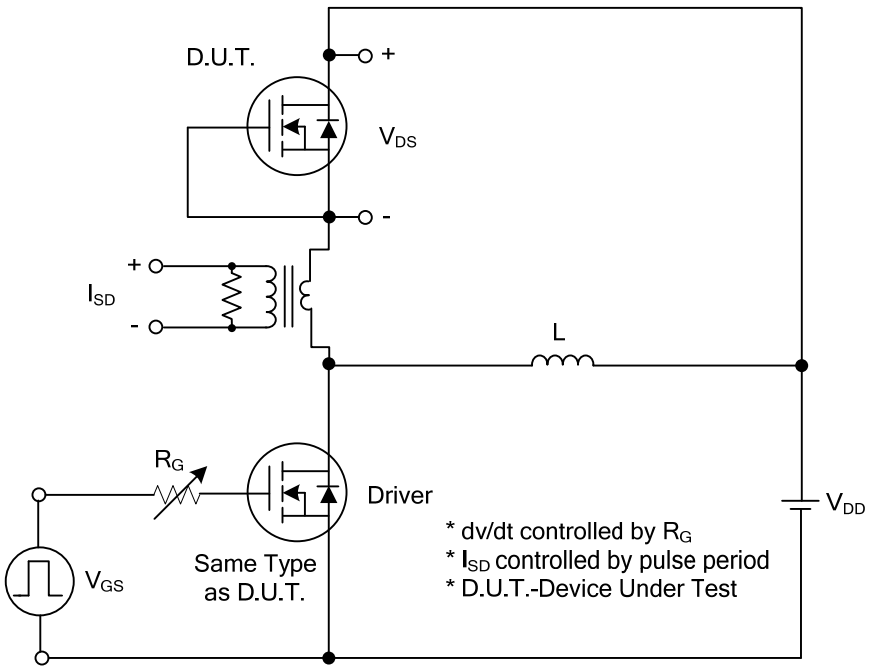
PARAMETER	PACKAGE	SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2	θ_{JA}	62.5	°C/W
	TO-251/TO-252		110	°C/W
Junction to Case	TO-220	θ_{JC}	1.0	°C/W
	TO-220F/TO-220F1		3.13	°C/W
	TO-220F2		2.97	°C/W
	TO-251/TO-252		2.27	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

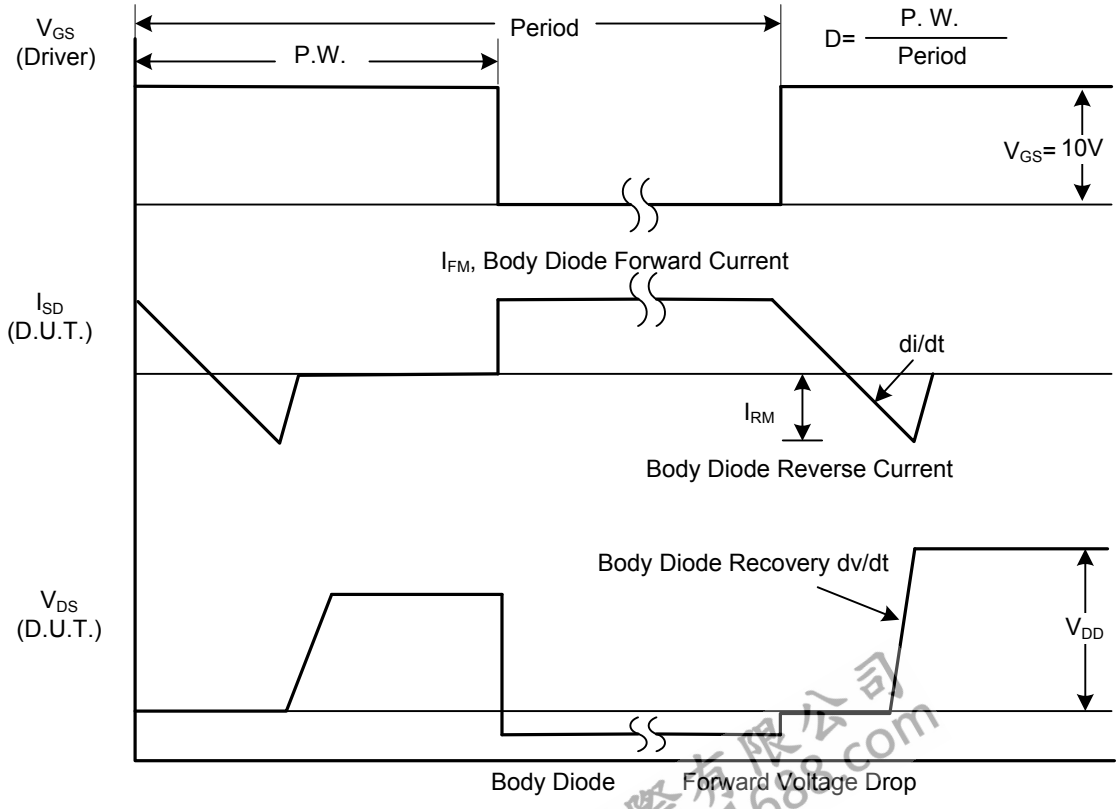
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	V_{GS}=0V, I_D=250μA	600			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=600V, V_{GS}=0V			1	μA
Gate- Source Leakage Current	Forward	I_{GSS}	V_G=30V, V_{DS}=0V			100	nA
	Reverse		V_{GS}=-30V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=3.0A			1.5	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C_{ISS}	V_{DS}=25V, V_{GS}=0V, f=1.0 MHz		900		pF
Output Capacitance		C_{OSS}			82		pF
Reverse Transfer Capacitance		C_{RSS}			63		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge (Note 1)		Q_G	V_{DS}=50V, V_{GS}=10V, I_D=1.3A I_G=100μA (Note 1, 2)		67		nC
Gate to Source Charge		Q_{GS}			5		nC
Gate to Drain Charge		Q_{GD}			5.8		nC
Turn-ON Delay Time (Note 1)		$t_{D(ON)}$	V_{DD}=30V, V_{GS}=10V, I_D =0.5A R_G =25Ω (Note 1, 2)		52		ns
Rise Time		t_R			30		ns
Turn-OFF Delay Time		$t_{D(OFF)}$			158		ns
Fall-Time		t_F			36		ns
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS							
Maximum Body-Diode Continuous Current		I_S				6.0	A
Maximum Body-Diode Pulsed Current (Note 1)		I_{SM}				24	A
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I_S=6.0A, V_{GS}=0V			1.4	V
Body Diode Reverse Recovery Time		t_{rr}	I_S=6.0A, V_{GS}=0V, di/dt=100A/μs		370		ns
Body Diode Reverse Recovery Charge		Q_{rr}			1.9		μC

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

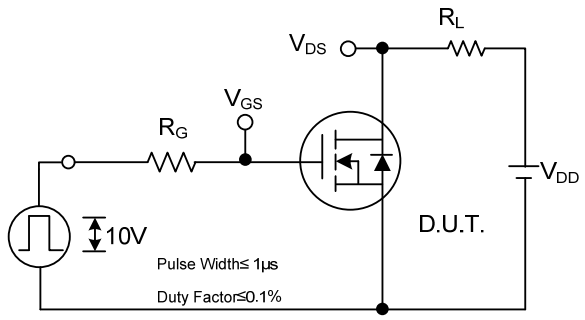


Peak Diode Recovery dv/dt Test Circuit

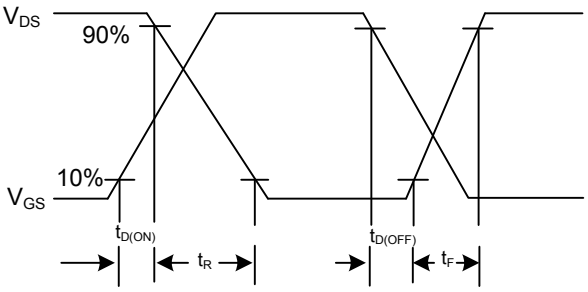


Peak Diode Recovery dv/dt Waveforms

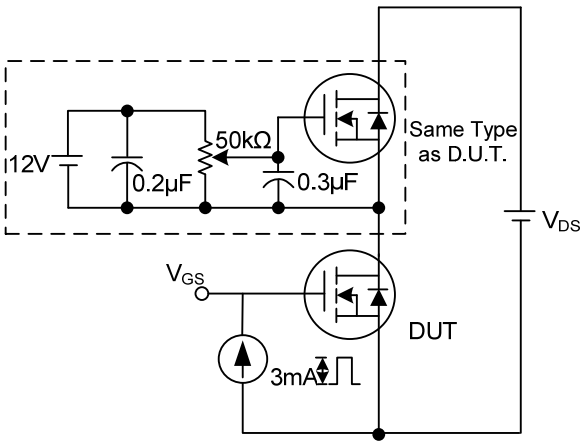
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



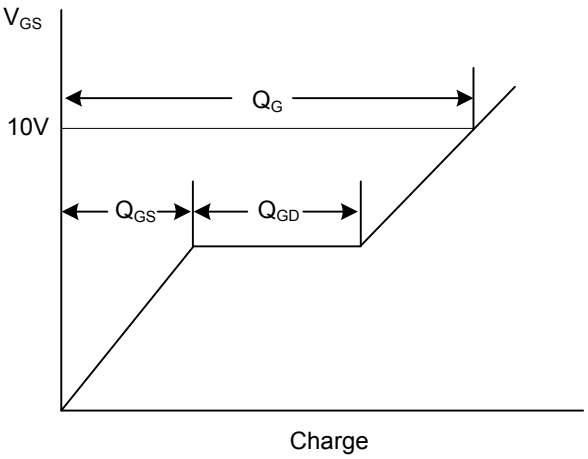
Switching Test Circuit



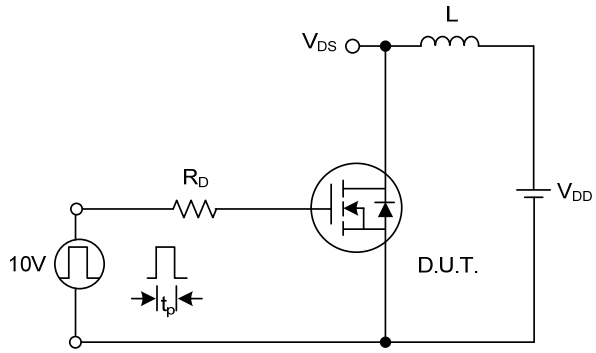
Switching Waveforms



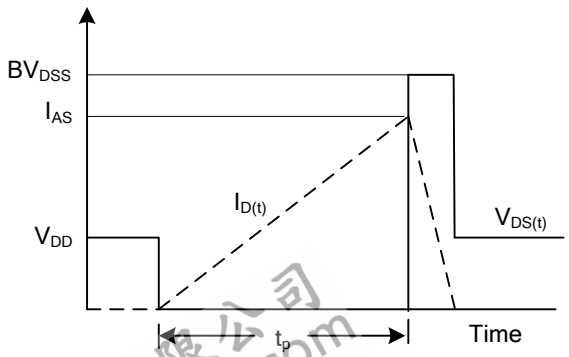
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms