



# 6N60-TC2

**Power MOSFET**

## 6A, 600V N-CHANNEL POWER MOSFET

■ DESCRIPTION

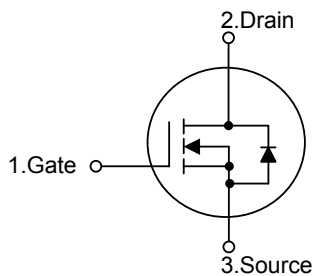
The UTC **6N60-TC2** is a N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology allows a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

The UTC **6N60-TC2** is generally applied in high efficiency switch mode power supplies.

■ FEATURES

- \*  $R_{DS(ON)} \leq 1.8\Omega @ V_{GS}=10V, I_D=3.0A$
- \* High Switching Speed

■ SYMBOL

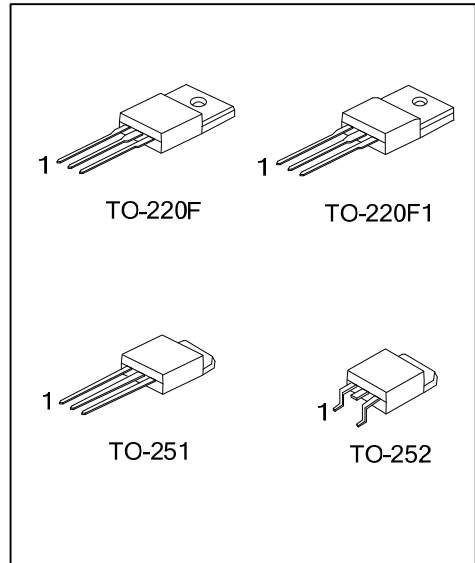


■ ORDERING INFORMATION

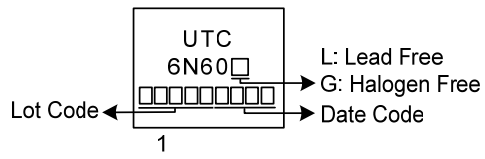
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
6N60L-TF1-T	6N60G-TF1-T	TO-220F1	G	D	S	Tube
6N60L-TF3-T	6N60G-TF3-T	TO-220F	G	D	S	Tube
6N60L-TM3-T	6N60G-TM3-T	TO-251	G	D	S	Tube
6N60L-TN3-R	6N60G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>6N60G-TF1-T</p>	<p>(1) T: Tube, R: Tape Reel</p> <p>(2) TF1: TO-220F1, TF3: TO-220F, TM3: TO-251, TN3: TO-252</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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■ MARKING



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■ ABSOLUTE MAXIMUM RATINGS ( $T_C = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	600	V
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Drain Current	Continuous	$I_D$	6	A
	Pulsed (Note 2)	$I_{DM}$	12	A
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	202	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3.26	V/ns
Power Dissipation	TO-220F/TO-220F1	$P_D$	40	W
	TO-251/TO-252		55	W
Junction Temperature		$T_J$	+150	$^\circ\text{C}$
Storage Temperature		$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3.  $L = 10\text{mH}$ ,  $I_{AS} = 6.36\text{A}$ ,  $V_{DD} = 50\text{V}$ ,  $R_G = 25\ \Omega$  Starting  $T_J = 25^\circ\text{C}$

4.  $I_{SD} \leq 6.0\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220F/TO-220F1	$\theta_{JA}$	62.5	$^\circ\text{C}/\text{W}$
	TO-251/TO-252		110	$^\circ\text{C}/\text{W}$
Junction to Case	TO-220F/TO-220F1	$\theta_{JC}$	3.125	$^\circ\text{C}/\text{W}$
	TO-251/TO-252		2.27 (Note)	$^\circ\text{C}/\text{W}$

Note: The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.

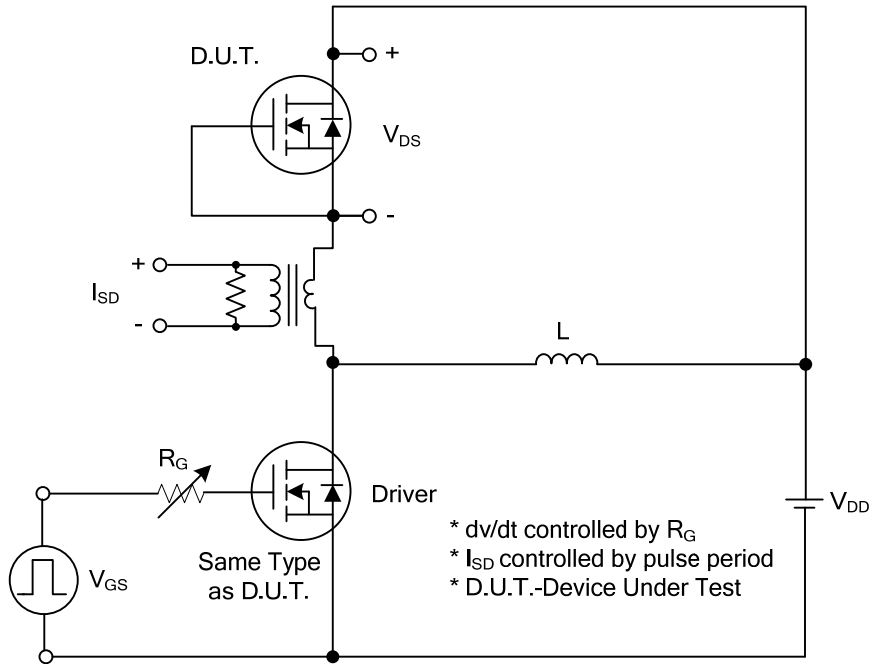
■ ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	600			V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=600V, V_{GS}=0V$			10	$\mu A$
Gate-Source Leakage Current	Forward	$I_{GSS}$			100	nA
	Reverse				-100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=3.0A$			1.8	$\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$C_{ISS}$	$V_{GS}=0V, V_{DS}=25V, f=1.0\text{ MHz}$		675		pF
Output Capacitance	$C_{OSS}$			73		pF
Reverse Transfer Capacitance	$C_{RSS}$			3.9		pF
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge (Note 1)	$Q_G$	$V_{DS}=50V, V_{GS}=10V, I_D=6.0A$ $I_G=1\text{ mA}$ (Note 1, 2)		14		nC
Gate-source Charge	$Q_{GS}$			4.8		nC
Gate-drain Charge	$Q_{GD}$			2.2		nC
Turn-on Delay Time (Note 1)	$t_{D(ON)}$	$V_{DS}=100V, V_{GS}=10V, I_D=6.0A,$ $R_G=25\Omega$ (Note 1, 2)		10		ns
Rise Time	$t_R$			19		ns
Turn-off Delay Time	$t_{D(OFF)}$			37		ns
Fall-Time	$t_F$			24		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_S$				6	A
Maximum Pulsed Drain-Source Diode Forward Current	$I_{SM}$				12	A
Drain-Source Diode Forward Voltage (Note 1)	$V_{SD}$	$V_{GS}=0V, I_S=6.0A$			1.4	V
Reverse Recovery Time (Note 1)	$t_{rr}$	$V_{GS}=0V, I_S=6.0A,$		308		ns
Reverse Recovery Charge	$Q_{rr}$	$di_F/dt=100A/\mu s$ (Note1)		3		$\mu C$

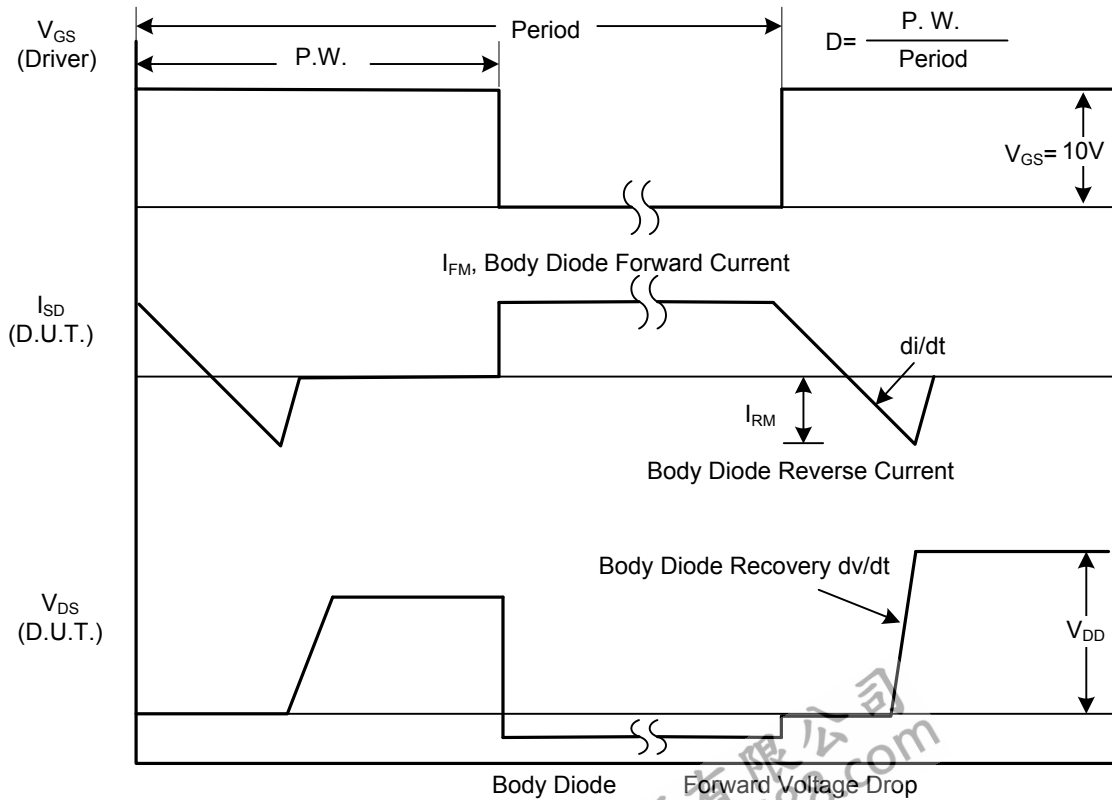
Notes: 1. Pulse Test : Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$ .

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

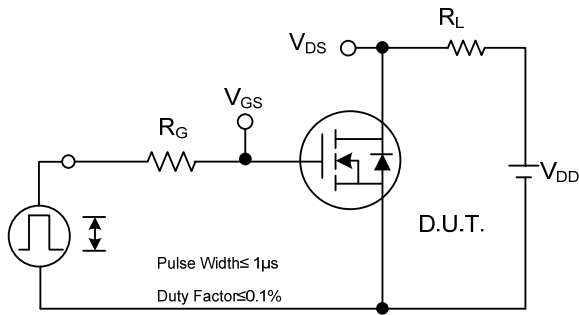


Peak Diode Recovery  $dv/dt$  Test Circuit

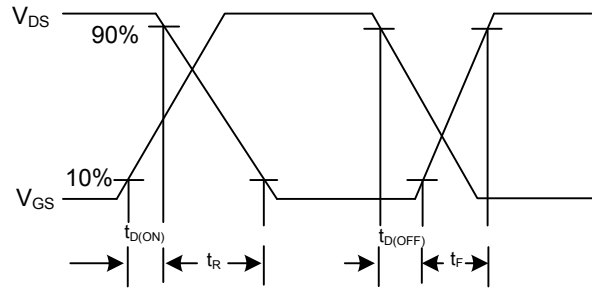


Peak Diode Recovery  $dv/dt$  Waveforms

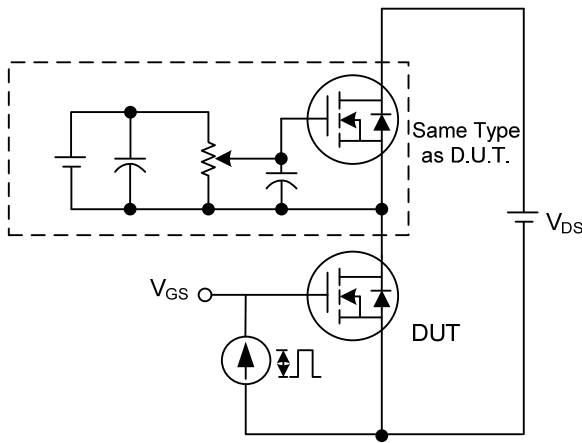
■ TEST CIRCUITS AND WAVEFORMS



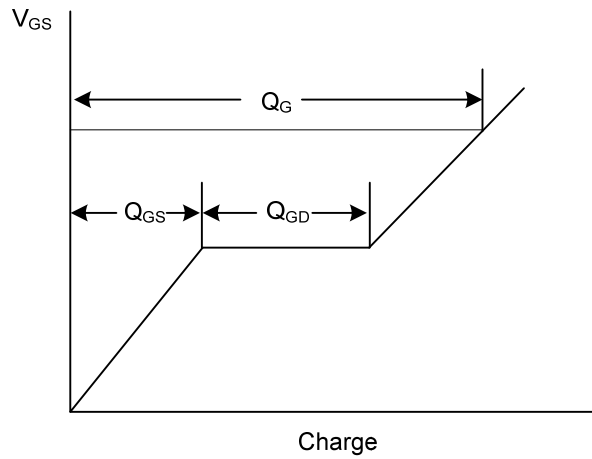
Switching Test Circuit



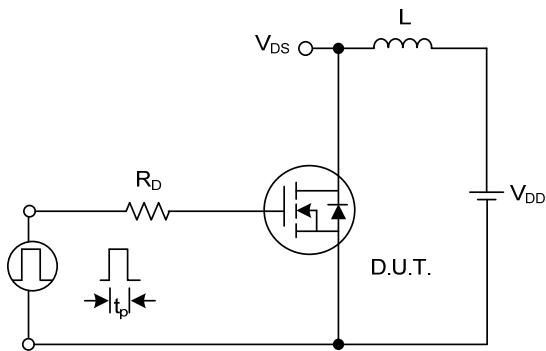
Switching Waveforms



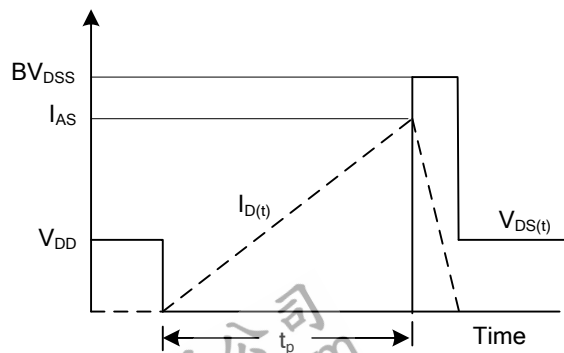
Gate Charge Test Circuit



Gate Charge Waveform

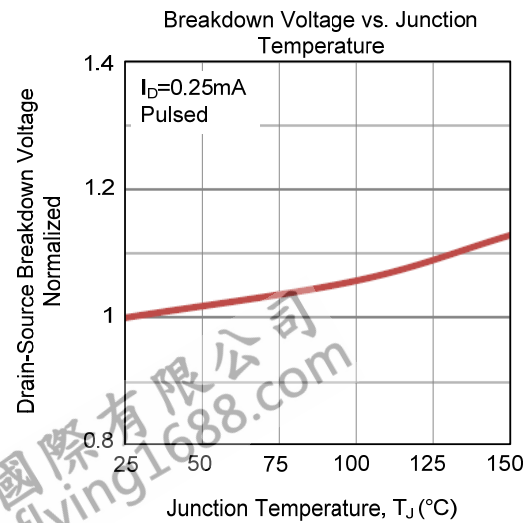
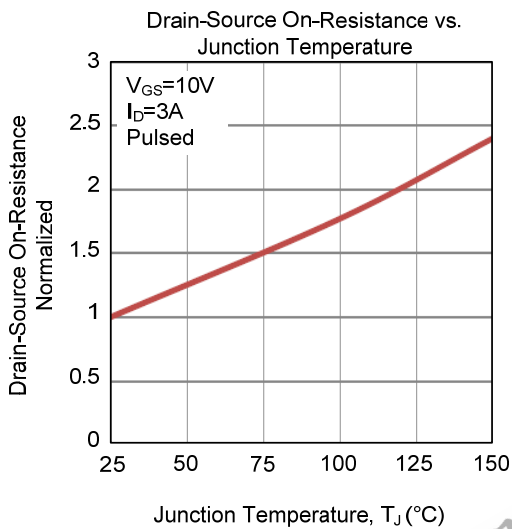
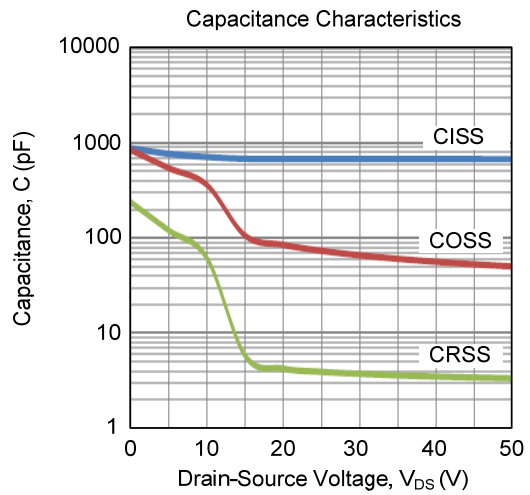
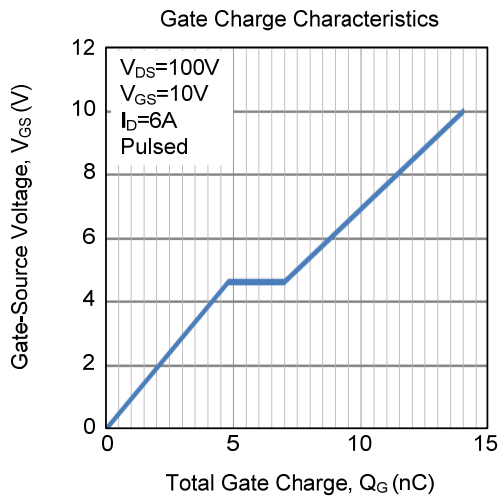
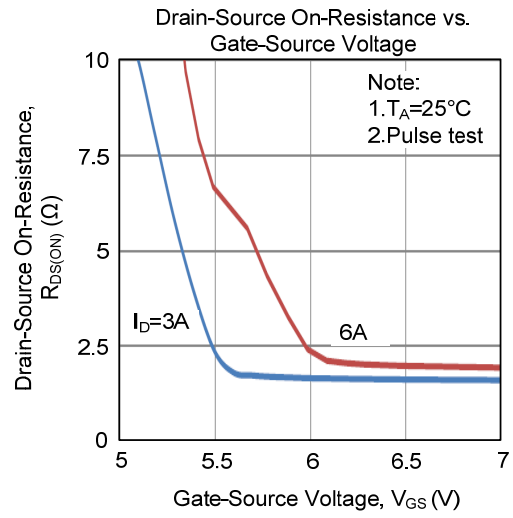
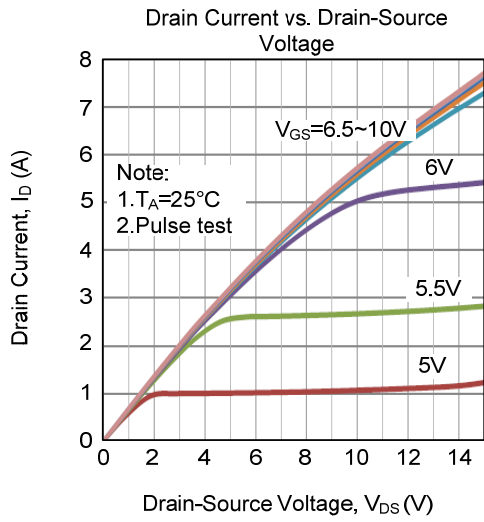


Unclamped Inductive Switching Test Circuit

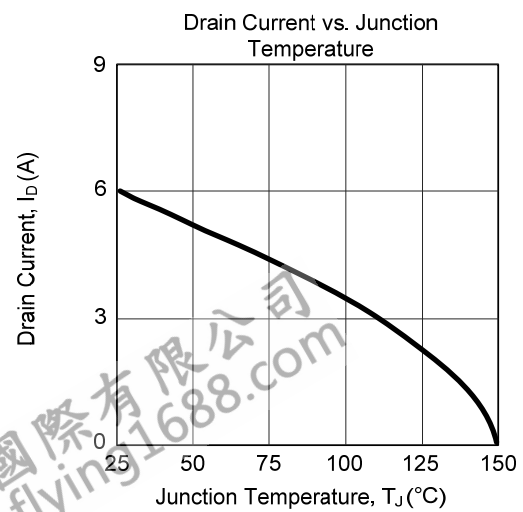
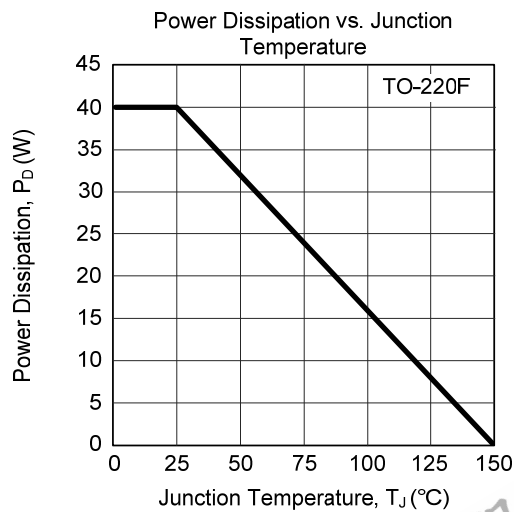
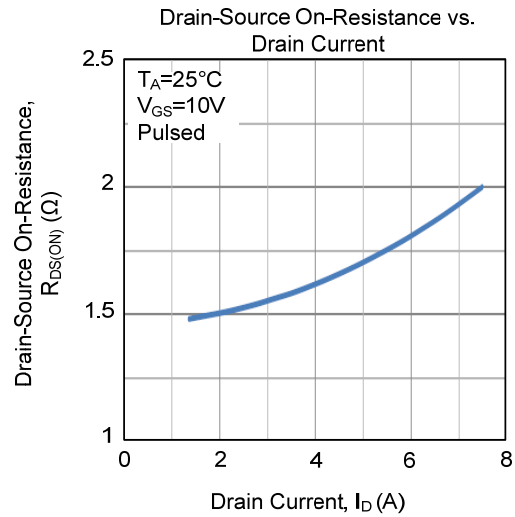
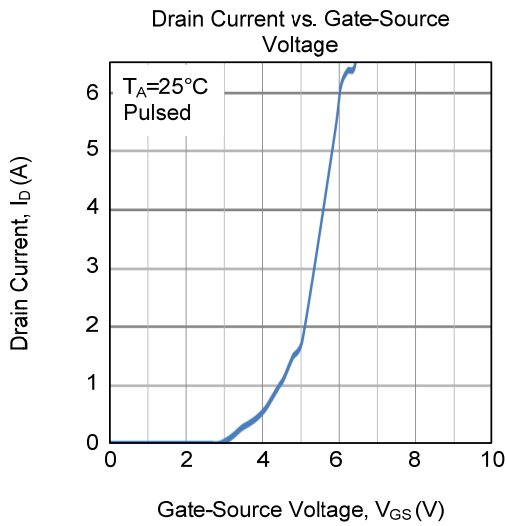
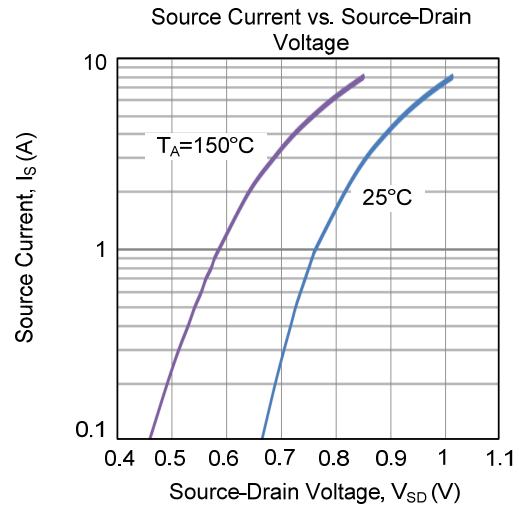
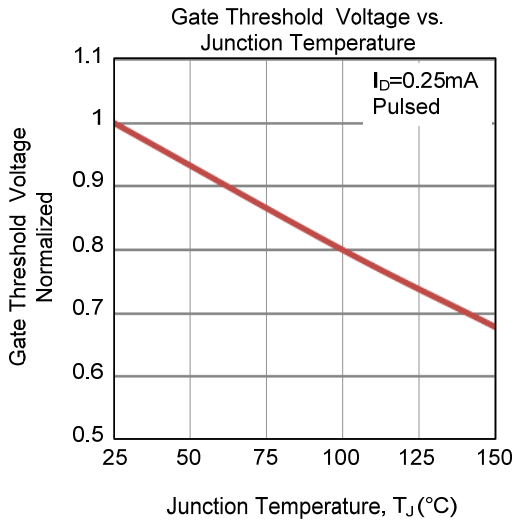


Unclamped Inductive Switching Waveforms

## TYPICAL CHARACTERISTICS

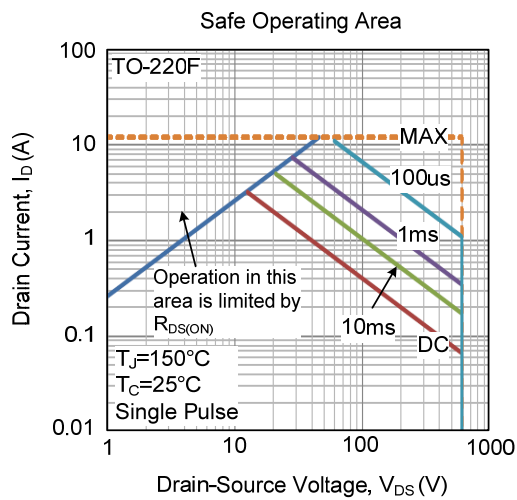


■ TYPICAL CHARACTERISTICS (Cont.)





■ TYPICAL CHARACTERISTICS (Cont.)



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