# UNISONIC TECHNOLOGIES CO., LTD

6N65-CB **Preliminary** Power MOSFET

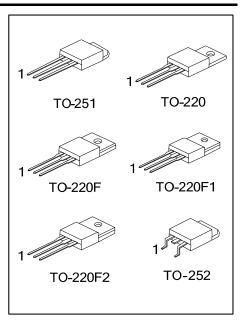
# 6.0A, 650V **N-CHANNEL POWER MOSFET**

#### **DESCRIPTION**

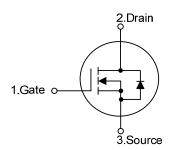
The UTC 6N65-CB is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in switching power supplies and adaptors.

### **FEATURES**

- \*  $R_{DS(ON)}$  < 1.62 $\Omega$  @  $V_{GS}$  = 10V,  $I_{D}$  = 3.0A
- \* Fast switching capability
- \* Avalanche energy tested
- \* Improved dv/dt capability, high ruggedness



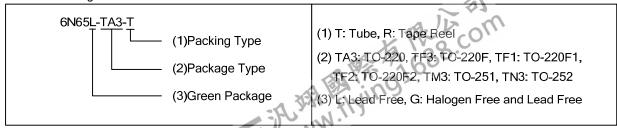
#### **SYMBOL**



#### **ORDERING INFORMATION**

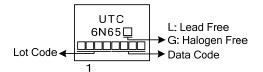
Ordering Number		Dealtage	Pin Assignment			Deaking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
6N65L-TA3-T	6N65G-TA3-T	N65G-TA3-T TO-220 G		D	S	Tube	
6N65L-TF1-T	6N65G-TF1-T	TO-220F1	G	D	S	Tube	
6N65L-TF2-T	6N65G-TF2-T	TO-220F2	G	D	S	Tube	
6N65L-TF3-T	6N65G-TF3-T	TO-220F	G	D	S	Tube	
6N65L-TM3-R	6N65G-TM3-R	TO-251	G	D	S	Tape Reel	
6N65L-TN3-R	6N65G-TN3-R	TO-252	G	D	S	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



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## **MARKING**





## ■ ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	650	V
Gate-Source Voltage		$V_{GSS}$	±30	V
Drain Current	Continuous	I <sub>D</sub>	6	Α
	Pulsed (Note 2)	$I_{DM}$	24	Α
Avalanche Current (Note 2)		I <sub>AR</sub>	2.6	Α
Avalanche Energy	Single Pulsed (Note 3)	E <sub>AS</sub>	34	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3	V/ns
Power Dissipation	TO-220		125	W
	TO-220F/TO-220F1	В	40	W
	TO-220F2	$P_D$	42	
	TO-251/TO-252		55	W
Junction Temperature		TJ	+150	°C
Storage Temperature		T <sub>STG</sub>	-55 ~ +150	°C

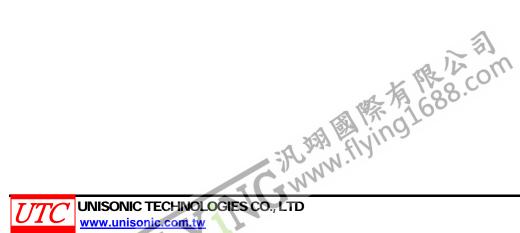
Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L = 10mH,  $I_{AS}$  = 2.6A,  $V_{DD}$  = 50V,  $R_{G}$  = 25  $\Omega$ , Starting  $T_{J}$  = 25°C
- 4.  $I_{SD} \le 6.0 \text{A}$ , di/dt  $\le 200 \text{A}/\mu \text{s}$ ,  $V_{DD} \le BV_{DSS}$ , Starting  $T_J = 25^{\circ}\text{C}$

## **■ THERMAL DATA**

PARAMETER	PACKAGE	SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2	$\theta_{JA}$	62.5	°C/W
	TO-251/TO-252		110	°C/W
Junction to Case	TO-220		1	°C/W
	TO-220F/TO-220F1	0	3.2	°C/W
	TO-220F2	θις	2.97	°C/W
	TO-251/TO-252		2.27	°C/W



## ELECTRICAL CHARACTERISTICS (T<sub>J</sub> =25°C, unless otherwise specified)

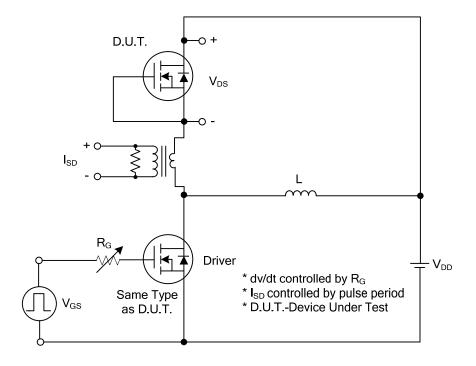
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS			•				
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	650			V
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> =650V, V <sub>GS</sub> =0V			10	μΑ
Gate- Source Leakage Current	Forward		$V_{G=30V}$ , $V_{DS}=0V$			100	nA
	Reverse	$I_{GSS}$	V <sub>GS</sub> =-30V, V <sub>DS</sub> =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$ , $I_D=250\mu A$			4.0	V
Static Drain-Source On-State Resistance		R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =3.1A			1.62	Ω
DYNAMIC CHARACTERISTICS					=.		
Input Capacitance		$C_{ISS}$			850		pF
Output Capacitance		Coss	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1.0 MHz		80		pF
Reverse Transfer Capacitance		$C_{RSS}$	]		65		pF
SWITCHING CHARACTERISTICS	S				=.		
Total Gate Charge (Note 1)		$Q_G$	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =1.3A		23		nC
Gate to Source Charge		$Q_GS$	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =1.3A -I <sub>G</sub> =100μA (Note 1, 2)		4		nC
Gate to Drain Charge		$Q_GD$	IG-100μΑ (Note 1, 2)		4.5		nC
Turn-ON Delay Time (Note 1)		$t_{D(ON)}$			55		ns
Rise Time		$t_R$	$V_{DD}$ =30V, $V_{GS}$ =10V, $I_{D}$ =0.5A,		27		ns
Turn-OFF Delay Time		$t_{D(OFF)}$	$R_G = 25\Omega$ (Note 1, 2)		180		ns
Fall-Time	Fall-Time				31		ns
DRAIN-SOURCE DIODE CHARA	CTERISTICS	S AND MAXIM	IUM RATINGS				
Maximum Body-Diode Continuous Current		Is				6	Α
Maximum Body-Diode Pulsed Current		I <sub>SM</sub>				24	Α
Drain-Source Diode Forward Voltage (Note 1)		$V_{SD}$	I <sub>S</sub> =6.0A, V <sub>GS</sub> =0V			1.4	V
Body Diode Reverse Recovery Time (Note 1)		t <sub>rr</sub>	I <sub>S</sub> =6.0A, V <sub>GS</sub> =0V,		470		ns
Body Diode Reverse Recovery Charge		$Q_{rr}$	dI <sub>F</sub> /dt=100A/μs (Note 1)		2.3		μC

Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%.

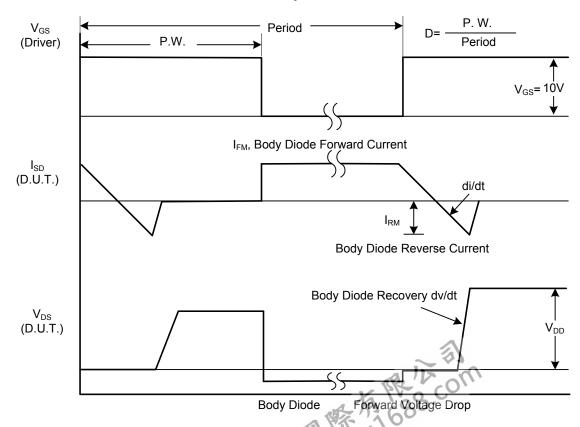


<sup>2.</sup> Essentially independent of operating temperature.

### **■ TEST CIRCUITS AND WAVEFORMS**

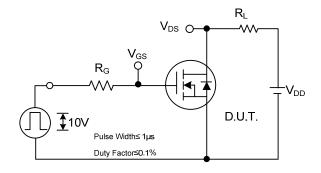


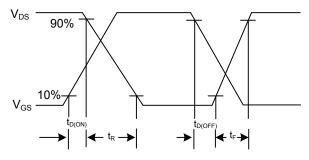
## Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

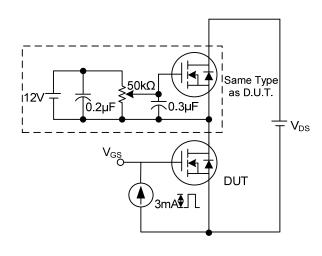
# **TEST CIRCUITS AND WAVEFORMS (Cont.)**

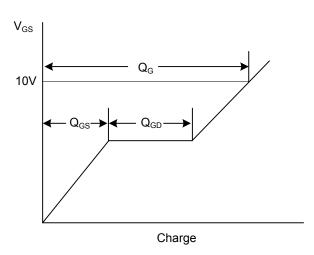




**Switching Test Circuit** 

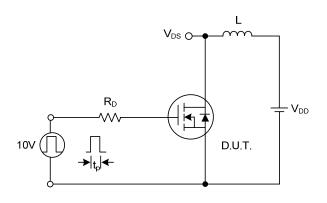
**Switching Waveforms** 

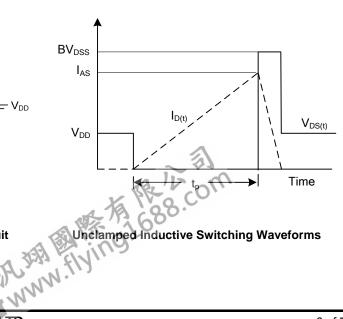




**Gate Charge Test Circuit** 

**Gate Charge Waveform** 





**Unclamped Inductive Switching Test Circuit** 

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