UNISONIC TECHNOLOGIES CO., LTD

6N65-P Power MOSFET

6.2A, 650V **N-CHANNEL POWER MOSFET**

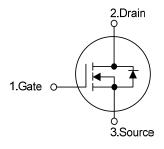
DESCRIPTION

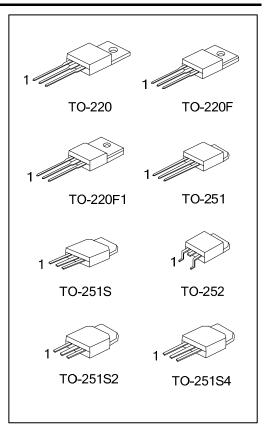
The UTC 6N65-P is a high voltage power MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

FEATURES

- * $R_{DS(ON)}$ < 2.00 @ V_{GS} = 10V, I_D = 3.1A
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness

SYMBOL

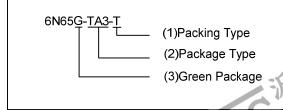




ORDERING INFORMATION

| Ordering Number | | Package | Pin Assignment | | | Packing | |
|-----------------|--------------|----------|----------------|---|---|-----------|--|
| Lead Free | Halogen Free | Fackage | 1 | 2 | 3 | i acking | |
| 6N65L-TA3-T | 6N65G-TA3-T | TO-220 | G | D | S | Tube | |
| 6N65L-TF1-T | 6N65G-TF1-T | TO-220F1 | G | D | S | Tube | |
| 6N65L-TF3-T | 6N65G-TF3-T | TO-220F | G | D | S | Tube | |
| 6N65L-TM3-T | 6N65G-TM3-T | TO-251 | G | D | S | Tube | |
| 6N65L-TMS-T | 6N65G-TMS-T | TO-251S | G | D | S | Tube | |
| 6N65L-TMS2-T | 6N65G-TMS2-T | TO-251S2 | G | D | S | Tube | |
| 6N65L-TMS4-T | 6N65G-TMS4-T | TO-251S4 | G | D | S | Tube | |
| 6N65L-TN3-R | 6N65G-TN3-R | TO-252 | G | D | S | Tape Reel | |

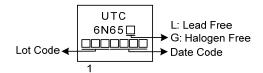
Pin Assignment: G: Gate D: Drain S: Source



- (1) T: Tube, R: Tape Reel
- (2) TA3: TO-220, TF1: TO-220F1, TF3: TO-220F,
 - TM3: TO-251, TN3: TO-252, TMS: TO-251S,
 - TMS2: TO-251S2, TMS4: TO-251S4
- (3) G: Halogen Free and Lead Free, L: Lead Free

www.unisonic.com.tw 1 of 9

MARKING





■ **ABSOLUTE MAXIMUM RATINGS** (T_C = 25°C, unless otherwise specified)

| PARAMETER | | SYMBOL | RATINGS | UNIT |
|------------------------------------|------------------------|-----------------|------------|------|
| Drain-Source Voltage | | V_{DSS} | 650 | V |
| Gate-Source Voltage |) | V_{GSS} | ±30 | V |
| Avalanche Current (N | Note 2) | I _{AR} | 6.2 | Α |
| Continuous Drain Cu | rrent | I_D | 6.2 | Α |
| Pulsed Drain Current (Note 2) | | I_{DM} | 24.8 | Α |
| Avalanche Energy | Single Pulsed (Note 3) | E _{AS} | 130 | mJ |
| Peak Diode Recovery dv/dt (Note 4) | | dv/dt | 4.5 | V/ns |
| • | TO-220 | P _D | 125 | W |
| | TO-220F/TO-220F1 | | 40 | W |
| Power Dissipation | TO-251/TO-251S | | | |
| | TO-251S2/TO-251S4 | | 55 | W |
| | TO-252 | | | |
| Junction Temperature | | TJ | +150 | °C |
| Operating Temperature | | T_OPR | -55 ~ +150 | °C |
| Storage Temperature | | T_{STG} | -55 ~ +150 | °C |

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L = 7.2mH, I_{AS} = 6A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C
- 4. $I_{SD} \le 6.2A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

| PARAMETER | | SYMBOL | RATING | UNIT |
|---------------------------------------|---|--------|--------|------|
| Junction to Ambient | TO-220/TO-220F TO-220F1 | | 62.5 | °C/W |
| | TO-251/TO-251S TO-251S2/TO-251S4 TO-252 | θЈА | 110 | °C/W |
| Junction to Ambient Junction to Case | TO-220 | | 1.0 | °C/W |
| | TO-220F/TO-220F1 | | 3.2 | °C/W |
| | TO-251/TO-251S TO-251S2/TO-251S4 TO-252 | θις | 2.27 | °C/W |



ELECTRICAL CHARACTERISTICS (T_J =25°C, unless otherwise specified)

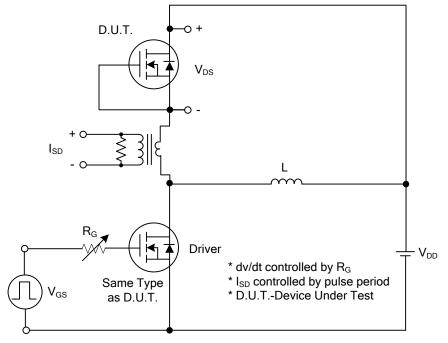
| PARAMETER | | SYMBOL | TEST CONDITIONS | | TYP | MAX | UNI T |
|---|-------------------|-------------------------------------|--|-----|------|------|----------|
| OFF CHARACTERISTICS | | • | | | | | |
| Drain-Source Breakdown Voltage | | BV _{DSS} | $V_{GS} = 0V, I_D = 250\mu A$ | 650 | | | V |
| Drain-Source Leakage Current | | I _{DSS} | V _{DS} = 650V, V _{GS} = 0V | | | 10 | μA |
| Gate- Source Leakage Current | Forward | I _{GSS} | $V_{GS} = 30V, V_{DS} = 0V$ | | | 100 | nA |
| | Reverse | | $V_{GS} = -30V, V_{DS} = 0V$ | | | -100 | nA |
| Breakdown Voltage Temperature | Coefficient | △BV _{DSS} /△T _J | I _D =250μA, Referenced to 25°C | | 0.53 | | V/°C |
| ON CHARACTERISTICS | | | | | | | |
| Gate Threshold Voltage | | $V_{GS(TH)}$ | $V_{DS} = V_{GS}, I_{D} = 250 \mu A$ | 2.0 | | 4.0 | V |
| Static Drain-Source On-State Resistance | | R _{DS(ON)} | $V_{GS} = 10V, I_D = 3.1A$ | | 1.6 | 2.0 | Ω |
| DYNAMIC CHARACTERISTICS | | | | | | | |
| Input Capacitance | Input Capacitance | | | | 920 | | pF |
| Output Capacitance | | C _{ISS} | V _{DS} =25V, V _{GS} =0V, f=1.0 MHz | | 80 | | рF |
| Reverse Transfer Capacitance | | C _{RSS} | | | 10 | | рF |
| SWITCHING CHARACTERISTIC | S | | | | | | |
| Total Gate Charge | | Q_G | \\ -200\\ I -40 \\ -10\\ | | 26 | | nC |
| Gate-Source Charge | | Q_GS | V_{DS} =200V, I_{D} =4A, V_{GS} =10V I_{G} =3mA (Note 1, 2) | | 7 | | nC |
| Gate-Drain Charge | | Q_GD | IG-SITIA (Note 1, 2) | | 8.4 | | nC |
| Turn-On Delay Time | | t _{D(ON)} | | | 40 | | ns |
| Turn-On Rise Time | | t _R | V_{DD} =30V, I_{D} =0.5A, V_{GS} =10V | | 40 | | ns |
| Turn-Off Delay Time | | t _{D(OFF)} | $R_G = 25\Omega$ (Note 1, 2) | | 140 | | ns |
| Turn-Off Fall Time | | t _F | | | 60 | | ns |
| DRAIN-SOURCE DIODE CHARA | CTERISTIC | CS AND MAXII | MUM RATINGS | - | - | = | - |
| Maximum Continuous Drain-Source Diode | | Is | | | | 6.2 | Α |
| Forward Current | | | | | | 0.2 | A |
| Maximum Pulsed Drain-Source Diode | | I _{SM} | | | | 24.8 | Α |
| Forward Current | | | | | | 24.0 | Α |
| Drain-Source Diode Forward Voltage | | V_{SD} | $V_{GS} = 0 \text{ V}, I_{S} = 6.2 \text{ A}$ | | | 1.4 | V |

Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%

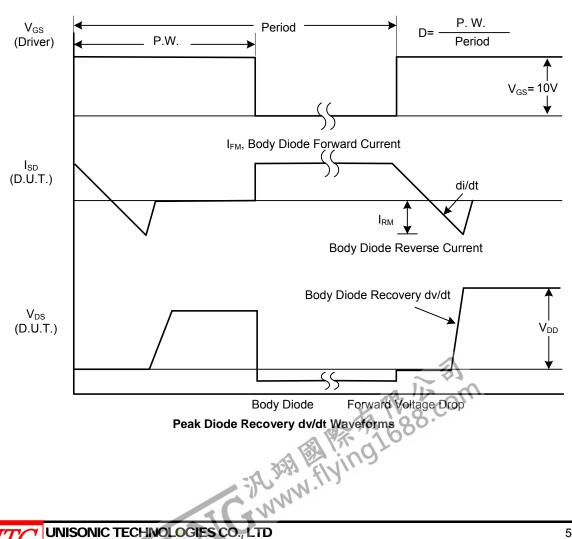
2. Essentially independent of operating temperature



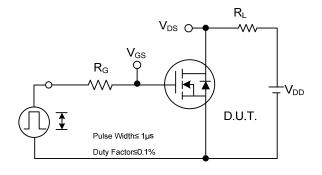
TEST CIRCUITS AND WAVEFORMS

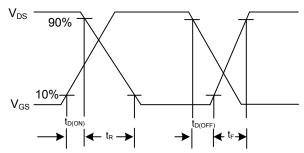


Peak Diode Recovery dv/dt Test Circuit



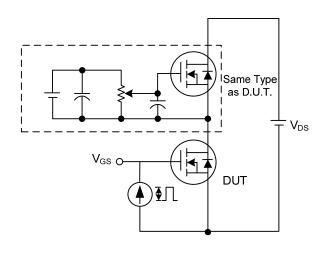
TEST CIRCUITS AND WAVEFORMS

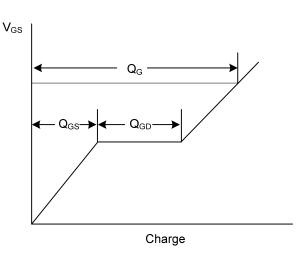




Switching Test Circuit

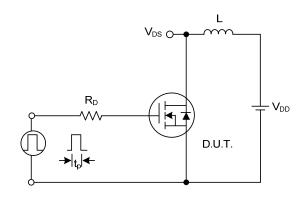
Switching Waveforms

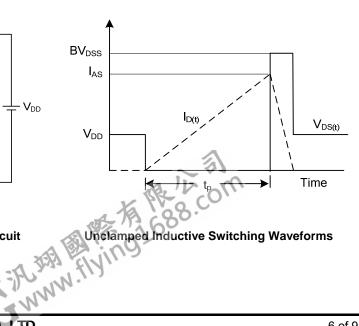




Gate Charge Test Circuit

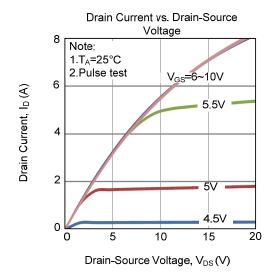
Gate Charge Waveform

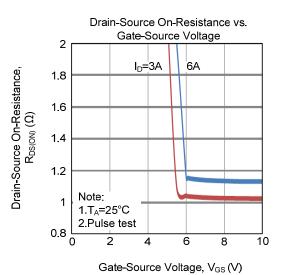


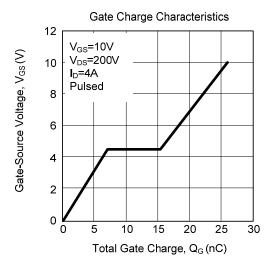


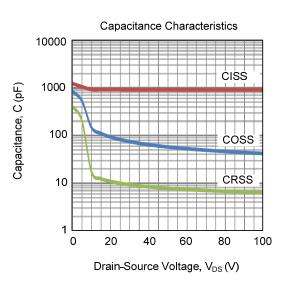
Unclamped Inductive Switching Test Circuit

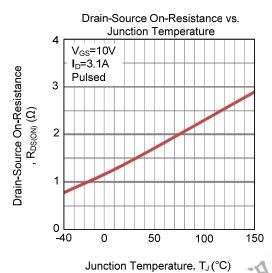
■ TYPICAL CHARACTERISTICS

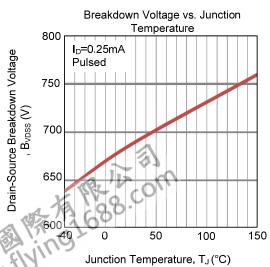




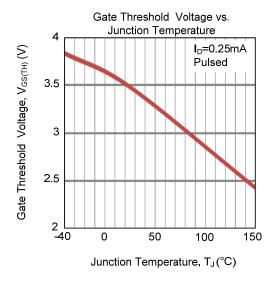


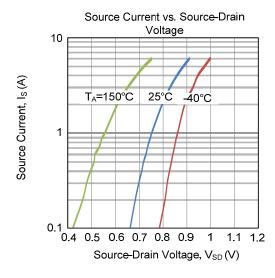


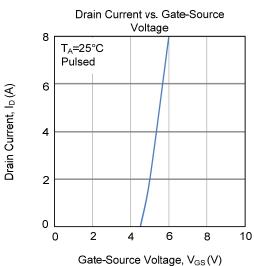


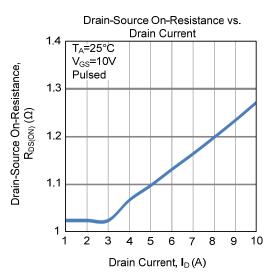


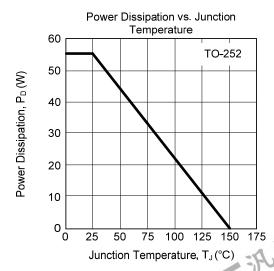
■ TYPICAL CHARACTERISTICS (Cont.)

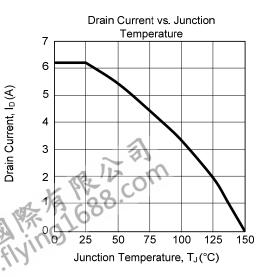




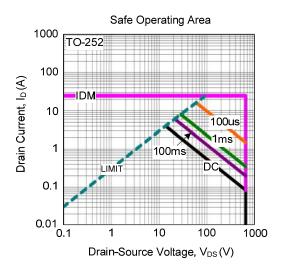








■ TYPICAL CHARACTERISTICS (Cont.)



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