

6NM95

Preliminary

Power MOSFET

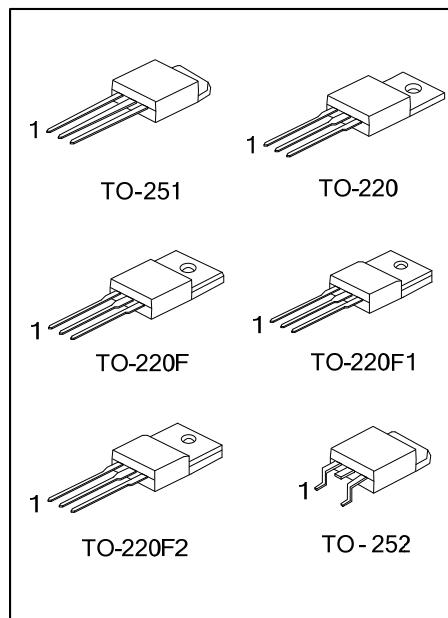
6.0A, 950V N-CHANNEL
SUPER-JUNCTION MOSFET

■ DESCRIPTION

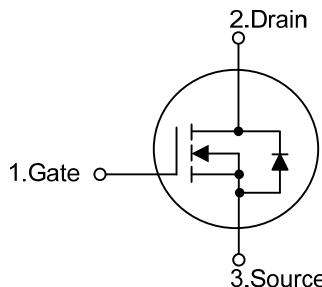
The **UTC 6NM95** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at DC-DC, AC-DC converters for power applications.

■ FEATURES

- * $R_{DS(on)} < 1.9 \Omega$ @ $V_{GS}=10V$, $I_D=3.0A$
- * Improved dv/dt capability
- * Fast switching
- * 100% avalanche tested



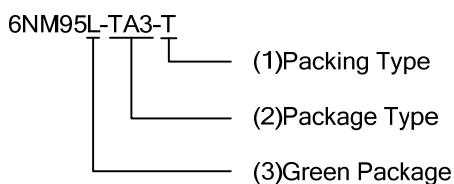
■ SYMBOL



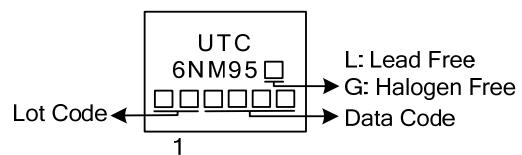
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
6NM95L-TA3-T	6NM95G-TA3-T	TO-220	G	D	S	Tube
6NM95L-TF3-T	6NM95G-TF3-T	TO-220F	G	D	S	Tube
6NM95L-TF1-T	6NM95G-TF1-T	TO-220F1	G	D	S	Tube
6NM95L-TF2-T	6NM95G-TF2-T	TO-220F2	G	D	S	Tube
6NM95L-TM3-T	6NM95G-TM3-T	TO-251	G	D	S	Tube
6NM95L-TN3-R	6NM95G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source



- (1) T: Tube, R: Tape Reel
- (2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1, TF2: TO-220F2, TM3: TO-251, TN3: TO-252
- (3) L: Lead Free, G: Halogen Free and Lead Free

■ MARKING

■ ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT	
Drain-Source Voltage	V_{DSS}	950	V	
Gate-Source Voltage	V_{GSS}	± 30	V	
Continuous Drain Current	I_D	6.0	A	
Pulsed Drain Current (Note 2)	I_{DM}	24	A	
Avalanche Current (Note 2)	I_{AR}	3.0	A	
Single Pulsed Avalanche Energy (Note 3)	E_{AS}	45	mJ	
Peak Diode Recovery dv/dt (Note 4)	dv/dt	1.56	V/ns	
Power Dissipation	TO-220	P_D	132	W
	TO-220F/TO-220F1		56	W
	TO-220F2		60	W
	TO-251/TO-252			
Junction Temperature	T_J	+150	$^\circ\text{C}$	
Storage Temperature	T_{STG}	-55 ~ +150	$^\circ\text{C}$	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

3. $L = 10\text{mH}$, $I_{AS} = 3.0\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 6.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction-to-Ambient	θ_{JA}	62.5	$^\circ\text{C/W}$
		110	$^\circ\text{C/W}$
Junction-to-Case	θ_{JC}	0.95	$^\circ\text{C/W}$
		2.23	$^\circ\text{C/W}$
		2.08	$^\circ\text{C/W}$

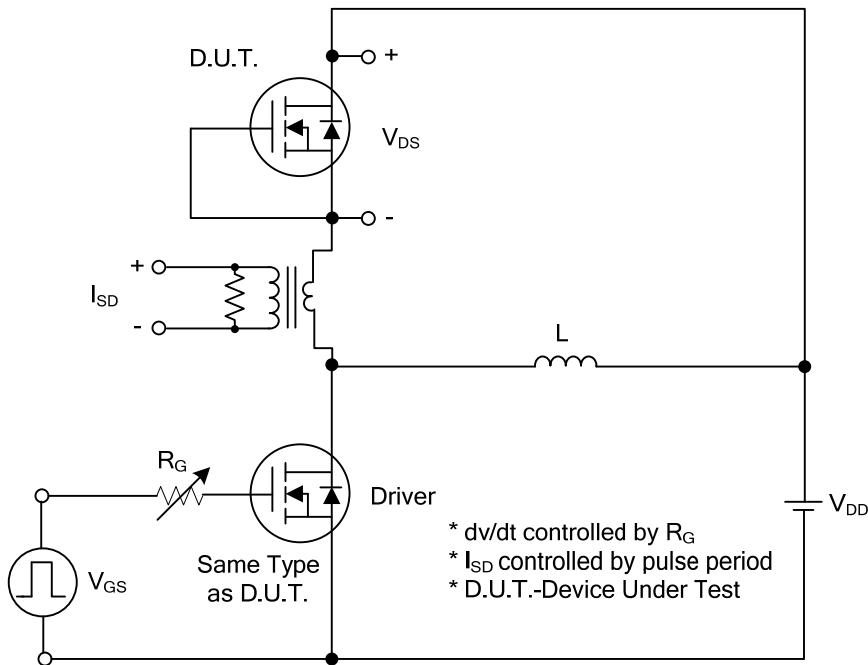
■ ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 250\mu\text{A}$	950			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}} = 950\text{V}, V_{\text{GS}} = 0\text{V}$		10		μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}} = 30\text{V}, V_{\text{DS}} = 0\text{V}$		100		nA
		$V_{\text{GS}} = -30\text{V}, V_{\text{DS}} = 0\text{V}$		-100		nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS(TH)}}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250\mu\text{A}$	2.5		4.5	V
Static Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}} = 10\text{V}, I_{\text{D}} = 3.0\text{A}$			1.9	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$		500		pF
Output Capacitance	C_{OSS}			165		pF
Reverse Transfer Capacitance	C_{RSS}			7.0		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge (Note 1)	Q_G	$V_{\text{DS}}=50\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=1.3\text{A}, I_{\text{G}}=100\mu\text{A}$ (Note 1, 2)		50		nC
Gate to Source Charge	Q_{GS}			4.5		nC
Gate to Drain Charge	Q_{GD}			14.5		nC
Turn-ON Delay Time (Note 1)	$t_{\text{D(ON)}}$	$V_{\text{DD}}=30\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=0.5\text{A}, R_{\text{G}}=25\Omega$ (Note 1, 2)		50		nS
Rise Time	t_R			85		nS
Turn-OFF Delay Time	$t_{\text{D(OFF)}}$			220		nS
Fall-Time	t_F			48		nS
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				6	A
Maximum Body-Diode Pulsed Current	I_{SM}				24	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$I_S=6.0\text{A}, V_{\text{GS}}=0\text{V}$			1.4	V
Body Diode Reverse Recovery Time (Note 1)	t_{rr}	$I_S=6.0\text{A}, V_{\text{GS}}=0\text{V}, dI_F/dt = 100\text{A}/\mu\text{s}$		525		nS
Body Diode Reverse Recovery Charge	Q_{rr}			5.6		μC

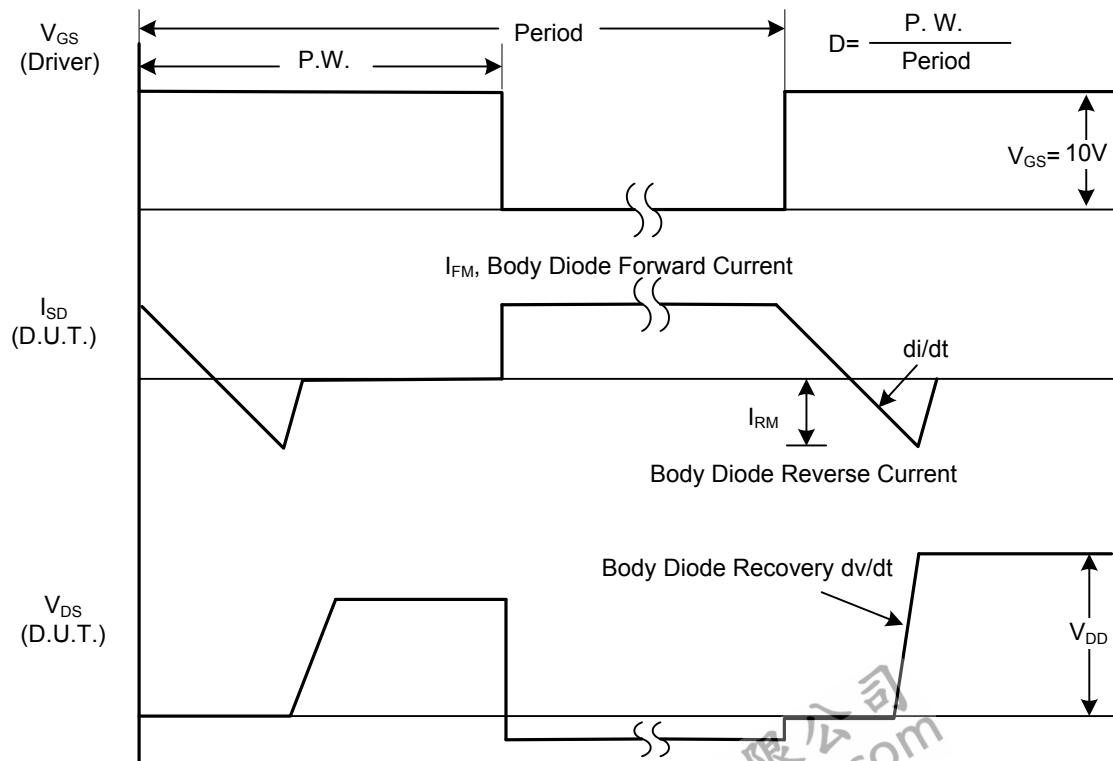
Notes: 1. Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

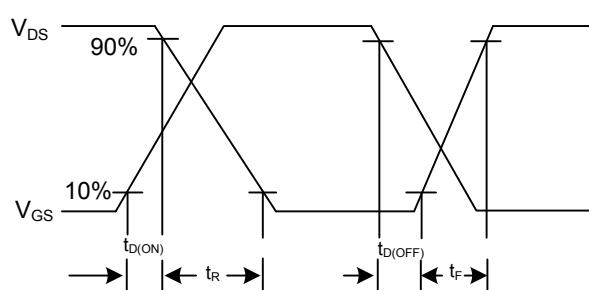
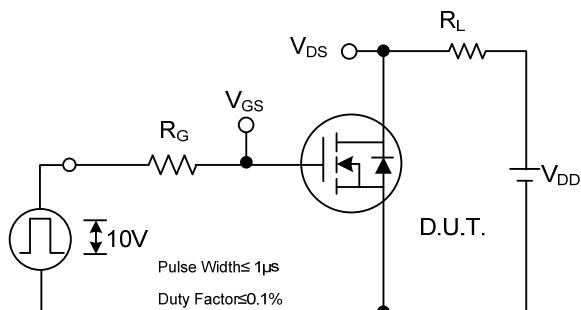


Peak Diode Recovery dv/dt Test Circuit



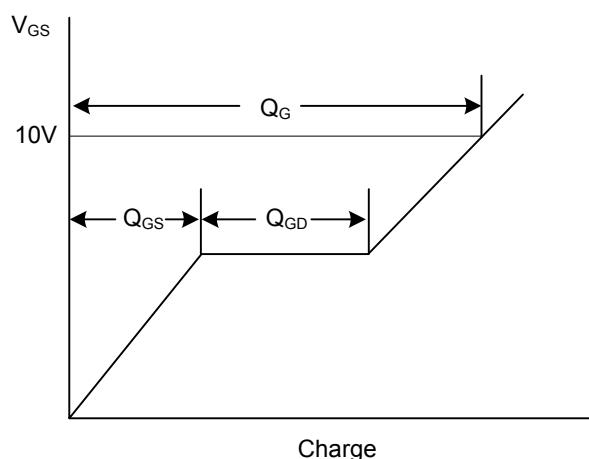
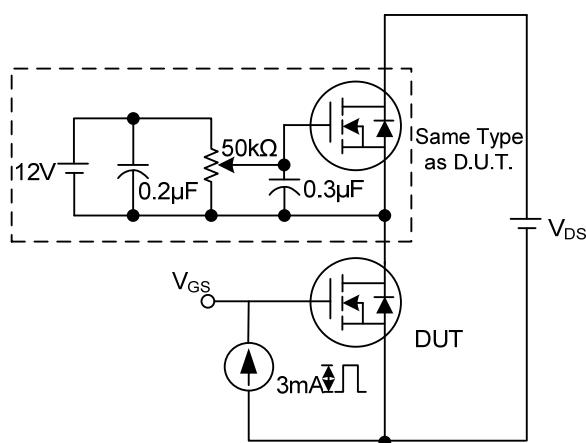
Peak Diode Recovery dv/dt Waveforms

■ TEST CIRCUITS AND WAVEFORMS (Cont.)



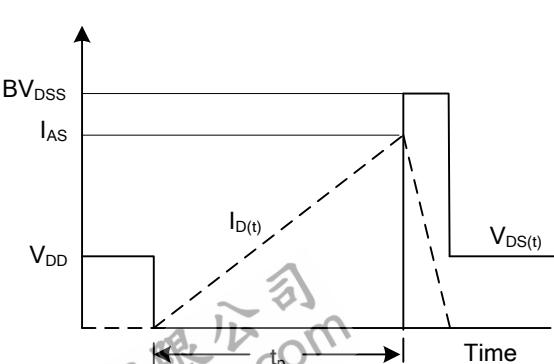
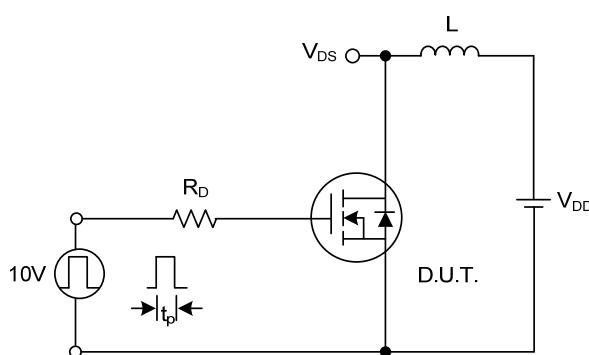
Switching Test Circuit

Switching Waveforms



Gate Charge Test Circuit

Gate Charge Waveform



Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

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