# UNISONIC TECHNOLOGIES CO., LTD

**7N70** Power MOSFET

# **7A, 700V N-CHANNEL POWER MOSFET**

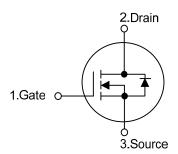
#### **DESCRIPTION**

The UTC 7N70 is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

#### **FEATURES**

- \*  $R_{DS(ON)}$  < 1.6 $\Omega$  @ $V_{GS}$  = 10 V
- \* Fast switching capability
- \* Avalanche energy specified
- \* Improved dv/dt capability, high ruggedness

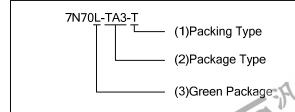




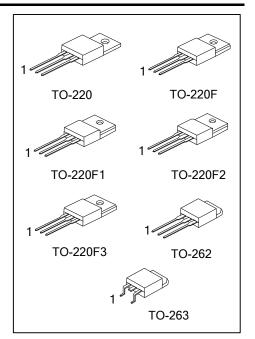
### ORDERING INFORMATION

Order Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
7N70L-TA3-T	7N70G-TA3-T	TO-220	G	D	S	Tube	
7N70L-TF3-T	7N70G-TF3-T	TO-220F	G	D	S	Tube	
7N70L-TF1-T	7N70G-TF1-T	TO-220F1	G	D	S	Tube	
7N70L-TF2-T	7N70G-TF2-T	TO-220F2	G	D	S	Tube	
7N70L-TF3T-T	7N70G-TF3T-T	TO-220F3	G	D	S	Tube	
7N70L-T2Q-T	7N70G-T2Q-T	TO-262	G	D	S	Tube	
7N70L-TQ2-T	7N70G-TQ2-T	TO-263	G	D	S	Tube	
7N70L-TQ2-R	7N70G-TQ2-R	TO-263	G	D	S	Tape Reel	

Pin Assignment: G: Gate D: Drain S: Source Note:

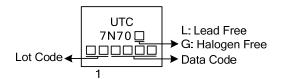


- (1) T: Tube, R: Tape Reel
- (2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1,
  - TF3T: TO-220F3, T2Q: TO-262, TQ2: TO-263
- (3) L: Lead Free, G: Halogen Free



www.unisonic.com.tw 1 of 8

#### **MARKING**





#### ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>C</sub> = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	700	V
Gate-Source Voltage		$V_{GSS}$	±30	V
Continuous Drain Current	$T_C = 25^{\circ}C$		7.0	Α
	$T_{\rm C} = 100^{\circ}{\rm C}$	I <sub>D</sub>	4.7	Α
Drain Current Pulsed (Note 2)		$I_{DM}$	28	Α
Avalanche Energy, Single Pulsed (Note 3)		E <sub>AS</sub>	530	mJ
Avalanche Energy, Repetitive, Limited by T <sub>JMAX</sub>		$E_{AR}$	14.2	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation (T <sub>C</sub> = 25°C)	TO-220F/TO-220F1 TO-220F3		48	W
	TO-220/TO-262 TO-263	$P_D$	142	W
	TO-220F2		50	W
Junction Temperature		$T_J$	+150	°C
Storage Temperature		$T_{STG}$	-55 ~ +150	Ô

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating : Pulse width limited by  $T_{\mathsf{J}}$
- 3. L=19.5mH, I<sub>AS</sub>=7.0A, V<sub>DD</sub>=50V, R<sub>G</sub>=0  $\Omega$ , Starting T<sub>J</sub>=25°C
- 4.  $I_{SD} \le 7.0 A$ , di/dt  $\le 100 A/\mu s$ ,  $V_{DD} \le BV_{DSS}$ , Starting  $T_J = 25 ^{\circ}C$

#### **■ THERMAL DATA**

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient		$\theta_{JA}$	62.5	°C/W
Junction to Case	TO-220F/TO-220F1 TO-220F3		2.6	°C/W
	TO-220/TO-262 TO-263	θις	0.88	°C/W
	TO-220F2		2.5	°C/W

#### ■ ELECTRICAL CHARACTERISTICS (T<sub>C</sub> =25°C, unless otherwise specified)

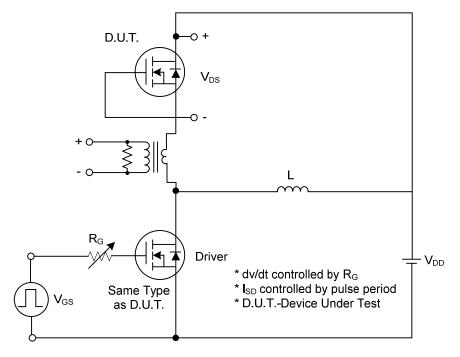
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	$V_{GS} = 0V, I_D = 250\mu A$	700			V
Danier Courses I college Courses		$V_{DS} = 700V, V_{GS} = 0V$			1	μΑ
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> = 560V, T <sub>C</sub> = 125°C			1	μΑ
Gate-Source Leakage Current Forwa	rd	$V_{GS} = 30V, V_{DS} = 0V$			100	nA
Rever	se I <sub>GSS</sub>	$V_{GS} = -30V, V_{DS} = 0V$			-100	nA
Breakdown Voltage Temperature Coeffic	ient ∆BV <sub>DSS</sub> /∆T <sub>J</sub>	I <sub>D</sub> = 250mA Referenced to 25°C		0.67		V/°C
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
Drain-Source ON-State Resistance	R <sub>DS(ON)</sub>	$V_{GS} = 10V, I_D = 3.5A$		1.4	1.6	Ω
Forward Transconductance (Note 1)	<b>9</b> FS	$V_{DS} = 40V, I_{D} = 3.5A$		8.0		S
DYNAMIC CHARACTERISTICS						
Input Capacitance	C <sub>ISS</sub>	V - 25V V - 0V		1200	1600	pF
Output Capacitance	Coss	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1MHz		150	190	pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			60	80	pF
SWITCHING CHARACTERISTICS	_		_			
Turn-on Delay Time	t <sub>D(ON)</sub>			60	80	ns
Turn-on Rise Time	t <sub>R</sub>	$V_{DD} = 30V, I_{D} = 1A$		200	230	ns
Turn-off Delay Time	t <sub>D(OFF)</sub>	(Note 1, 2)		280	350	ns
Turn-off Fall Time	t <sub>F</sub>			250	300	ns
Total Gate Charge	$Q_G$	V <sub>DS</sub> = 100V, I <sub>D</sub> = 7.0A,		163		nC
Gate-Source Charge	$Q_GS$	$V_{\rm DS}$ = 100V, $I_{\rm D}$ = 7.0A, $V_{\rm GS}$ = 10V (Note 1, 2)		12		nC
Gate-Drain Charge	$Q_{DD}$	V <sub>GS</sub> - 10V (Note 1, 2)		30		nC
SOURCE- DRAIN DIODE RATINGS AN	D CHARACTERIST	TICS				
Drain-Source Diode Forward Voltage	$V_{SD}$	$V_{GS} = 0V, I_{S} = 7.0A$			1.4	V
Maximum Continuous Drain-Source Dioc	tinuous Drain-Source Diode				7.0	Α
Forward Current	I <sub>S</sub>				7.0	^
Maximum Pulsed Drain-Source Diode	I <sub>SM</sub>				28	Α
Forward Current	ISM				20	
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS} = 0V, I_S = 7.0A,$		320		ns
Reverse Recovery Charge	$Q_{RR}$	$dI_F/dt = 100 A/\mu s$ (Note 1)		2.4		μC

Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%

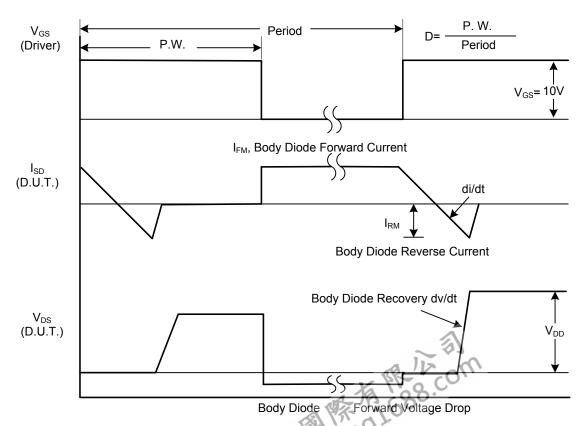
2. Essentially independent of operating temperature



#### **■ TEST CIRCUITS AND WAVEFORMS**

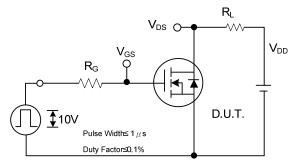


Peak Diode Recovery dv/dt Test Circuit

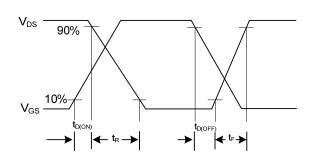


Peak Diode Recovery dv/dt Waveforms

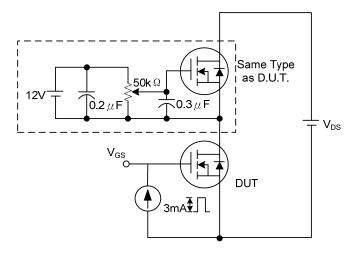
## ■ TEST CIRCUITS AND WAVEFORMS (Cont.)



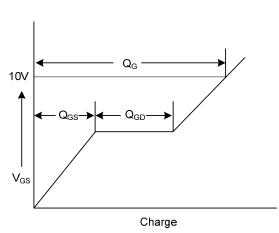
**Switching Test Circuit** 



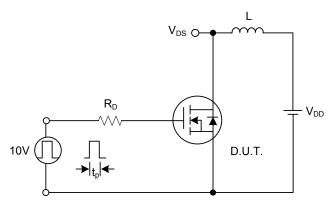
**Switching Waveforms** 



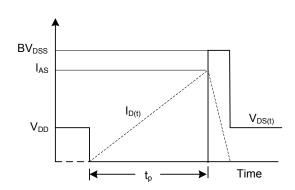
**Gate Charge Test Circuit** 



**Gate Charge Waveform** 

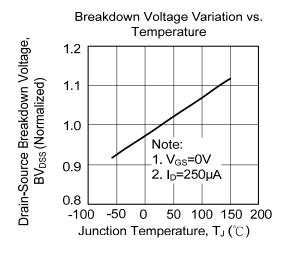


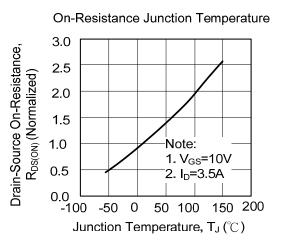
**Unclamped Inductive Switching Test Circuit** 



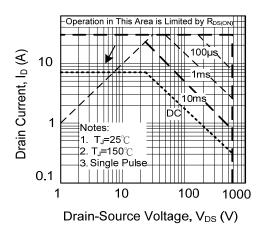
**Unclamped Inductive Switching Waveforms** 

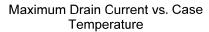
#### ■ TYPICAL CHARACTERISTICS

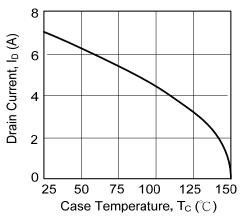


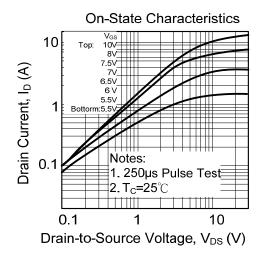


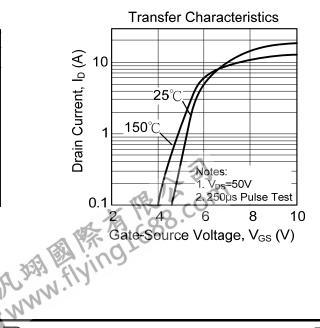
Maximum Safe Operating Area











#### **TYPICAL CHARACTERISTICS(Cont.)**

Thermal Response, θ<sub>JC</sub> (t)

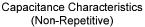
Transient Thermal Response Curve

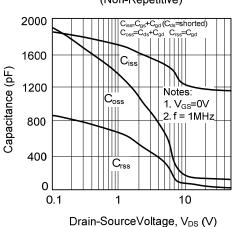
1E-4 1E-3 0.01 0.1 Square Wave Pulse Duration, t<sub>1</sub> (sec)

Duty Factor, D=t1/t2

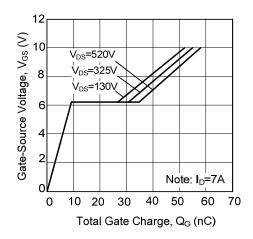
Allowable Case Temperature Reverse Drain Current, IDR (A) 150 **25**℃ Notes: 1. V<sub>GS</sub>=0V 2. 250µs Test 0.2 0.4 0.6 0.8 1.0 1.2 1.4 1.6 1.8 Source-Drain Voltage, V<sub>SD</sub> (V)

On State Current vs.





Gate Charge Characteristics



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