



9N50K-MT

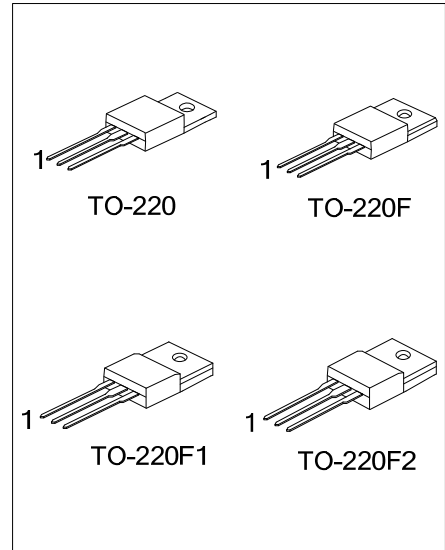
Power MOSFET

9A, 500V N-CHANNEL POWER MOSFET

DESCRIPTION

The UTC **9N50K-MT** is an N-channel mode power MOSFET using UTC's advanced technology to provide customers planar stripe and DMOS technology. This technology allows a minimum on-state resistance, superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

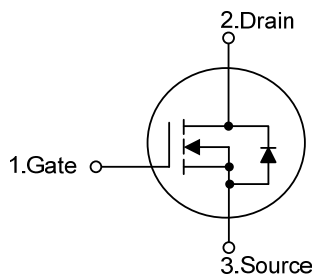
The UTC **9N50K-MT** is generally applied in high efficiency switch mode power supplies, active power factor correction and electronic lamp ballasts based on half bridge topology.



FEATURES

- * $R_{DS(ON)} < 0.85\Omega @ V_{GS} = 10V, I_D = 4.5A$
- * High Switching Speed
- * Improved dv/dt Capability
- * 100% Avalanche Tested

SYMBOL



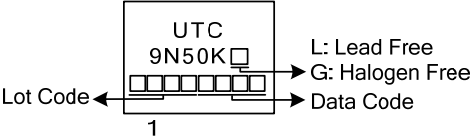
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
9N50KL-TA3-T	9N50KG-TA3-T	TO-220	G	D	S	Tube
9N50KL-TF3-T	9N50KG-TF3-T	TO-220F	G	D	S	Tube
9N50KL-TF1-T	9N50KG-TF1-T	TO-220F1	G	D	S	Tube
9N50KL-TF2-T	9N50KG-TF2-T	TO-220F2	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>9N50KL-TA3-T</p> <p>(1)Packing Type (2)Package Type (3)Green Package</p>	<p>(1) T: Tube (2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1, TF2: TO-220F2 (3) L: Lead Free, G: Halogen Free and Lead Free</p>
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MARKING



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■ ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$, unless otherwise noted)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	500	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	Continuous ($T_c=25^\circ\text{C}$)	I_D	9 (Note 5)	A
	Pulsed (Note 2)	I_{DM}	36 (Note 5)	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	360	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220	P_D	140	W
	TO-220F/TO-220F1		47	W
	TO-220F2			
Derate above 25°C	TO-220	P_D	1.1	W/ $^\circ\text{C}$
	TO-220F/TO-220F1		0.38	W/ $^\circ\text{C}$
	TO-220F2			
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55~+150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. $L = 8\text{mH}$, $I_{AS} = 9\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$
4. $I_{SD} \leq 9\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$
5. Drain current limited by maximum junction temperature

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient		θ_{JA}	62.5	$^\circ\text{C}/\text{W}$
Junction to Case	TO-220	θ_{JC}	0.8	$^\circ\text{C}/\text{W}$
	TO-220F/TO-220F1		2.62	$^\circ\text{C}/\text{W}$
	TO-220F2			

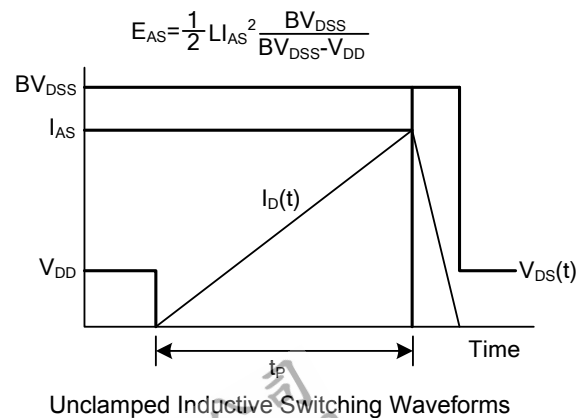
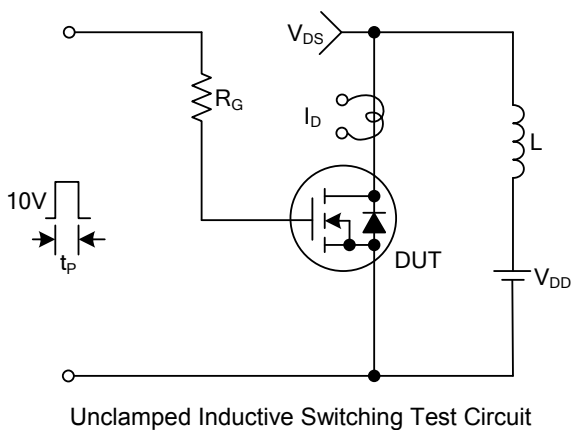
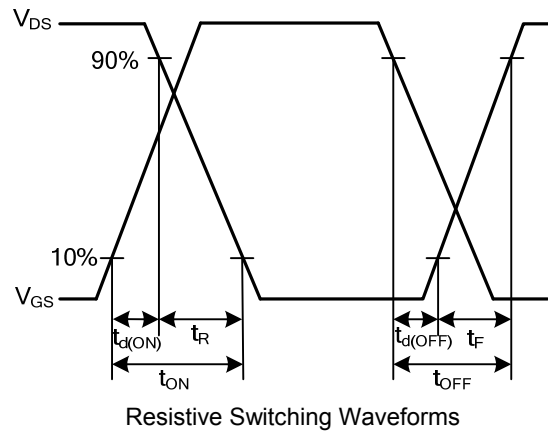
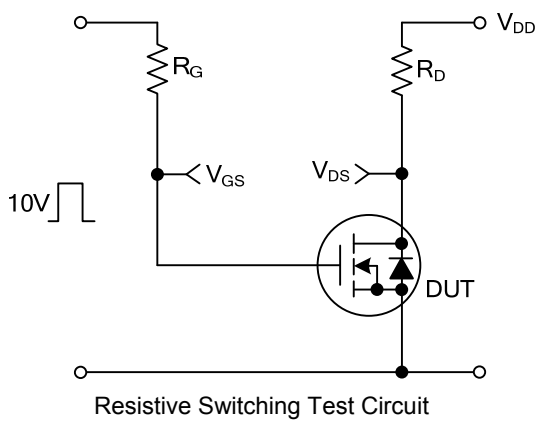
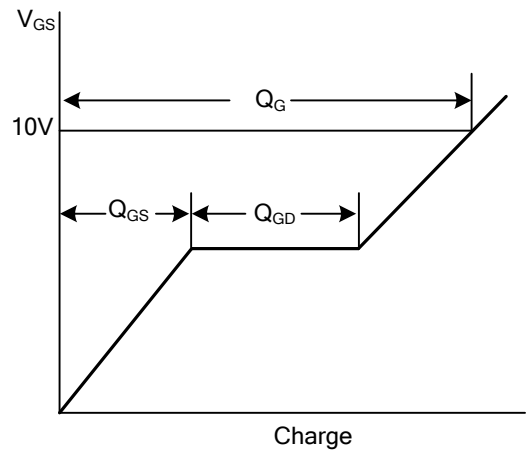
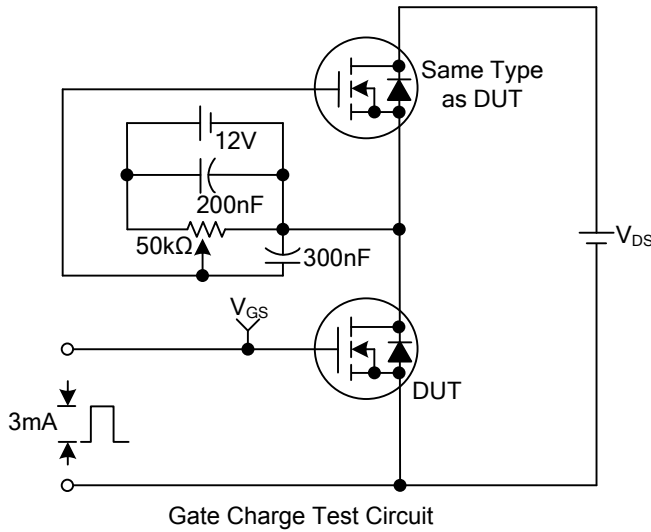
■ ELECTRICAL CHARACTERISTICS ($T_c=25^\circ\text{C}$, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu\text{A}$, $V_{GS}=0\text{V}$	500			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=500\text{V}$, $V_{GS}=0\text{V}$			1	μA
		$V_{DS}=400\text{V}$, $T_c=125^\circ\text{C}$			10	
Gate- Source Leakage Current	Forward	I_{GSS}				
	Reverse					
		$V_{GS}=+30\text{V}$, $V_{DS}=0\text{V}$			+100	nA
		$V_{GS}=-30\text{V}$, $V_{DS}=0\text{V}$			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10\text{V}$, $I_D=4.5\text{A}$			0.85	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{GS}=0\text{V}$, $V_{DS}=25\text{V}$, $f=1.0\text{MHz}$		685		pF
Output Capacitance	C_{OSS}			115		pF
Reverse Transfer Capacitance	C_{RSS}			12		pF
SWITCHING PARAMETERS						
Turn-ON Delay Time	$t_{D(ON)}$	$V_{DD}=30\text{V}$, $I_D=0.5\text{A}$, $R_G=25\Omega$ (Note 1, 2)		62		ns
Rise Time	t_R			86		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			149		ns
Fall-Time	t_F			83		ns
Total Gate Charge	Q_G	$V_{DS}=50\text{V}$, $I_D=1.3\text{A}$, $V_{GS}=10\text{V}$ (Note 1, 2)		27.2		nC
Gate to Source Charge	Q_{GS}			8.4		nC
Gate to Drain Charge	Q_{GD}			6.8		nC
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				9	A
Maximum Body-Diode Pulsed Current	I_{SM}				36	A
Drain-Source Diode Forward Voltage	V_{SD}	$I_S=9\text{A}$, $V_{GS}=0\text{V}$			1.4	V

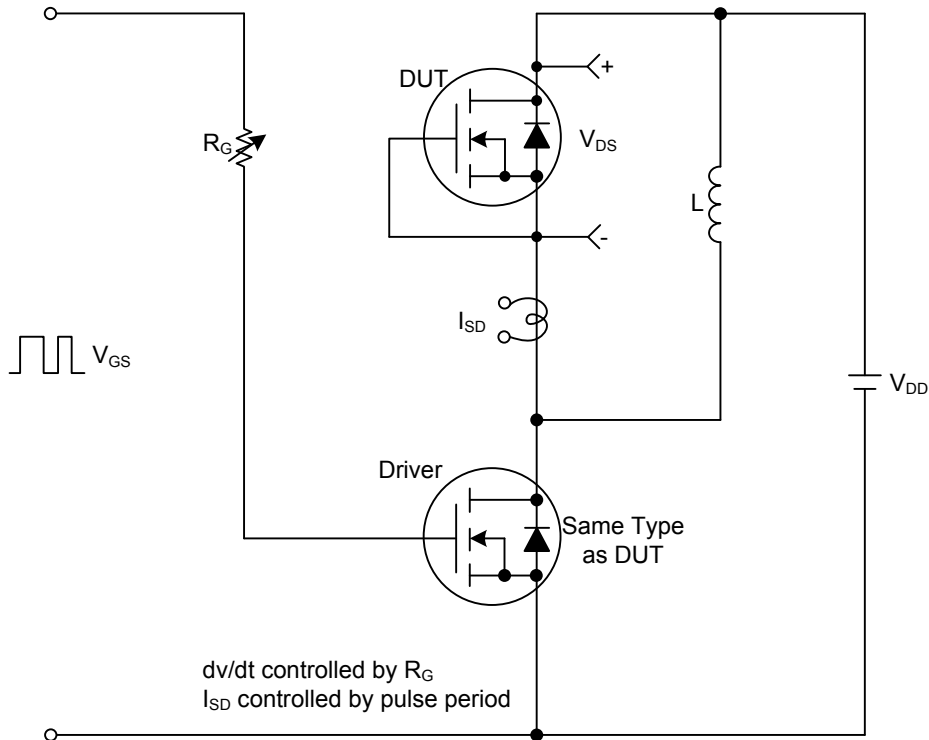
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$

2. Essentially independent of operating temperature.

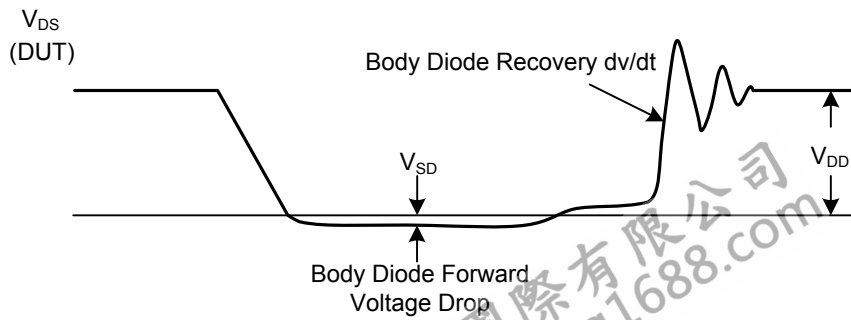
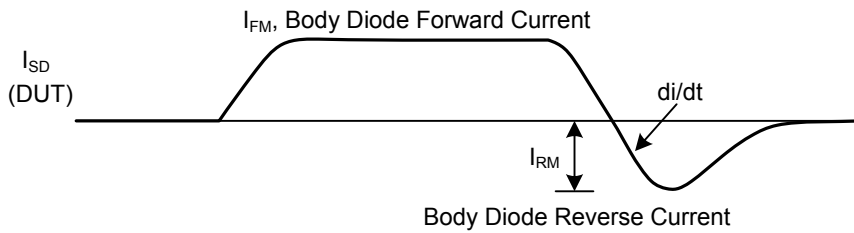
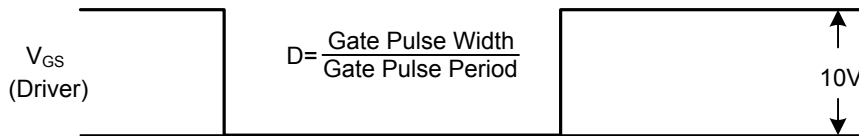
TEST CIRCUITS AND WAVEFORMS



■ TEST CIRCUITS AND WAVEFORMS(Cont.)



Peak Diode Recovery dv/dt Test Circuit & Waveforms



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