



9NM70

Power MOSFET

9A, 700V N-CHANNEL SUPER-JUNCTION MOSFET

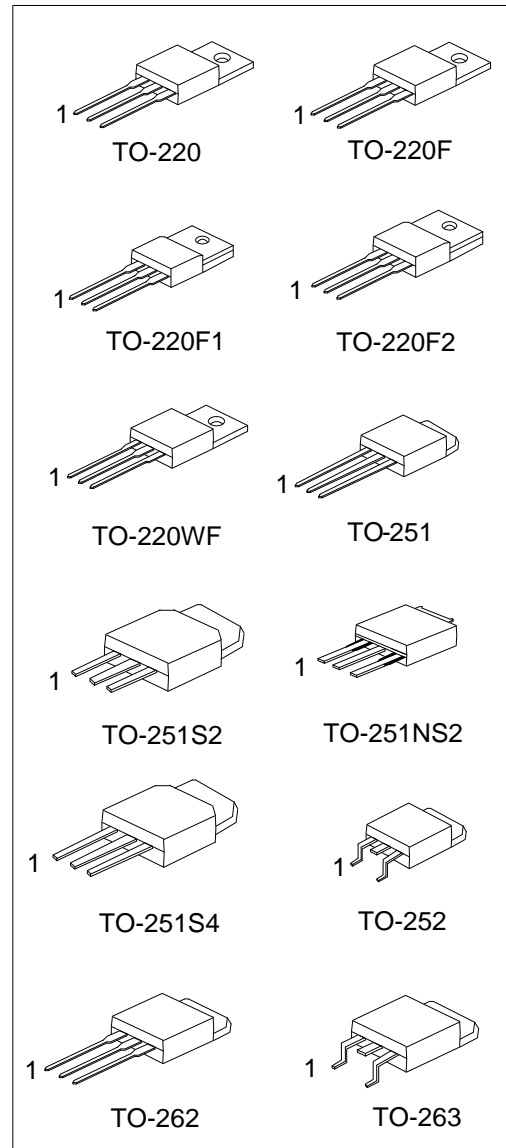
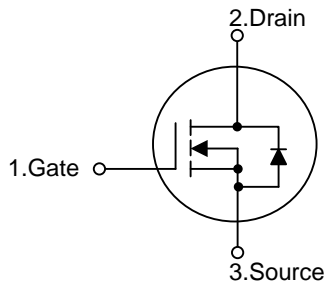
DESCRIPTION

The UTC 9NM70 is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

FEATURES

- * $R_{DS(ON)} \leq 0.7 \Omega$ @ $V_{GS}=10V, I_D=4.5A$
- * High switching Speed
- * 100% avalanche tested
- * Improved dv/dt capability

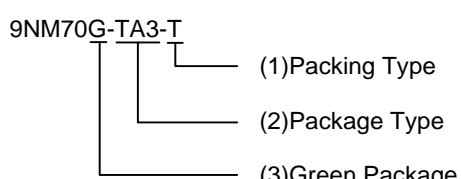
SYMBOL



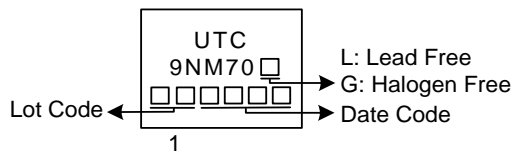
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
9NM70L-TA3-T	9NM70G-TA3-T	TO-220	G	D	S	Tube
9NM70L-TF1-T	9NM70G-TF1-T	TO-220F1	G	D	S	Tube
9NM70L-TF2-T	9NM70G-TF2-T	TO-220F2	G	D	S	Tube
9NM70L-TF3-T	9NM70G-TF3-T	TO-220F	G	D	S	Tube
9NM70L-TW1-T	9NM70G-TW1-T	TO-220WF	G	D	S	Tube
9NM70L-TM3-T	9NM70G-TM3-T	TO-251	G	D	S	Tube
9NM70L-TMS2-T	9NM70G-TMS2-T	TO-251S2	G	D	S	Tube
9NM70L-TMS4-T	9NM70G-TMS4-T	TO-251S4	G	D	S	Tube
9NM70L-TMN2-T	9NM70G-TMN2-T	TO-251NS2	G	D	S	Tube
9NM70L-TN3-R	9NM70G-TN3-R	TO-252	G	D	S	Tape Reel
9NM70L-T2Q-T	9NM70G-T2Q-T	TO-262	G	D	S	Tube
9NM70L-TQ2-T	9NM70G-TQ2-T	TO-263	G	D	S	Tube
9NM70L-TQ2-R	9NM70G-TQ2-R	TO-263	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>9NM70G-TA3-T</p>  <p>(1) Packing Type (2) Package Type (3) Green Package</p>	<p>(1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1, TF2: TO-220F2, TW1: TO-220WF, TM3: TO-251, TMS2: TO-251S2, TMS4: TO-251S4, TMN2: TO-251NS2, TN3: TO-252, T2Q: TO-262, TQ2: TO-263 (3) G: Halogen Free and Lead Free, L: Lead Free</p>
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MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	700	V
Gate-Source Voltage		V _{GSS}	±30	V
Drain Current	Continuous	I _D	9	A
	Pulsed (Note 2)	I _{DM}	18	A
Avalanche Current		I _{AR}	3.0	A
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	270	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	5.2	V/ns
Power Dissipation	TO-220/TO-262/TO-263	P _D	80	W
	TO-220F/TO-220F1		28	W
	TO-220F2/TO-220WF			
	TO-251/TO-251S2		57	W
	TO-251S4/TO-251NS2			
TO-252				
Junction Temperature		T _J	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L=60mH, I_{AS}=3.0A, V_{DD}=50V, R_G=25Ω, Starting T_J=25°C.

4. I_{SD} ≤ 9.0A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J=25°C.

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2 TO-220WF/TO-262 TO-263	θ _{JA}	62.5	°C/W
	TO-251/TO-251S2 TO-251S4/TO-251NS2 TO-252		110	
Junction to Case	TO-220/TO-220WF TO-262/TO-263	θ _{JC}	1.56	°C/W
	TO-220F/TO-220F1 TO-220F2		4.46	
	TO-251/TO-251S2 TO-251S4/TO-251NS2 TO-252		2.19 (Note)	

Note: Device mounted on FR-4 substrate P_C board, 2oz copper, with 1inch square copper plate.

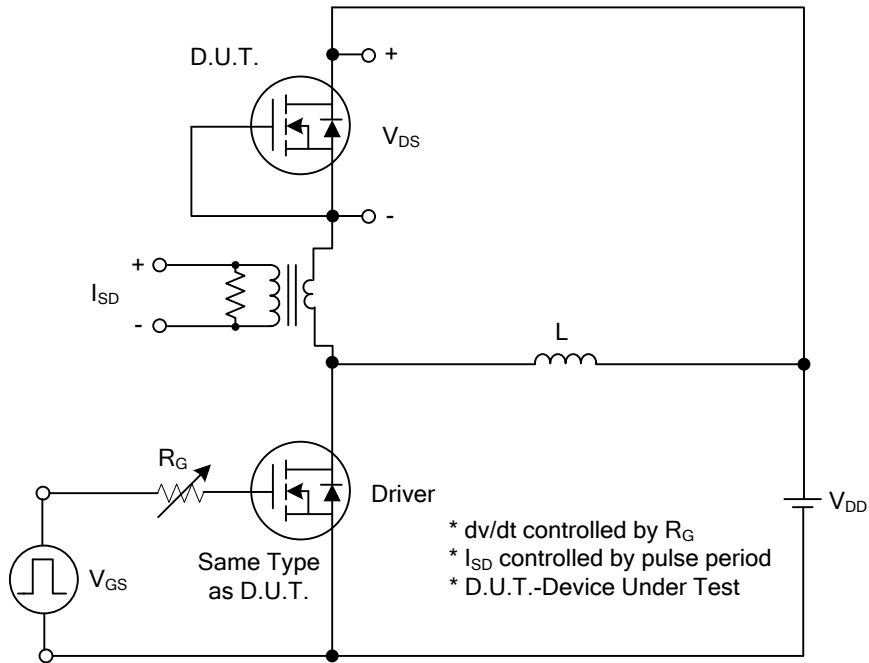
■ **ELECTRICAL CHARACTERISTICS** ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=1\text{mA}$, $V_{GS}=0\text{V}$	700			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=700\text{V}$, $V_{GS}=0\text{V}$			10	μA
Gate- Source Leakage Current	Forward	$V_{GS}=+30\text{V}$			+100	nA
	Reverse	$V_{GS}=-30\text{V}$			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	2.5		4.5	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10\text{V}$, $I_D=4.5\text{A}$			0.7	Ω
		$V_{GS}=10\text{V}$, $I_D=4.5\text{A}$, $T_J=100^\circ\text{C}$			1.3	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{GS}=0\text{V}$, $V_{DS}=25\text{V}$, $f=1.0\text{MHz}$		620		pF
Output Capacitance	C_{OSS}			240		pF
Reverse Transfer Capacitance	C_{RSS}			19		pF
SWITCHING PARAMETERS						
Total Gate Charge (Note 1)	Q_G	$V_{DS}=560\text{V}$, $V_{GS}=10\text{V}$, $I_D=9\text{A}$, $I_G=100\mu\text{A}$ (Note 1, 2)		23		nC
Gate to Source Charge	Q_{GS}			8		nC
Gate to Drain Charge	Q_{GD}			6		nC
Turn-ON Delay Time (Note 1)	$t_{D(ON)}$	$V_{DD}=100\text{V}$, $V_{GS}=10\text{V}$, $I_D=9\text{A}$, $R_G=25\Omega$ (Note 1, 2)		10		ns
Rise Time	t_R			19		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			62		ns
Fall-Time	t_F			42		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				9	A
Maximum Body-Diode Pulsed Current	I_{SM}				18	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$I_S=9\text{A}$, $V_{GS}=0\text{V}$			1.4	V
Body Diode Reverse Recovery Time (Note 1)	t_{rr}	$I_S=9\text{A}$, $V_{GS}=0\text{V}$,		350		ns
Body Diode Reverse Recovery Charge	Q_{rr}	$di_F/dt=100\text{A}/\mu\text{s}$		4.1		μC

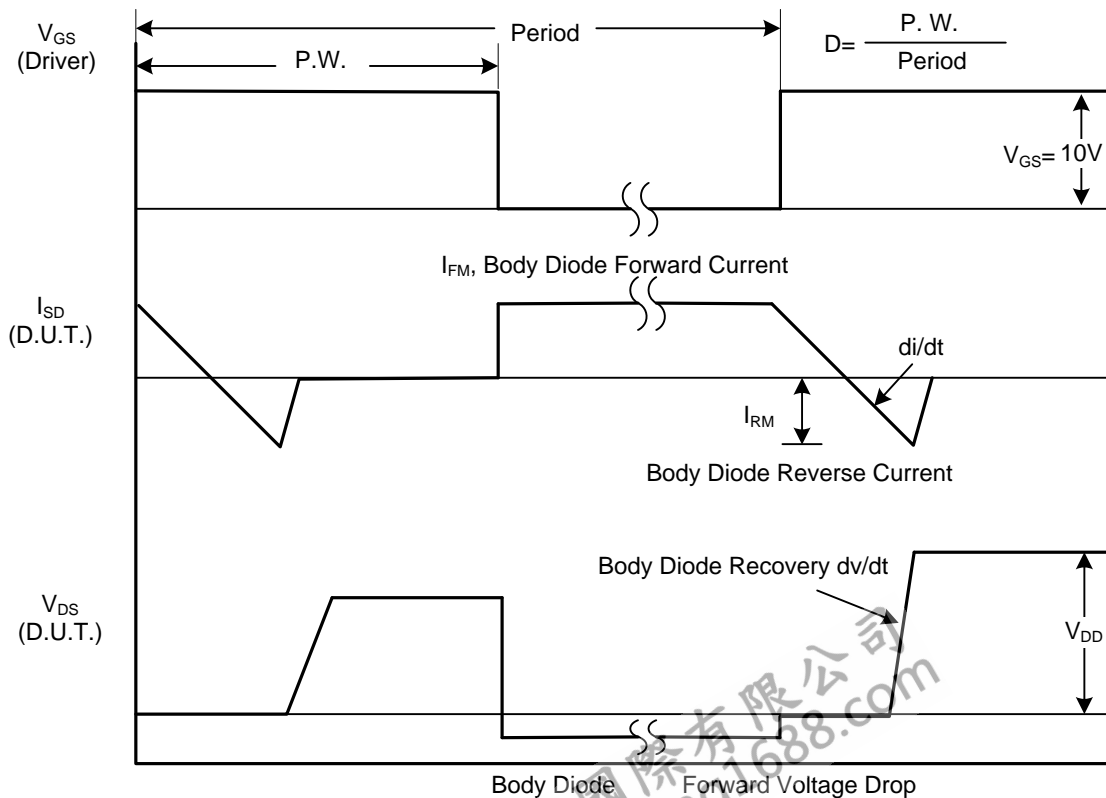
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

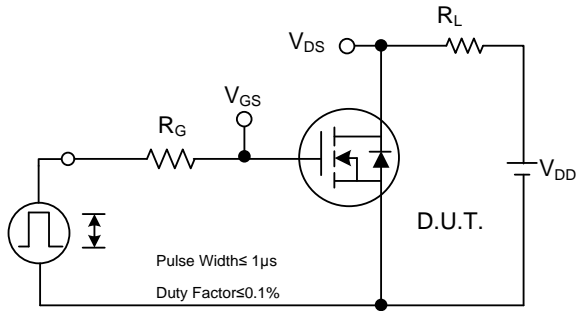


Peak Diode Recovery dv/dt Test Circuit

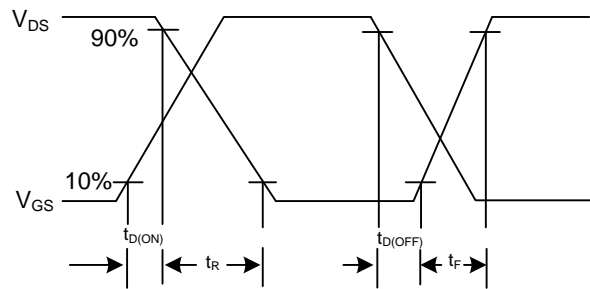


Peak Diode Recovery dv/dt Waveforms

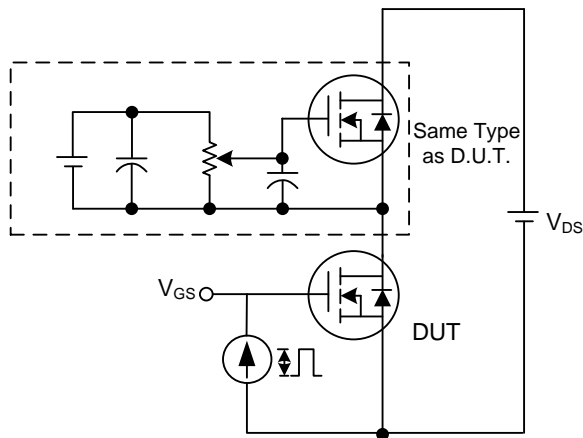
TEST CIRCUITS AND WAVEFORMS



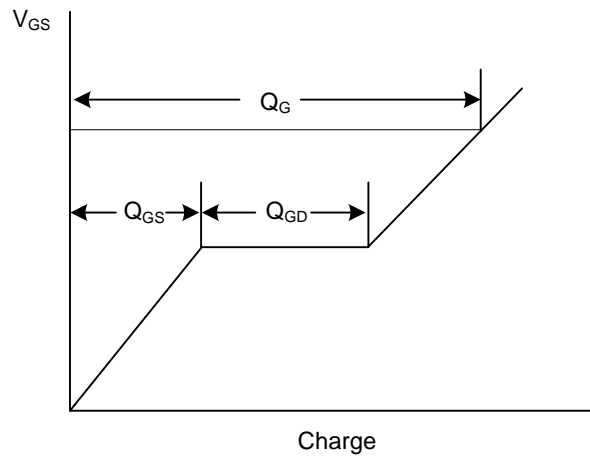
Switching Test Circuit



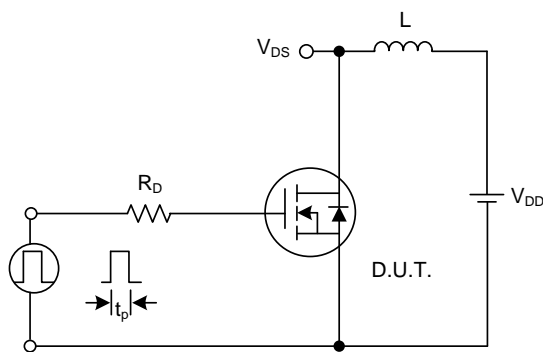
Switching Waveforms



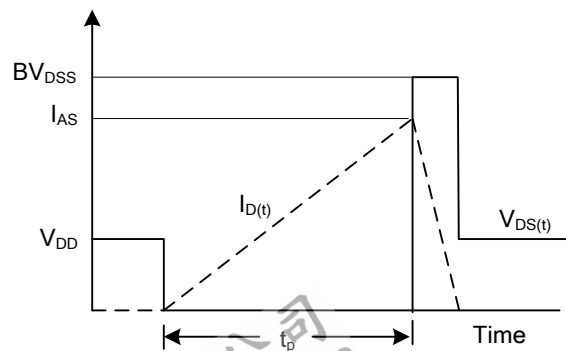
Gate Charge Test Circuit



Gate Charge Waveform

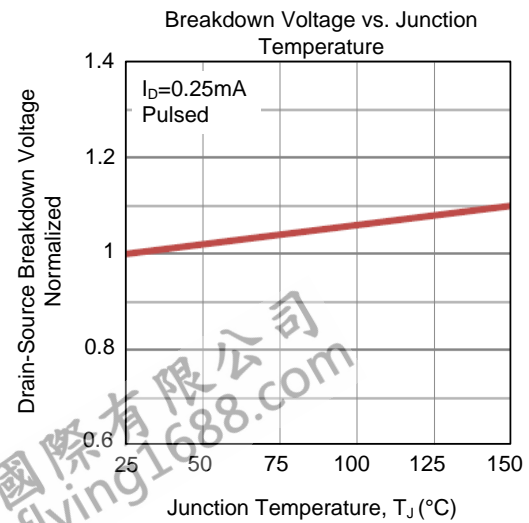
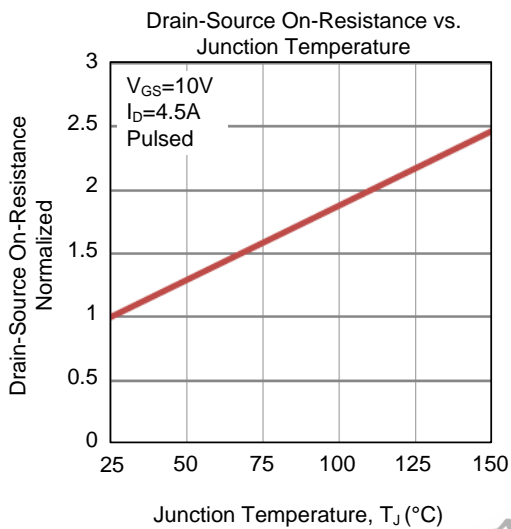
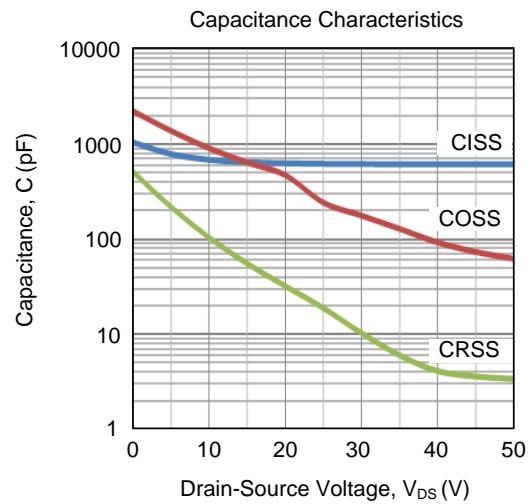
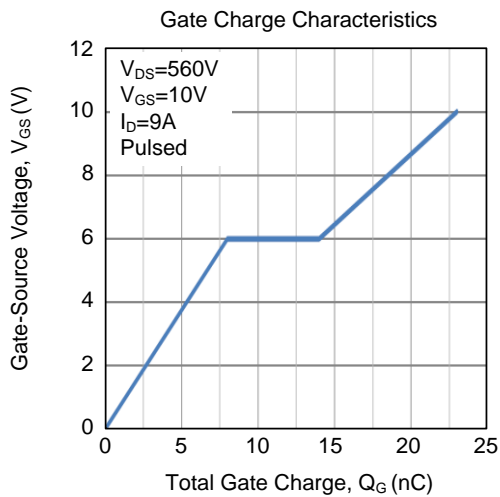
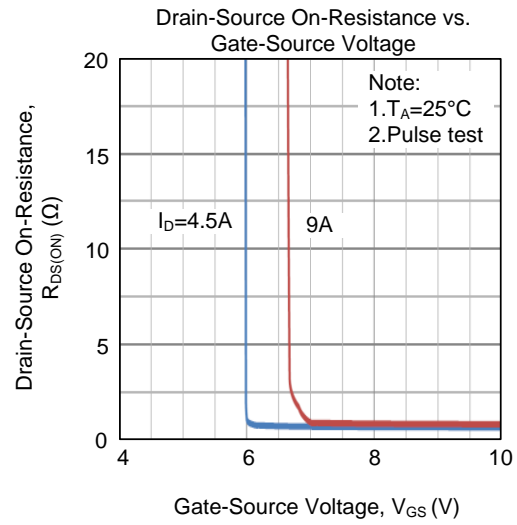
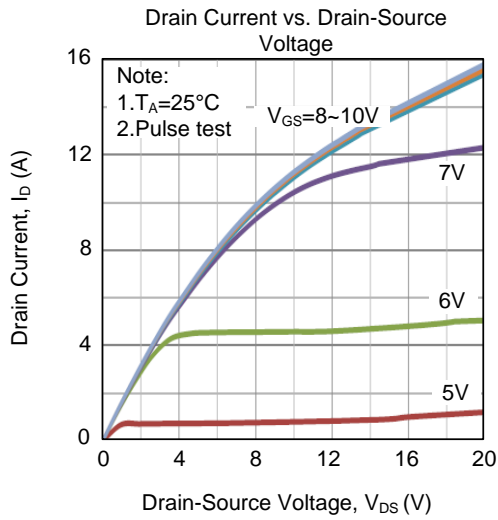


Unclamped Inductive Switching Test Circuit

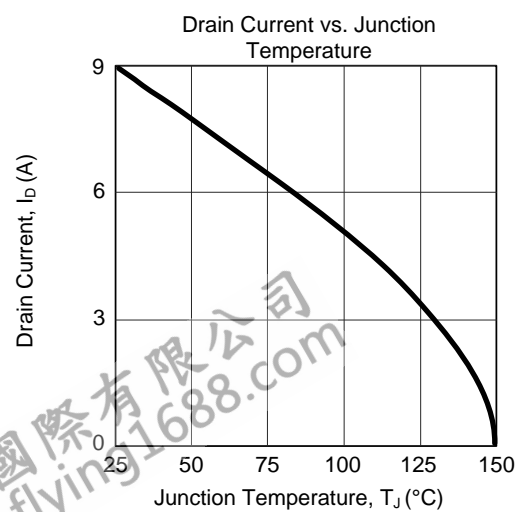
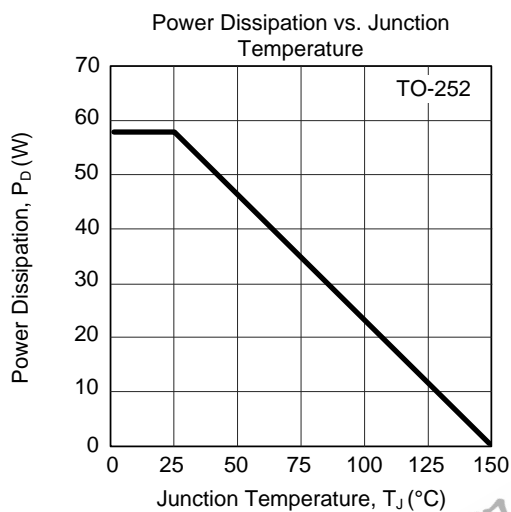
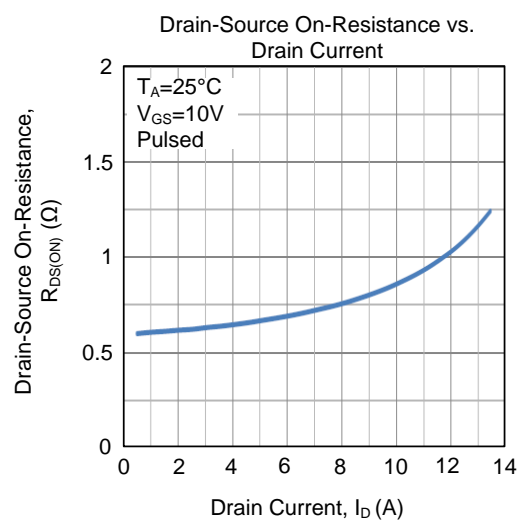
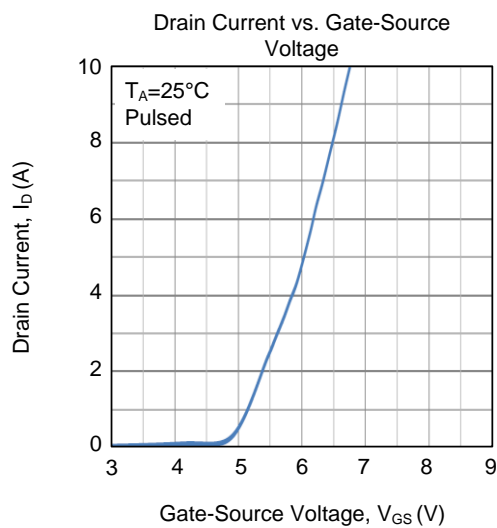
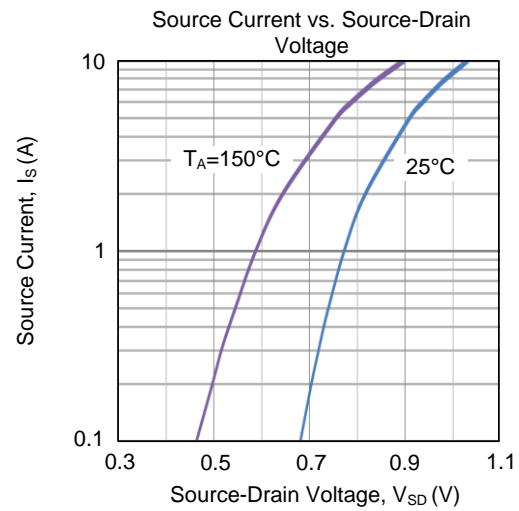
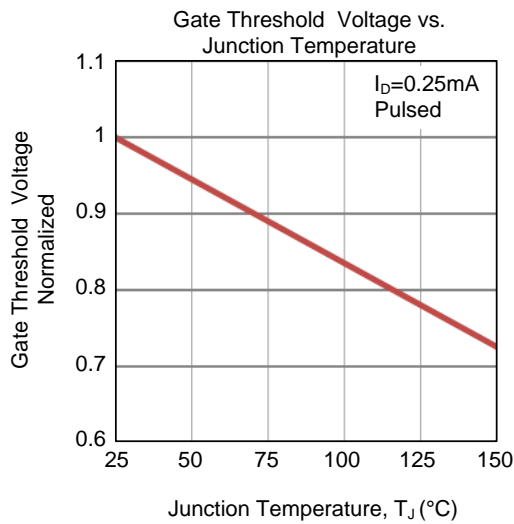


Unclamped Inductive Switching Waveforms

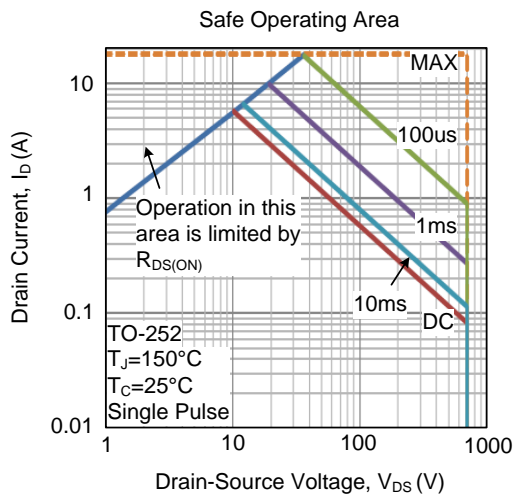
TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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