

UTC UNISONIC TECHNOLOGIES CO., LTD

BTA310A Preliminary TRIAC

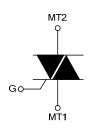
10A TRIACS

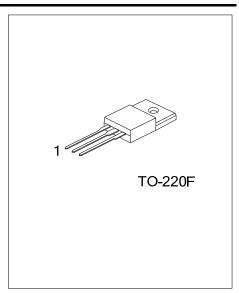
DESCRIPTION

The UTC BTA310A is a 10A triacs which can be operated in 3 quadrants only, it uses UTC's advanced technology to provide customers with high commutation performances, etc.

The UTC BTA310A is suitable for inductive load switching operations, also can be used in ON/OFF function applications such as induction motor starting circuits, heating regulation, static relays etc.

SYMBOL

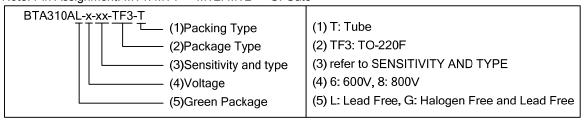




ORDERING INFORMATION

Ordering Number Lead Free Halogen Free		Packago	Pin /	Assignn	Packing	
		Package	1	2	3	racking
BTA310AL-x-xx-TF3-T BTA310AG-x-xx-TF3-T		TO-220F	MT1	MT2	G	Tube

Note: Pin Assignment: MT1: MT1 MT2: MT2 G: Gate

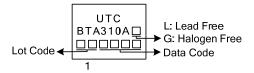


SENSITIVITY AND TYPE

	VOL	TAGE	SENSITIVITY	TYPE		
PART NUMBER 600V 800V		SENSITIVITY	ITPE			
BW	0	0	50mA	SNUBBERLESS		
CW	0	0	35mA	SNUBBERLESS		

: Available

MARKING



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■ ABSOLUTE MAXIMUM RATINGS

PARAMETER			SYMBOL	RATINGS	UNIT
RMS On-State Current (Full Sine Wave)	T _C =95°C		I _{T(RMS)}	10	Α
in the product of the grant of the country	F=50Hz	t=20ms	I _{TSM}	100	Α
Current (Full Cycle T _J initial=25°C)	F=60Hz	t=16.7ms	- 1 3 W	105	Α
I ² t Value for Fusing	t _P =10ms		l ² t	55	A^2s
Critical Rate of Rise of On-State Current: I _G =2xI _{GT} , tr≤100ns	F=120Hz	T _J =125°C	dl/dt	50	A/µs
Non Repetitive Surge Peak Off-State Voltage	t _P =10ms T _J =25°0		V_{DSM}/V_{RSM}	V _{DSM} /V _{RSM} +100	>
Peak Gate Current	t _P =20µs	T _J =125°C	I_{GM}	4	Α
Average Gate Power Dissipation T _J =125			$P_{G(AV)}$	1	W
Operating Junction Temperature			T_J	-40~+125	°C
Storage Junction Temperature			T_{STG}	-40~+150	Ŝ

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL RESISTANCES

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	60	°C/W
Junction to Case (AC)	θ _{JC}	2.4	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J= 25°C, unless otherwise specified)

FOR SNUBBERLESS (3 QUADRANTS)

DADAMETED	0)/4/00/	TEST CONDITIONS		CW			BW			LINUT
PARAMETER	SYMBOL			MIN	TYP	MAX	MIN	TYP	MAX	UNIT
Gate Trigger Current (Note 1)	I _{GT}	V _D =12V,	1-11-111			35			50	mA
Gate Trigger Voltage	V_{GT}	$R_L=33\Omega$	1-11-111			1.3			1.3	V
Gate Non-Trigger Voltage	V_{GD}	$V_D=V_{DRM}$, $R_L=3.3k\Omega$, $T_J=125^{\circ}C$	I-II-III	0.2			0.2			٧
Holding Current (Note 2)	Ι _Η	I _T =500mA				35			50	mA
Latching Current	ΙL	I _G =1.2I _{GT}	1-111			50			70	mA
			П			60			80	mA
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V_D =67% V_{DRM} , Gate Open, T_J =125°C		500			1000			V/µs
Critical Rate of Rise of Off-State Voltage at Commutation (Note 2)	(dl/dt)c	Without Snubber, T _J =125°C		5.5			9.0			A/ms

Notes: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

■ STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Peak On-State Voltage (Note 2)	V_{T}	I _{TM} =14A, t _P =380μs	T _J =25°C			1.55	V
Threshold Voltage (Note 2)	V_{TO}		T _J =125°C			0.85	V
Dynamic Resistance (Note 2)	R_D		T _J =125°C			40	mΩ
Repetitive Peak Off-State Current	I _{DRM}	\/ -\/	T _J =25°C			5	μΑ
	I _{RRM}	V _{DRM} =V _{RRM}	T _J =125°C			1	mA

Notes: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

2. For both polarities of MT2 referenced to MT1.

^{2.} For both polarities of MT2 referenced to MT1.

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