# UTC UNISONIC TECHNOLOGIES CO., LTD

**BTA316A TRIAC** 

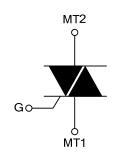
# **16A TRIACS**

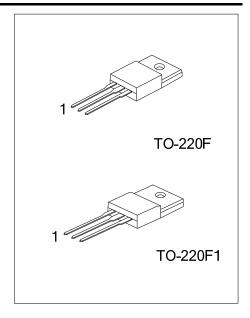
#### **DESCRIPTION**

The UTC BTA316A is a 16A triacs which can be operated in 3 quadrants only, it uses UTC's advanced technology to provide customers with high commutation performances, etc.

The UTC BTA316A is suitable for inductive load switching operations, also can be used in ON/OFF function applications such as induction motor starting circuits, heating regulation, static relays etc.

#### **SYMBOL**

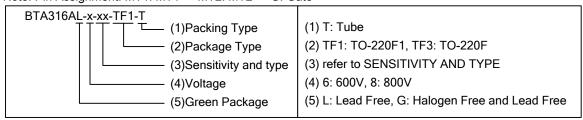




#### ORDERING INFORMATION

Ordering	Dookogo	Pin	Assignn	Doolsing			
Lead Free Halogen Free		Package	1	2	3	Packing	
BTA316AL-x-xx-TF1-T BTA316AG-x-xx-TF1-T		TO-220F1	MT1	MT2	G	Tube	
BTA316AL-x-xx-TF3-T BTA316AG-x-xx-TF3-T		TO-220F	MT1	MT2	G	Tube	

Note: Pin Assignment: MT1: MT1 MT2: MT2 G: Gate

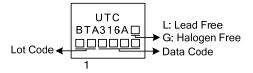


#### **SENSITIVITY AND TYPE**

	VOL.	TAGE	SENSITIVITY	TYPE			
PART NUMBER	600V	800V	SENSITIVITY				
BW	0	0	50mA	SNUBBERLESS			
CW	0	0	35mA	SNUBBERLESS			
SW	0 0		10mA	LOGIC LEVEL			

#### : Available

#### **MARKING**



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## ■ ABSOLUTE MAXIMUM RATINGS

PARAME	TER		SYMBOL	RATINGS	UNIT	
RMS On-State Current (Full Sine Wave) T <sub>C</sub> =86°C			I <sub>T(RMS)</sub>	16	Α	
Non Repetitive Surge Peak On-State Current (Full	F=50 Hz	t=20ms	I <sub>TSM</sub>	160	Α	
Cycle, T <sub>J</sub> initial=25°C)	F=60 Hz	t=16.7ms	TISM	168	Α	
$I^2$ t Value for Fusing $t_P$ =10ms			l <sup>2</sup> t	144	$A^2s$	
Critical Rate of Rise of On-State Current I <sub>G</sub> =2xI <sub>GT</sub> , tr≤100ns	F=120 Hz	T <sub>J</sub> =125°C	dI/dt	50	A/μs	
Non Repetitive Surge Peak Off-State Voltage	t <sub>P</sub> =10ms	T <sub>J</sub> =25°C	$V_{DSM}/V_{RSM}$	V <sub>DRM</sub> /V <sub>RRM</sub> +100	V	
Peak Gate Current	t <sub>P</sub> =20µs	T <sub>J</sub> =125°C	$I_{GM}$	4	Α	
Average Gate Power Dissipation T <sub>J</sub> =125°		T <sub>J</sub> =125°C	$P_{G(AV)}$	1	W	
Operating Junction Temperature			$T_J$	-40~+125	°C	
Storage Junction Temperature			$T_{STG}$	-40~+150	°C	

Note Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

## **■ THERMAL RESISTANCES**

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	$\theta_{JA}$	60	°C/W	
Junction to Case (AC)	$\theta_{JC}$	2.1	°C/W	

## ■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub> =25°C unless otherwise specified.)

# FOR SNUBBERLESS TYPE and LOGIC LEVEL TYPE (3 QUADRANTS)

DADAMETED	SYMBOL -	TEST CONDITIONS			SW		CW			BW			UNIT
PARAMETER	SYMBOL	TEST CONDIT	TEST CONDITIONS		TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	OINIT
Gate Trigger Current (Note 1)	I <sub>GT</sub>	V <sub>D</sub> =12V, R <sub>L</sub> =33Ω	1-11-111			10			35			50	mA
Gate Trigger Voltage	$V_{GT}$		1-11-111			1.3			1.3			1.3	V
Gate Non-Trigger Voltage	$V_{GD}$	$V_D = V_{DRM}$ , $R_L = 3.3k\Omega$ , $T_J = 125^{\circ}C$	1-11-111	0.2			0.2			0.2			٧
Holding Current (Note 2)	I <sub>H</sub>	I <sub>T</sub> =500mA				15			35			50	mA
Latching Current	ΙL	I <sub>G</sub> =1.2I <sub>GT</sub>	I-III			25			50			70	mA
Latering Current	ıĽ		II			30			60			80	mΑ
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V <sub>D</sub> =67%V <sub>DRM</sub> , Gate Open, T <sub>J</sub> =125°C		40			500			1000			V/µs
Critical Rate of Rise		(dV/dt)c=0.1V/μs, T <sub>J</sub> =125°C		8.5									A/ms
of Off-State Voltage at Commutation	(dl/dt)c	(dV/dt)c=10V/μs, Τ <sub>J</sub> =125°C		3.0									A/ms
(Note 2)		Without Snubber T <sub>J</sub> =125°C					8.5			14			A/ms

Notes: 1. Minimum  $I_{GT}$  is guaranteed at 5% of  $I_{GT}$  max.

2. For both polarities of MT2 referenced to MT1.

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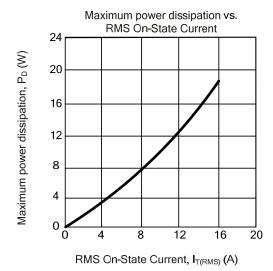
# **■ STATIC CHARACTERISTICS**

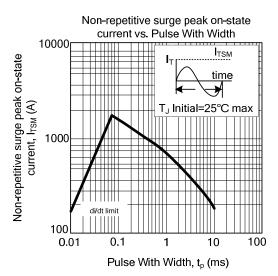
PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Peak On-State Voltage(Note 2)	$V_{TM}$	$I_{TM}$ =22.5A, $t_p$ =380 $\mu$ s	T <sub>J</sub> =25°C			1.55	V
Threshold Voltage(Note 2)	$V_{TO}$		T <sub>J</sub> =125°C			0.85	٧
Dynamic Resistance(Note 2)	$R_D$		T <sub>J</sub> =125°C			25	mΩ
Denetities Deals Off State Comment	I <sub>DRM</sub>	\/ -\/	T <sub>J</sub> =25°C			5	μA
Repetitive Peak Off-State Current	I <sub>RRM</sub>	$V_{DRM}=V_{RRM}$	T <sub>J</sub> =125°C			2	mA

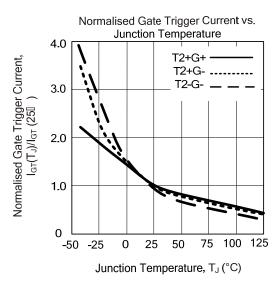
Notes: 1. Minimum  $I_{\text{GT}}$  is guaranteed at 5% of  $I_{\text{GT}}$  max.

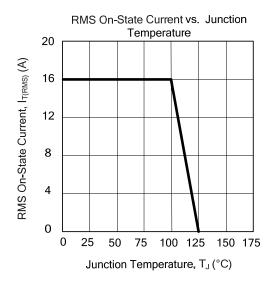
<sup>2.</sup> For both polarities of MT2 referenced to MT1.

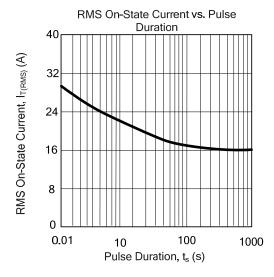
## **■ TYPICAL CHARACTERISTICS**

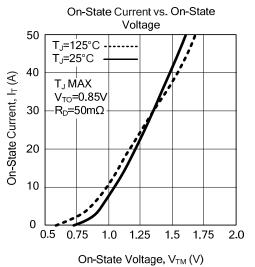






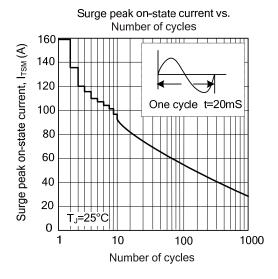






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# ■ TYPICAL CHARACTERISTICS (Cont.)



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