

# UNISONIC TECHNOLOGIES CO., LTD

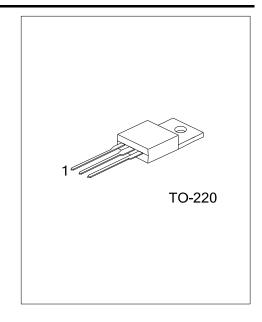
BTB324A Preliminary TRIAC

# **25A TRIACS**

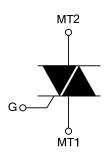
#### DESCRIPTION

The UTC **BTB324A** is a 25A triacs which can be operated in 3 quadrants, it uses UTC's advanced technology to provide customers with high commutation performances.

The UTC **BTB324A** is suitable for inductive load switching operations, also can be used in ON/OFF function applications such as induction motor starting circuits, heating regulation, static relays etc.



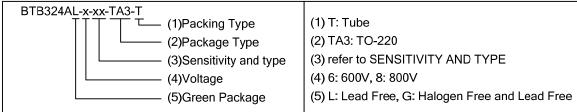
#### SYMBOL



# ■ ORDERING INFORMATION

Ordering	Dookogo	Pin	Assignn	Doolsing		
Lead Free	Halogen Free	Package	1	2	3	Packing
BTB324AL-x-xx-TA3-T	BTB324AG-x-xx-TA3-T	TO-220	MT1	MT2	G	Tube

Note: Pin Assignment: MT1: MT1 MT2: MT2 G: Gate

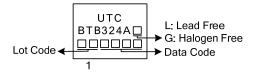


#### **■ SENSITIVITY AND TYPE**

	VOL1	ΓAGE	SENSITIVITY	TYPE		
PART NUMBER	PART NUMBER 600V 800V SE		SENSITIVITY	ITPE		
BW	0	0	50mA	SNUBBERLESS		
CW	0	0	35mA	SNUBBERLESS		

#### O: Available

#### **■** MARKING



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# **■ ABSOLUTE MAXIMUM RATINGS**

PARAMETER			SYMBOL	RATINGS	UNIT
RMS On-State Current (Full	IS On-State Current (Full Sine Wave) T <sub>C</sub> =75°C		I <sub>T(RMS)</sub>	25	Α
Non Repetitive Surge Peak On-State Current (Full	F=50 Hz	t=20ms	l	250	Α
Cycle, T <sub>J</sub> initial=25°C)	F=60 Hz	t=16.7ms	I <sub>TSM</sub>	260	Α
I <sup>2</sup> t Value for Fusing	t <sub>P</sub> =10ms		l <sup>2</sup> t	340	$A^2s$
Critical Rate of Rise of On-State Current I <sub>G</sub> =2xI <sub>GT</sub> , tr≤100ns	F=120 Hz	T <sub>J</sub> =125°C	dl/dt	50	A/μs
Non Repetitive Surge Peak Off-State Voltage	t <sub>P</sub> =10ms	T <sub>J</sub> =25°C	V <sub>DSM</sub> /V <sub>RSM</sub>	V <sub>DRM</sub> /V <sub>RRM</sub> +100	V
Peak Gate Current	t <sub>P</sub> =20µs	T <sub>J</sub> =125°C	$I_{GM}$	4	Α
Average Gate Power Dissipa	ation	T <sub>J</sub> =125°C	$P_{G(AV)}$	1	W
Operating Junction Temperature		$T_J$	-40~+125	°C	
Storage Junction Temperature		T <sub>STG</sub>	-40~+150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

## **■ THERMAL RESISTANCES**

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	60	°C/W
Junction to Case (AC)	$\theta_{JC}$	0.8	°C/W

## ■ **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> =25°C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS -		CW			BW			UNIT
PARAMETER	STIVIDOL			MIN	TYP	MAX	MIN	TYP	MAX	UNIT
SNUBBERLESS TYPE (3 QUADRANTS)										
Gate Trigger Current (Note 1)	$I_{\mathrm{GT}}$	$V_D=12V$ ,	1-11-111			35			50	mA
Gate Trigger Voltage	$V_{GT}$	$R_L=33\Omega$	1-11-111			1.3			1.3	V
Gate Non-Trigger Voltage	$V_{GD}$	$V_D=V_{DRM}$ , $R_L=3.3k\Omega$ , $T_J=125^{\circ}C$	1-11-111	0.2			0.2			V
Holding Current (Note 2)	l <sub>Η</sub>	I <sub>T</sub> =500mA				50			75	mA
Latabia a Commant	IL IG	I <sub>G</sub> =1.2I <sub>GT</sub>	1-111			70			80	mA
Latching Current		IG-1.2IGT	II			80			100	mA
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V <sub>D</sub> =67%V <sub>DRM</sub> , Gate Open, T <sub>J</sub> =125°C		500			1000			V/µs
Critical Rate of Rise of Off-State Voltage at Commutation (Note 2)	(dl/dt)c	Without Snubber, T <sub>J</sub> =125°C		13			22			A/ms

# **■ STATIC CHARACTERISTICS**

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Peak On-State Voltage (Note 2)	$V_{TM}$	I <sub>TM</sub> =35A, t <sub>P</sub> =380μs	TJ=25°C			1.55	V
Threshold Voltage (Note 2)	$V_{TO}$		TJ=125°C			0.85	٧
Dynamic Resistance (Note 2)	$R_D$		TJ=125°C			16	mΩ
Repetitive Peak Off-State	I <sub>DRM</sub>	\\\ -\\\	TJ=25°C			5	μΑ
Current	I <sub>RRM</sub>	$V_{DRM}=V_{RRM}$	TJ=125°C			3	mA

Notes: 1. Minimum  $I_{\text{GT}}$  is guaranteed at 5% of  $I_{\text{GT}}$  max.

2. For both polarities of MT2 referenced to MT1.

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