

UNISONIC TECHNOLOGIES CO., LTD

BTB41 Preliminary TRIAC

40A STANDARD TRIAC

DESCRIPTION

The UTC BTB41 is a 40A standard triac, it uses UTC's advanced technology to provide customers with low thermal resistance with clip bonding and high commutation capability, etc.

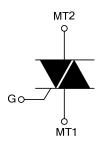
The UTC BTB41 is suitable for general purpose AC switching, heating regulation and on/off function in static relays, etc.

FEATURES

- * Low thermal resistance with clip bonding
- * High current capability
- * High commutation capability

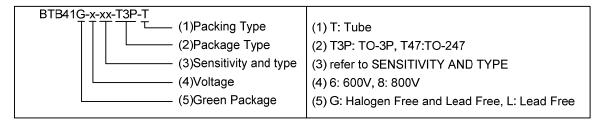
TO-247 TO-3P

SYMBOL



ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Doolsing	
Lead Free Halogen Free		Package	1	2	3	Packing	
BTB41L-x-xx-T3P-T BTB41G-x-xx-T3P-T		TO-3P	MT1	MT2	G	Tube	
BTB41L-x-xx-T47-T	BTB41G-x-xx-T47-T	TO-247	MT1	MT2	G	Tube	

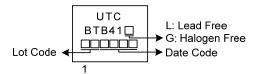


SENSITIVITY AND TYPE

PART NUMBER	VOL1	TAGE	SENSITIVITY	TYPE
	600V	800V	SENSITIVITY	ITPE
В	0	0	50mA	STANDARD

①: Available

■ MARKING



■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT	
On-State RMS Current (Full Sine Wave)	T _C =95°C	I _{T(RMS)}	40	Α
Non Repetitive Surge Peak On-State	F=50Hz, t=20ms	1	400	Α
Current (Full Cycle, T _J initial=25°C)	F=60Hz, t=16.7ms	I _{TSM}	420	Α
I ² t Value for Fusing	t _p =10ms	l ² t	1000	A^2s
Critical Rate of Rise of On-State Current: I _G =2xI _{GT} , t _r ≤100ns	F=120Hz, T _J =125°C	dl/dt	50	A/µs
Non Repetitive Surge Peak Off-State Voltage	t _p =10ms, T _J =25°C	V _{DSM} /V _{RSM}	V _{DSM} /V _{RSM} +100	٧
Peak Gate Current	t _p =20μs, Τ _J =125°C	I_{GM}	8	Α
Average Gate Power Dissipation	T _J =125°C	P _{G(AV)}	1	W
Storage Junction Temperature		T _{STG}	-40~+150	°C
Operating Junction Temperature	TJ	-40~+125	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ DEVICE SUMMARY

PARAMETER	SYMBOL	RATINGS	UNIT
On-State RMS Current	I _{T(RMS)}	40	Α
Repetitive Peak Off-State Voltage	V_{DRM}/V_{RRM}	600	٧
Triggering Gate Current	I _{GT}	50	mA

■ THERMAL RESISTANCES

PARAMETER		RATINGS	UNIT
Junction to Ambient	θ_{JA}	50	°C/W
Junction to Case (AC)	θ_{JC}	0.6	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J=25 °C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Gate Trigger Current (Note 1)	I _{GT}		1-11-111			50	mA
		V_D =12V, R_L =33 Ω	IV			100	mA
Gate Trigger Voltage	V_{GT}		ALL			1.3	V
Gate Non-Trigger Voltage	V_{GD}	$V_D=V_{DRM}, R_L=3.3k\Omega,$ $T_J=125^{\circ}C$	ALL	0.2			V
Holding Current (Note 2)	lΗ	I _T =500mA				80	mA
Latching Current	I _L I _G =1.2I _{GT}	1 -1 21	I-III-IV			70	mA
		IG-1.2IGT	II			160	mA
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V _D =67%V _{DRM} , Gate Open, T _J =125°C		500			V/µs
Critical Rate of Rise of Off-State Voltage at Commutation (Note 2)	(dV/dt)c	(dl/dt)c=20A/ms, T _J =125°C		10			V/µs

■ STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Peak On-State Voltage (Note 1)	V_{T}	I_{TM} =60A, t_p =380 μ s, T_J =25 $^{\circ}$ C			1.55	V
Threshold Voltage (Note 2)	V_{TO}	T _J =125°C			0.85	V
Dynamic Resistance (Note 2)	R_D	T _J =125°C			10	mΩ
Repetitive Peak Off-State Current	I_{DRM}	V _{DRM} =V _{RRM} , T _J =25°C			5	μΑ
	I_{RRM}	V _{DRM} =V _{RRM} , T _J =125°C			5	mA

Notes: 1. Minimum I_{GT} is guaranted at 5% of I_{GT} max.

2. For both polarities of MT2 referenced to MT1.

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