

DTA113T

PNP SILICON TRANSISTOR

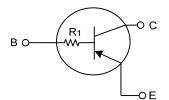
PNP DIGITAL TRANSISTOR (BUILT- IN BIAS RESISTORS)

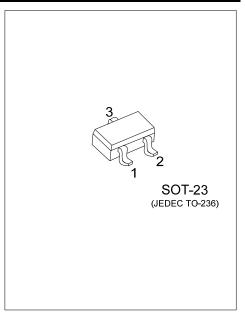
FEATURES

* Built-in bias resistors that implies easy ON/OFF applications.

* The bias resistors are thin-film resistors with complete isolation to allow positive input.

EQUIVALENT CIRCUIT



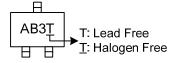


ORDERING INFORMATION

	Ordering Number		Dookogo	Pin Assignment			Dooking	
	Lead Free	Halogen Free	Package	1	2	3	Packing	
	DTA113TL-AE3-R	DTA113TG-AE3-R	SOT-23	В	Е	С	Tape Reel	
N	Note: Pin assignment: B: Base E: Emitter C: Collector							

(2)Package Type (2)	1) R: Tape Reel 2) AE3: SOT-23 3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING



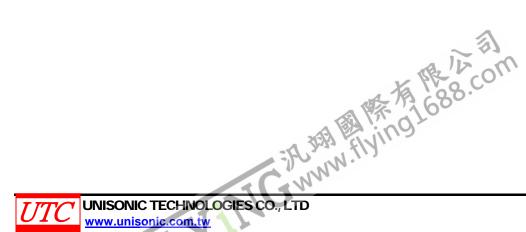
■ **ABSOLUTE MAXIMUM RATINGS** (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT	
Collector-Base Voltage	V _{CBO}	-50	V	
Collector-Emitter Voltage	V _{CEO}	-50	V	
Emitter-Base Voltage	V _{EBO}	-6	V	
Collector Current	Ι _C	-100	mA	
Peak Collector Current	I _{CM}	-200	mA	
Collector Power Dissipation	Pc	150	mW	
Junction Temperature	TJ	+150	°C	
Storage Temperature	T _{STG}	-55 ~ +150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (T_A=25°C, unless otherwise specified)

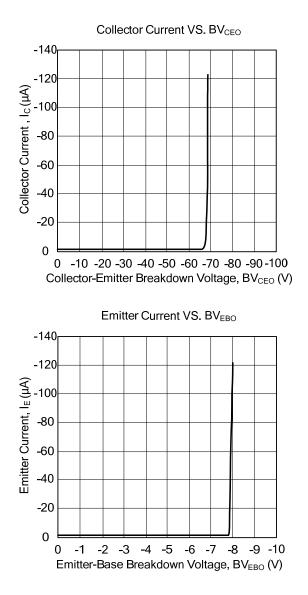
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Emitter Breakdown Voltage	BV _{CEO}	I _C =-100μΑ, R _{BE} =∞	-50			V
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	I _C =-10mA, I _B =-0.5mA			-0.3	V
Collector Cut-off Current	I _{CBO}	V _{CB} =-50V, I _E =0			-0.1	μA
DC Current Gain	h _{FE}	V _{CE} =-5V, I _C =-1mA	100			
Input Resistance	R _{IN}		0.7	1.0	1.3	kΩ
Current Gain Bandwidth Product	f⊤	V _{CE} =-6V, I _E =10mA		150		MHz

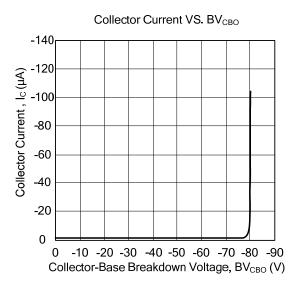


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TYPICAL CHARACTERISTICS





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