UTC UNISONIC TECHNOLOGIES CO., LTD

DTA115T

PNP SILICON TRANSISTOR

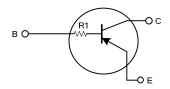
DIGITAL TRANSISTORS (BUILT- IN BIAS RESISTORS)

FEATURES

- * Built-in bias resistors that implies easy ON/OFF applications.
- * The bias resistors are thin-film resistors with complete isolation to allow positive input.

SOT-23 (JEDEC TO-236)

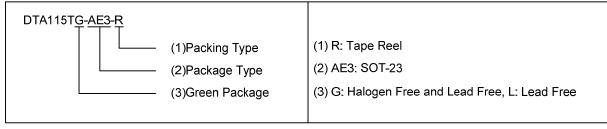
EQUIVALENT CIRCUIT



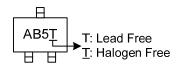
ORDERING INFORMATION

Ordering Number		Daakaga	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
DTA115TL-AE3-R	DTA115TG-AE3-R	SOT-23	В	Е	С	Tape Reel	

Note: Pin Assignment: B: Base E: Emitter C: Collector



MARKING



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■ **ABSOLUTE MAXIMUM RATINGS** (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATING	UNIT
Collector-Base Voltage	V_{CBO}	-50	٧
Collector-Emitter Voltage	$V_{\sf CEO}$	-50	V
Emitter-Base Voltage	V_{EBO}	-5	V
Collector Current	Ic	-100	mA
Collector Power Dissipation	Pc	200	mW
Junction Temperature	T _J		Ç
Storage Temperature	T _{STG}	-55 ~ + 150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ **ELECTRICAL SPECIFICATIONS** (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	BV _{CBO}	I _C =-50μA	-50			V
Collector-Emitter Breakdown Voltage	BV _{CEO}	I _C =-1mA	-50			V
Emitter-Base Breakdown Voltage	BV _{EBO}	I _E =-50μA	-5			V
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	I _C =-1mA, I _B =-0.1mA			-0.3	V
Collector Cutoff Current	I _{CBO}	V _{CB} =-50V			-0.5	μΑ
Emitter Cutoff Current	I _{EBO}	V _{EB} =-4V			-0.5	μΑ
DC Current Gain	h _{FE}	V_{CE} =-5V, I_{C} =-1mA	100	250	600	
Input Resistance	R ₁		70	100	130	kΩ
Transition Frequency	f _T	V _{CE} =-10V, I _E =5mA,f=100MHz (Note)		250		MHz

Note: Transition frequency of the device



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