

## **DTA144T**

## **PNP SILICON TRANSISITOR**

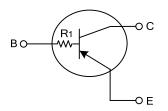
## PNP DIGITAL TRANSISTOR (BUILT-IN RESISTOR)

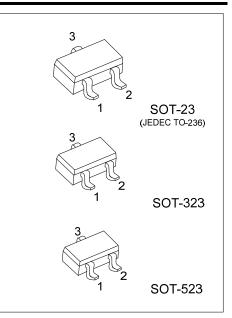
#### **FEATURES**

\* Built-in bias resistors that implies easy ON/OFF applications.

\* The bias resistors are thin-film resistors with complete isolation to allow positive input.

#### EQUIVALENT CIRCUIT





#### **ORDERING INFORMATION**

Ordering Number		Daakaga	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
DTA144TL-AE3-R	DTA144TG-AE3-R	SOT-23	В	E	С	Tape Reel	
DTA144TL-AL3-R	DTA144TG-AL3-R	SOT-323	В	Е	С	Tape Reel	
DTA144TL-AN3-R	DTA144TG-AN3-R	SOT-523	В	E	С	Tape Reel	
Note: Pin Assignment: B: Base E: Emitter C: Collector							

DTA144TG-AE3-R		
	acking Type	(1) R: Tape Reel
(2)Pa	ackage Type	(2) AE3: SOT-23, AL3: SOT-323, AN3: SOT-523
(3)G	reen Package	(3) G: Halogen Free and Lead Free, L: Lead Free

### MARKING



#### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> =25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Base Voltage	V <sub>CBO</sub>	-50	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-50	V
Emitter-Base Voltage	V <sub>EBO</sub>	-5	V
Collector Current	lc	-100	mA
Collector Power Dissipation	Pc	200	mW
Junction Temperature	TJ	+150	°C
Storage Temperature	T <sub>STG</sub>	-55 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub> =25°C, unless otherwise specified)

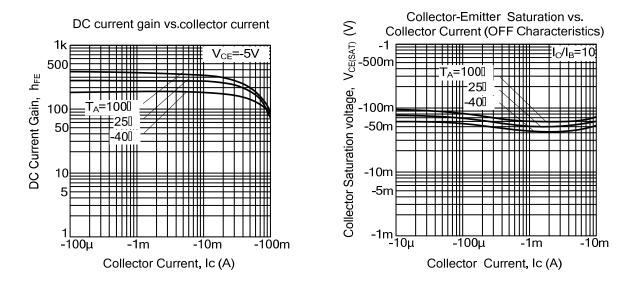
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	<b>BV</b> <sub>CBO</sub>	I <sub>C</sub> =-50μΑ	-50			V
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	I <sub>C</sub> =-1mA	-50			V
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	I <sub>E</sub> =-50μA	-5			V
Collector Cutoff Current	I <sub>CBO</sub>	V <sub>CB</sub> =-50V			-0.5	μA
Emitter Cutoff Current	I <sub>EBO</sub>	V <sub>EB</sub> =-4V			-0.5	μA
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> =-5mA, I <sub>B</sub> = -0.5mA			-0.3	V
DC Current Transfer Ratio	h <sub>FE</sub>	V <sub>CE</sub> =-5V, I <sub>C</sub> = -1mA	100	250	600	
Transition Frequency (Note)	f⊤	V <sub>CE</sub> =-10V, I <sub>E</sub> =5mA, f=100MHz		250		MHz
Input Resistance	R1		32.9	47	61.1	kΩ

Note: Transition frequency of the device.



# DTA144T

### TYPICAL CHARACTERISTICS



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